# Popular Electronics WORLD'S LARGEST-SELLING ELECTRONICS MAGAZINE NOVEMBER 1980/95¢ 

Preview: Panasonic's Four-Head Video Recorder Guide To Floppy-Disk Formats Computerized Tune Player With Expandable Memory

## PE EXCLLSSIVE: <br> Microprocessor Product Development Sustem




# ng and the Apple. 

If you could talk to Orville Wright, he'd tell you the problems he faced as a turn-of-the-century engineer. You could tell him all about the technological solutions available to today's engineer and scientist. particularly a 20 th century phenomenon that tests assumptions and defines models before a project gets off the ground. The Apple personal computer.

## Computation, calculation, analysis...the power to pilot your projects.

With a highly-integrated system from the extensive Apple personal computer family, Orville and brother Wilbur would have increased their productivity. Perhaps even launched the Kitty Hawk Flyer well before 1903.

An Apple in their hangar would have freed them from the time and tedium of crunching numbers by hand.

An Apple in your lab or office will give you the problem-solving capabilities you demand from a big computer... without the time-consuming problems typical of remote processing.

But the Apple system solution doesn't stop there. It keeps on soaring with proven performance, power and expandability


Apple's existing software library includes a program that plots the shape of an airfoil, given its parameters.
that's unparalleled for analyzing alternative paths of design and modeling a wide variety of physical processes.

Want more memory? Depending on your choice of system, Apple has memory expandable to 64 K bytes or 128 K bytes. Prefer wide displays? Choose 40 or 80 characters. Need to control instruments in the lab? Get on the IEEE 488 bus. Over

100 companies also supply peripherals for Apple because Apple is the most popular personal computer with the least complicated interface.

Want an efficient system of data storage and access? Apple's $51 / 4^{\prime \prime}$ disk drive not only offers you increased application versatility, but high density ( 143 K bytes), high speed and low cost. You can even add up to four or more drives to your Apple svstem. With proven reliability, no wonder it's the most popular drive on the market today.

Wilbur determined that birds didn"t have to constantly flap their wings to fly. With an Apple, be could've determined the fixed-wing design of the Kitty Hawk Flyer much faster.
 programs, the Apple also speaks in languages other than FORTRAN: Pascal, BASIC, PLLOT and 6502 assembly language.

## Where to learn more about Apple, the small-yetserious solution.

## FORTRAN that helped to design a 20th century flying machine.

Fluent in the same language that helped to design the 747, Apple FORTRAN lets you tackle differential equations at the touch of a key. And since more the 170 companies also offer software for the Apple family, you can have one of the most impressive program libraries ever...including vast subroutine libraries for math, science, engineering and statistics. When you write
parts selection. Learn why Apple emerges as the technological leader of reliable personal computer products that increase your productivity.

Let the Apple dealer show you how, by putting the system of your choice through its paces. He'll tell you about our extended warranty, support and service. And he'll prove that a personal computer is not just a flight of fancy but a serious solution. Don't let history pass you by. Visit your nearest Apple dealer, or call 800-538-9696. In California, 800-662-9238.

# -apple computer 

CIRCLE NO. 6 ON FREE INFORMATION CARD

# NOW CIEANING YOUR OWN DISKETTE HEADS COULD SAVE YOUA ${ }^{4} 40$ SERVICE CALL. AND A LOT MORE. 

The recording heads on your diskette drives may be dirtyand that can cause you a lot of grief. There's the serviceman you have to call when the machine doesn't perform. (You know how much service calls cost these days!) There's machine down-time. Idle data entry clerks. All the other delays a cranky machine can cause.

And that service call might not even be necessary.

## 3M solves the problem <br> in seconds-and leaves your heads

"Computer Room Clean".
The Scotch head-cleaning diskette kit lets you clean the read-write heads on your $8^{\prime \prime}$ or $5^{1 / 4^{\prime \prime}}$ diskette drives. In just 30 seconds, without any disassembly, mess or bother, the heads can be completely cleansed of dit, dust, magnetic oxides-all the things that can get into your machines every day. And foul them up.

Just saturate the special white cleaning pad in its jacket with the cleaning solution. Then insert the jackət into the diskette drive and turn it on. Your machine does the rest. The

heads are microscopically cleaned without wear, without abrasion.

This 3 M head-cleaning diskette kit has been evaluated and approved by major diskette drive manufacturers. It's the best possible way to clean your heads without service calls or machine teardowns.

At only $\$ 1$ per cleaningit's the best insurance you san get.
This fast-cleaning now Scotch kit comes with everything you need (including special fluid, applicator tip, cleaning diskettes) to handle up to 30 cleanings. That's only about a dollar a cleaning.
With the Scotch head-cleaning diskette kit, you could save yourself a lot more than just a service call. So try th s remarkable kit today. For the name of


A Scotch cleaning dishette shown before use, end aiter 15 sleanings of recoraing head's.

the dealer nearest you, call toll free: 800-328-1300. (In Minnesota, call collect: 612-736-9625.) Ask for the Data Recording Products Division.
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The famous SC-1 stylus brush (standard of the record and hifi industries) now has a synergistic fluid called SC-2.

SC-2 Fluid enhances and speeds cleaning and yet protects diamond adhesives, cartridge mounting polymers and fine-metal cantilevers against the corrosive effects of many other "cleaners."

The Discwasher SC-2 System. Stylus care you can finger as clearly superior.



CIRCLE NO. 21 ON FREE INFORMATION CARD

NOVEMBER 1980


## Editorial

## The Great Software Ripoff

It is really amazing how many good, honest citizens who are kind to children and pets, help senior citizens across streets, and wouldn't think of keeping the extra nickel in change that the newsdealer gave by mistake are perfectly willing to steal software. After all, that's what it is when you make up ten assetter of that new computer game for the guys in your club or when you and your brother-in-law chip in on a new album, one taking the disc and the other making a cassette copy. Not only are these practices fundamentally unethical, but they amount to taking potshots at the goose that lays the golden egg.

Naturally enough, anyone who invests capital in producing software of any description does so with the expectation of making a profit, and if the opportunity for that is taken away so is the motivation for creating the software in the first place. It was not too long ago
that owners of the rights to high-level languages were almost willing to bypass the hobby market completely, fearing that they could not adequately fend off unauthorized distribution of their produts. A repeat of that situation does not seem likely, but many of the small ingependent suppliers may well bail out if they find themselves working as a public service.
Though sanctions against unauthorized copying by individuals exist, even the RIAA (Recording Industries Assocation of America), the watchdog for the music recording industry, admits that enforcement is virtually impossible. And while sophisticated producers of computer software are often able to protect against piracy by hiding secret identifiers in their code (record companies cannot do the equivalent), they are equally powerless against individuals.

The only real solution to this is that
people remember what is fair and act accordingly. Making a back-up copy (technically a violation of copyright) is one thing, but giving copies to all your friends is clearly another. If the problem remains serious, we may end up with a legislative solution (probably expensive) that no one will like. More important, the victims of software ripoffs are not faceless companies, but honest individuald who pay the price driven up by others who lack scruples. Another point to remember is that many software suppliers started as hobbyists, and you may someday be selling programs yourself.


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# Buzz BOX 

# My wall timers were great for the first year. Then a strange thing happened. 

By Joseph Sugarman, President<br>JS\&A Group, Inc.

This advertisement is about a few wall timers I installed in my home. I expected them to automatically turn my lights on and off and make my home appear lived-in, even when I wasn't at home.
But I was in for a surprise. The timers started to buzz. Not loudly, at first, but still very noticeably. Then, after a few months, they started buzzing more loudly. And finally a few of them pooped out completely.

## ABSOLUTELY SILENT

No wonder. The timers I bought were mechanical devices with a motor and moving parts that switched on and off every 24 hours. In time, the parts aged and buzzed as they wore out. Eventually, they went out completely. I called my light timers, "Buzz Boxes" and knew the electronic revolution was not far behind with a silent and more dependable unit that would do a lot more.

Last month Dynascan Corporation contacted me with a new wall timer which they called the "Night Sentry." This one was totally solid state, no moving parts and above all, absolutely silent. But there were a few pleasant surprises.

## LIVING PATTERN PROGRAM

The Night Sentry is very easy to program. Just turn a dial to the time (an AM or PM indicator appears) and press a button which turns on the light. Set the time you want the light to go off and press the same button to turn the light off. Keep repeating this process and the Night Sentry will remember the entire program and repeat it for you in silent accuracy year after year after year.

You can also program it in its 'living-pattern' mode. Simply turn your lights on and off throughout a typical day as you normally do. After 24 hours the Night Sentry will remember the exact pattern and your timer will operate your lights just as you yourself did-all automatically.

## LONGER BULB LIFE

If by chance you have a power failure, the Night Sentry will keep its memory for at least 5 minutes. Since most power failures are
momentary, you won't have to reprogram it But if the power failure is longer than five minutes, when the power does come back on, your lights will remain in their off position.

The Night Sentry has a unique feature that in time could actually pay for the unit. Most bulbs burn out because the power surges each time a bulb is turned on. These power surges weaken a bulb. The Night Sentry has a built-in 'soft start' system that eliminates the surge and thus your bulb will last up to three times longer than its normal life. This is a very important feature for outdoor lighting where bulbs are often more difficult to change and seem to always go out in the dead of winter.

## BATTLESHIP CONSTRUCTION

When Dynascan Corporation developed the Night Sentry, they decided to go overboard and produce a product that far exceeded standard specifications. For example, the Raytheon Triac used in the system was designed for 220 -volt products - not just your


The Night Sentry is almost as small as a rypical light switch so you could even install two of them in a double gang wall plate.
standard 110 -volt wall switch. There are transient surpressors built to take a voltage surge of up to 2500 volts-in short the Night Sentry was built like a battleship.

Even if you've never installed a light switch in your life, you'd find it a snap to install the Night Sentry. Just turn off the power at your
fuse box, connect two wires by twisting them together and screw the Night Sentry into your old electrical box. Installation takes less than five minutes and you use the same wall plate you had on your switch before.

I have recently replaced all of the timers in my home. I even added a few in places like my bedroom so that all my lights turn on when it's time to get up. I don't need an alarm clock now. My outdoor and indoor lights are now silently and efficiently controlled, and my house looks lived in even when I'm away.

TRY ONE OUT
We urge you to order a Night Sentry for only $\$ 24.95$ plus $\$ 2.50$ for postage and handling (Illinois residents add 6\% sales tax) or order two for $\$ 47$ plus $\$ 3.00$ postage and handling. Credit card buyers may call our toll-free number below. We'll send you a Night Sentry solid-state timer complete with easy-to-install instructions and a one-year limited warranty. There is also a three-way switch timer for \$5 extra per unit
Then use the Night Sentry for a month. Program it. See how silently it operates and then order more for the other rooms in your house. If, however, you're not completely satisfied, retum your unit within 30 days for a prompt and courteous refund, including your postage and handling.

I can personally recommend the Dynascan Night Sentry timers. I have them throughout my home and my friends have them too. They really add to our security and peace of mind and I urge you to at least order one at no obligation, today.


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# The Age of Affordahle Pers 

 single board at a cost of under $\$ 300$. The Superboard II received rave reviews by microcomputer experts such as:
"We can heartily recommend the Superboard II computer system for the beginner who wants to get into microcomputers with a minimum of cost. Moreover, this is a 'real' computer with full expandability.'

POPULAR ELECTRONICS MARCH, 1979
"The Superboard II weighs in at $\$ 279$ and provides a remarkable amount of computing for this incredible price.'

KILOBAUD MICROCOMPUTING FEBRUARY, 1979
"The Superboard II and its fully dressed companion the Challenger IP series incorporate all the fundamental necessities of a personal computer at a very attractive price. With the expansion capabilities provided, this series becomes a very formidable competitor in the home computer area,"

INTERFACE AGE APRIL, 1979
"The graphics available permit some really dramatic effects and are relatively simple to program...The fact that the system can be easily expanded to include a floppy means that while you are starting out with a low-cost minimal system, you don't have to throw it away when you are ready to go on to more complex computer functions. At \$279, Superboard II is a tough act to follow.'

RADIO ELECTRONICS JUNE, 1979
"The Superboard is an excellent choice for the personal computer enthusiast on a budget."

BYTE MAY, 1979

Since the introduction of Superboard II, the cost of personal computers has actually gone up with new models by major manufacturers ranging from $\$ 1000$ to well over $\$ 4000$ due to the general cost of inflation and the increasing functionality included in these computers. Today Cleveland Consumer Computers is offering you the original Superboard II at its original price of just $\$ 279$. In today's economy this is by far the best buy
in personal computing ever!
The Superboard II can entertain your whole family with spectacular video games and cartoons, made possible by its ultra high resolution graphics and super fast BASIC. It can help you with your personal finances and budget planning, made possible by its decimal arithmetic ability and cassette data storage capabilities. It can assist you in school or industry as an ultra
powerful scientific calculator, made possible by its advanced scientific math functions and built-in "immediate" mode which allows complex problem solving without programming! This computer can actually entertain your children while it educates them in topics ranging from naming the Presidents of the United States to tutoring trigonometry - all possible by its fast extended BASIC, graphics and data storage ability.
The machine can be economically expanded to assist in your business, remotely control your home, communicate with other computers and perform many other tasks via the broadest line of expansion accessories in the microcomputer industry.
This machine is super easy to use because it communicates naturally in BASIC, an English-like programming language. So you can easily instruct it or program it to do whatever you want, but you don't have to. You don't because it comes with a complete software library on cassette including programs for each application stated above. Ohio Scientific also offers you hundreds of inexpensive programs on ready-to-run cassettes. Program it yourself or just enjoy it; the choice is yours.
The Superboard II comes fully assembled and tested. It requires +5 V at 3 Amps and a video monitor or TV with RF converter to be up and running.
$\$ 279.00$

## Standard Features:

- Uses the ultra powerful 6502 Microprocessor.
- 8 K Microsoft BASIC-in-ROM. Full feature BASIC runs faster than currently available personal computers and all 8080 based business computers.
- 4 K static RAM on board expandable to 8 K .
- Full 53-key keyboard with upper/lower case and user programmability.
- Kansas City standard audio cassette interface for high reliability.
- Full machine code monitor and I/O utilities in ROM.


## onal Computing is Still Here.



Direct access video display has 1 K of dedicated memory (besides 4 K user memory), features upper case, lower case, graphics and gaming characters for an effective screen resolution of up to 256 x 256 points. Normal TV's with overscan display about 24 rows of 24 characters without overscan up to $30 \times 30$ characters.

## Dptional Extras:

- Available 610 expander board features up to 24 K static RAM (additional), dual mini-floppy interface, and an OSI 48 line expansion interface
- Assembler/Editor and Extended Machine Code monitor available.
-630 I/O Expander.
RGB color and NTSC composite color outputs with up to 16 colors, Dual 8-axis joystick interface, AC remote control interface which mates with AC-12P, home security interface which mates with the AC-17P, 16 -line parallel I/O interface, 16 -pin I/O bus interface which allows the connection of parallel I/O lines or high speed analog I/O module, or a PROM blaster or solderless interface prototyping board, programmable sound generator and program selectable modem and high speed printer ports, and more.

Freight Policies All orders of $\$ 100$ or more are shipped freight prepaid. Orders of less than $\$ 100$ please add $\$ 4.00$ to cover shipping costs. Ohio Residents add 5.5\% Sales Tax.

## Guaranteed Shipment Cleveland

Consumer Computers \& Components guarantees shipment of computer systems within 48 hours upon receipt of your order Our failure to ship within 48 hours entitles you to $\$ 35$ of software, FREE.

## Hours:

 8:00 AM to 5:00 PM E.D.T
## Software:

Ohio Scientific and independent suppliers offer hundreds of programs for the Superboard II, in cassette and mini-floppy form. Here is a sampling of popular Ohio Scientific programs for the Superboard II.


| SBII \& Clip | Price |
| :---: | :---: |
| SCE-336 | \$35.00 |
| SCE-353 | 6.50 |
| SCE-332 | ${ }^{6.50}$ |
| SCE-326 | 6.50 |
| SCE-339 | 6.50 |
| SCE-342 | 6.50 |
| SCE-324 | 9.00 |
| SCE-34 | 6.50 |
| SCE-329 | 6.50 |
| SCE-319 | 6.50 |
| SCE-337 | 9.00 |
| SCE-345 | 6.50 |
| SCE-335 | 6.50 |
| SCE-332 |  |
| SCE-318 | ${ }_{6.50}^{6.50}$ |
| SCB-523 | 9.00 |
| SCB-520 | 6.50 |
| SCB-518 | 6.50 |
| SCB-524 | 6.50 |
| SCB-500 | 9.00 |
| SCB-525 | 00 |
| SCP-716 | 9.00 |
| SCP-708 | 6.50 |
| SCP-719 | . 00 |
| SCP-717 | 6.50 |
| SCP-718 | ${ }_{6} .50$ |
| SCP-720 | 9.00 |
| SCG-975 | 6.50 |
| SCG-955 | 6.50 |
| ScG-977 | 50 |
| ScG-95l | 6.50 |
| SCG-960 | 6.50 |
| SCG-979 | 6.50 |
| SCG-925 | 6.50 |
| SCG-956 | 6.50 |
| SCG-962 | 6.50 |
| SCG-949 | 6.50 |
| SCG-942 | 6.50 |
| SCG-946 | 6.50 |
| SCG-926 | 6.50 |
| SCG-945 |  |
| SCG-950 | 14.00 |



## Hardware:

Superbocred II
specitied in the advertisemen
pandable to 24 K or 32 K system total
Accepts up to two mini-floppy disk
drives. Requires+5V@4.5 amps. 298
Mini-Floppy Disk Drive

Includes Ohio Scientific's PICO
DOS software and connector cable
Compatible with 610 expander
board. Requires +12V@1.5 amps and +5V@0.7 amps.
630 Board As specified in the advertisement 229
AC-3P $12^{\prime \prime}$ combination black and white
TV/video monitor. 159
4KP 4KRAM chipset. 79
PS-005 5V 4.5 amp power supply for Superboard II.
Mini-floppy power supply. 29
S-003 ClP/Superboard II Manual
OS-65D V3.2 Disk Operating System with 9 -digit extended BASIC, random access and sequential files. Metal case for Superboard II,
$\begin{aligned} & \text { CS-600 Metal case for Superboard II, } \\ & \text { and } 630 \text { board and two power }\end{aligned}$ supplies.
Metal case for single floppy disk drive and power supply
CS-610 Metai case tor single hoppy disk
AC-12P Wireless AC remote control system
Includes control console, two lamp modules and two appliance
modules tor use with 630 board
AC-17P Home security system. Includes console, tire detector, window protection devices and door unit for use with 630 board.
C4P Sams C4P Manual.
C3 Sarms Chailenger III Manual

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Or to get our free catalog CALL 1-800-321-5805 TOLL FREE. Charge your order to your VISA or MASTER CHARGE ACCOUNT Ohio Residents Call: (216) 464-8047. Or write, including your check or money order, to the address listed below.

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## CLEVELAND CONSUMER P.O. Box 46627 <br> CLEVELAND CONSUMER COMPUTERS \& COMPONENTS Box 46627 Cleveland, ohio 44146

Superboard II \$279. $\square 630$ Board \$229.
610 Board $\$ 298$.
AC-3P 12" B-W Monitor \$159.
Mini-Floppy Disk Drive \$299. $\square$ ClP Sams Manual $\$ 8$.
(Attach separate sheet for other items.)
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1

## Video Review knows a bright idea when it sees one.



Video Review magazine tests á lot of sophisticated video products.

They get to see virtually every make and type of color TV receiver. Which makes their selection of Magnavox as their standard TV receiver pretty impressive.
"We thought the Magnavox picture quality and resolution were superb."
"Ever since Video Review began testing products," says the magazine, "we've been looking for a top quality, 19 -inch $T V$ set that might serve as a standard of reference for all of the other products we test...video cameras video cassette recorders, video cassettes.
"We thought the Magnavox picture quality and resolution were superb, and that off-the-air sensitivity was also extremely good
'Major VHF channels were received with uniformly accurate color fidelity. This receiver produced superior color pictures
even when using its own indoor VHF "Tand UHF antennas""
"The special tuning features and remote control capabilities of the Magnavox receiver are awesome."

The tuning system is purely electronic and totally digital," they continue. "There is a fine tune switch and a memory lock button. If any channel is received mistuned, the user simply fine tunes up or down in frequency by holding the button, and when perfect tuning has been achieved, the button is released and the memory lock button is depressed once.
"Nearby is Magnavox's Videomatic feature. Depressing this button activates the electronic eye for automatic brightness adjustment, color adjustment circuits and "utomatic fine tune"
"...unusually good for any receiver." Overall, Video Review rated the Magnavox 9.5 or better (out of a
possible 10.0) on Video Quality, Reception Sensitivity. Color Fidelity, and Video Resolution and Fidelity. As they put it, "... unusually good for any receiver.

We can only add that once you see a Magnavox color TV at your Magnavox dealer, we think you'll agree.

For Magnavox color TV specifications, write Magnavox Consumer Electronics Company, Dept. 700 P.O. Box 6950, Knoxville,

Tennessee 37914
© 1980 magnavox consumer electronics co


The brightest ideas in the world are here to play.

## $\Delta$ <br> Letters

## Noise Generators and Tinnitus

I have had several letters with regard to uses for noise generators such as those described in my Experimenter's Corner of March 1980. One of the more interesting came from a reader who suffers from tinnitus (the perception of sounds which are not actually present). He wondered if a noise generator would be effective in masking the tinnitus, which, in his case, is both a background noise and a pulsative noise synchronized with his heartbeat. He noted that he gets some relief by listening to the "pepper and salt" noise produced by a nonbroadcast TV channel via an earphone. He also stated that his background tinnitus is almost eliminated by the high-pitched sounds produced by crickets and other insects on a summer evening.
It is indeed possible that a noise generator could be used to partially alleviate tinnitus by simulating and masking the noise. If any reader has suggestions or wishes to correspond with the original letter writer regarding tinnitus or pulse-triggered masking circuits, please send them to me in care of popular electronics and I will forward them.-Forrest M. Mims.

## Switch Identification

In "Experimenting with a SoundEffects Generator" (May 1980), one side of switches S14 through S18 is labeled 0 . Does this mean "logic 0 "? If so, where is it supposed to be connected? To ground?-Dale Hileman, Topanga, CA 90290.

As identified in Table II, one side of each of these switches is connected to logic 0, which in this case is ground, logic $I$ being +5 volts. The switch points can all be connected together and then to ground. - Ed.

## Out of Tune

In "Build a LED Pendulum for Digital Clocks" (July 1980, p. 30), the connections between the 7400 and the 7442 should be reversed. That is, pin 1 of the 7400 should go to pin 11 of the 7442 and pin 5 of the 7400 to pin 1 of the 7442 .

In the Project of the Month in August 1980, the output terminal of the 741 op amp should be labelled 6 instead of 1 .


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## Micro-Acoustics

Phono Cartridge


Micro-Acoustics has announced a line of "System II" phono cartridges that it claims are the lightest weight, fast-est-transient-response cartridges ever manufactured. The Model 3002 is the middle of the three-model line. It uses a direct-coupled microminiature transducer instead of magnet coils for improved transient response. Use of the electret transducer, a low-mass carbon-fiber housing, hair-thin beryllium cantilever, and an almost microscopic diamond stylus are responsible for the weight reduction. The ultralow mass of the cartridge is said to aid in accurate tracking and reduced record wear. Specifications: frequency response, 5 to $20,000 \mathrm{~Hz} \pm 1.25 \mathrm{~dB}$; tracking force. 0.7 to 1.4 g ; transient ability (rise time), $5 \mu \mathrm{~s}$; channel separation, 30 dB at $1 \mathrm{kHz}, 20 \mathrm{~dB}$ at 10

## "Singing" DMM

The MX333 Digital Multimeter from Hickok Electrical Instrument Co., features $31 / 2$-digit LCD display, auto zero, polarity, and decimal point. It measures dc and ac voltage from 200 mV to 1 kV in five ranges with 10 megohms input resistance. Resistance is measured in seven ranges from 20 ohms to 20 megohms with overload protection to 500 volts de. The test voltage is 0.25 volt maximum full scale and 3.2 volts open circuit. Current, ac and dc, is measured from 2 mA to 10 A in five ranges, and a diode test function is provided. The MX333 contains VARI-PITCH, an audible tone with frequency proportional to the meter indication, and LOGITRAK, which combines the features of a logic probe and voltmeter and is
kHz ; cable capacitance/phono input load resistance (not critical), 25 to $100 \mathrm{pF} / 5$ to $100 \mathrm{k} \Omega$; cartridge weight: 2.5 to 4 g . \$150.

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## Technics "Class-A" Stereo Receiver



The Model SA-616 stereo receiver from Technics features a power amplifier, said to eliminate switching distortion, and rated to deliver 80 watts/ channel into 8 ohms from 20 to 20,000 Hz with no more than $0.005 \%$ THD. It uses a quartz synthesizer tuner,

## Disk Drive for HP-85 Computer



The HP 82900 Series disk drive from Hewlett-Packard reads double-sided, double-density $51 / 4^{\prime \prime}$ diskettes and can be configured to provide from 270 K bytes to 1.08 M bytes of storage for the HP-85 Personal Computer. With its interface, the HP-85 Mass Storage ROM (\$145), plugged into the HP-

85, the disk system is ready at power turn-on. The ROM also includes 30 additional BASIC commands, as well as a TRANSLATE command to automatically upgrade tape-based programs for the disk. It provides the ability to store and retrieve graphics, data forms and labelled charts; automatic default to disk; and volume labeling that allows the user to refer to disks by name and write programs independent of drive addresses. The HP 82901 M dual master drive having 540 K bytes storage costs $\$ 2500$, the HP 82901S dual add-on drive for 1.08 M bytes $\$ 2200$, the HP 82902 M single drive ( 270 K bytes) $\$ 1500$, and the HP 82902 S single add-on drive (total 540 K bytes) $\$ 1300$.

CIRCLE NO. 86 ON FREE INFORMATION CARO

said to catch pulses as short as 5 ns . Any standard 10:1 high-frequency scope probe can be connected to the MX333. \$235.

CIRCLE NO. B8 ON FREE INFORMATION CARD
with digital display of station frequency. Manual tuning is accomplished by pressing the up/down scanning bar, providing $200-\mathrm{kHz}$ steps on FM and $10-\mathrm{kHz}$ steps on AM. Furthermore, 16 stations ( 8 each AM and FM) can be preset for instant recall at the touch of a button. Other features: two-way tape dubbing;
h/mirange boost/cut switches; LED power display: and protection/safety indicators.

CIRCLE NO. B9 ON FREE INFORMATION CARD

## Indoor Scanner Antenna

Antenna Incorporated's Persuader is designed for scanner monitoring of the public service bands between 25 and 900 MHz . It is meant to be hidden behind curtains or drapes and is said to outperform the short, stock telescopic antennas provided with most scanning monitors. It consists of a seven-foot wire antenna element that is to be attached vertically to an interior wall, a small adhesive-backed box to be mounted on the baseboard, a two-foot length of wire for attachment to a ground, and a ten-foot length of signal cable terminated with a pin plug for insertion into the scanning monitor's antenna receptacie. \$11.95.

CIRCLE NO. 91 ON FREE INF ORMATION CARD

FET Power Amplifier


Optonica's Model SX-9305 power amplifier is rated to deliver 100 watts minimum power per channel into 8 ohms from 20 to $20,000 \mathrm{~Hz}$ with no


# Now, the closely held secret behind many of today's quartz timepieces is revealed. 

Most major companies would rather have these facts remain secret, but one little-known company decided to show its genius to the world.

To effectively compete in world markets and when lacking in necessary technology, many major companies have turned to smaller more dynamic companies to build their products. Sometimes the product is built to the specs of the major company. But more often than not, the only unique parts are a label and different owner's manual. This practice is quite prevalent in the digital watch industry.
One company that has been the real source behind products introduced in the U.S. by companies like Mattel, Timex and Texas Instruments, is Olympos Electronic Co., also known as Otron.
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| Timer and Night Light for Evening Viewing | $10: 3425$ | 12 or 24 hr count down, count up timer. |

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more than 0.01\% THD. It uses FET circuitry and direct coupling throughout. The power supply employs a toroidal transformer, and a gas/liquid heat-radiation system cools the output stage. The package includes a 10 band audio spectrum analyzer (switchable to display left or right channel) and LED power indicators, as well as a detented volume control and speaker selector switch (A, B, or $A+B) . \$ 850$.

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## EPROM Eraser

Operating at 2537 Angstroms with a power of 1.5 watts, the QUV-T8 UV EPROM Eraser can be used with 2704, 2708, 2716, 2532, 2564, and similar ultraviolet erasable EPROMs. Claimed erase time is 25 minutes at a distance 1 inch from the ultraviolet source. The ultraviolet lamp requires 8 watts and has a rated 7700 -hour operating life. $\$ 34.95$. Address: Logical Devices, Inc., 1525 N. E. 26th St., Ft. Lauderdale, FL 33305.

## ID System for Wrapped-Wire Sockets



OK Machine and Tool Corp. is now offering the "Socket-Wrap I.D.," an identification system designed for use with wrapped-wire IC sockets. This system comprises a number of socket-

## Kenwood 2-Meter FM Transceiver



Trio-Kenwood Communications' new Model TR-7800 FM transceiver covers the 2 -meter amateur band as well as adjacent CAP and MARS frequencies (143.900-148.995 M Hz). The microprocessor-controlled transceiver can be tuned to a desired frequency by means of either a keypad or a rotary tuning control. Fifteen programmable memory locations accept frequency and offset instructions. One of them accepts transmit and receive frequencies independently for access to repeaters with nonstandard splits, while another is for a "priority" channel. (An audible alert is generated upon receipt of a signal on the priority channel if this function is activated, and the push of a switch tunes the rig
to that channel.) Information is retained in the memories after removal of power if four AA NiCd batteries are installed. The front-panel keypad also controls an internal tone encoder for autopatch use, and is used to select transmit offsets, to program the memories, and to control the rig's scanning function. The latter can scan all 15 memory channels and the entire band in 5 - or $10-\mathrm{kHz}$ steps. Two buttons (UP and DOWN on the microphone's case) permit manual scanning of the band. Two seven-segment LED readouts display the memory channel selected and the receive frequency. A LED bargraph displays received signal strength and transmitter r-f output. Rated sensitivity is $0.5 \mu \mathrm{~V}$ for 30 $\mathrm{dB}(\mathrm{S}+\mathrm{N}) / \mathrm{N}$; selectivity -60 dB at $\pm 12 \mathrm{kHz}$; and r-f output power into 50 ohms is 25 watts or 5 watts (switchable). Current demand is 6 amperes max. at 13.8 volts. The rig weighs 4.6 lb and measures $81 / 16^{\prime \prime} \mathrm{D}$ $\times 6^{7 / 8^{\prime \prime} \mathrm{W} \times 21 / 2^{\prime \prime} \mathrm{H} . \$ 399.95 .}$

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## TRS-80 Cassettes Load at Disk Speed

Personal Micro Computers Inc., announces a Fastload Cassette Interface said to input data to a TRS-80 Model I, Level II computer at 8000 baud ( 16

times normal speed) using a modified CTR-41 recorder. Any cassette saved at normal ( 500 baud) speed can now be loaded at high speed. Fastload is also capable of high-speed search for BASIC programs by a single character designation, or system programs having a name up to six characters in length. High-speed recording is not provided. The system contains a keyboard debounce program having automatic repeat and provides a sound output for each keystroke. Instructions for the CTR-41 modification are provided. \$188. Modified CTR-41 recorder is $\$ 95$.
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## Low-Cost AM/FM-Stereo PLL Tuner



JVC's newly announced Model T-X3 is a low-cost stereo FM/AM tuner that features phase-locked-loop (PLL) tuning, phase tracking loop (PTL) FM-detector circuitry, and quicting slope control. The PTL system increases selectivity with wide bandpass response, while the quieting slope control automatically corrects weak FM signals by compensating the amount of left-with-right high-frequency mixing. The tuner's $50-\mathrm{dB}$ quieting sensitivity on FM stereo is 31 dBf (quicting on auto), 36.8 dBf (quieting off). Other specifications: stereo separation, 50 dB at 1 kHz : $0.15 \%$ stereo distortion at 100 Hz and $1 \mathrm{kHz} ; \mathbf{S} / \mathrm{N} .78 \mathrm{~dB}$ stereo; frequency response, 30 to $15.000 \mathrm{~Hz}+0.3 /-2$ dB; capture ratio, 1.0 dB . Size is $31 / 2^{\prime \prime} \mathrm{H}$

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## Crown Pressure-Zone Microphone

Crown International's Model PZM30GP pressure-zone microphone is said to employ a newly developed system of sound pickup, using the pressure zone that arises at an acoustic boundary to suppress frequency-response anomalies and other problems of conventional designs. The electret active element is positioned on a $5^{\prime \prime} \times$ 6 " flat plate so that it faces the acoustic boundary and lies within the pressure zone. Pickup pattern is hemispheric and a cardioid pattern can be produced by placing a foam template behind the transducer. An external buffer circuit/power supply is required. Specifications: frequency response, 50 to 15.000 Hz ; impedance, 150 ohms balanced; THD, less than $3 \%$ at 150 dB SPL.; $\mathrm{S} / \mathrm{N}$, less than 26 dB SPL equivalent acoustic input: sensitivity, 76 dB open-circuit voltage where $0 \mathrm{~dB}=1$ volt/microbar $\$ 349.00$

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## Spool Solder Dispenser

The Model SM-I "Solder-Mate" from Electronic Products Equipment Corp. is a new dispenser designed to accommodate 1-lb spools of wire solder. The solder spool mounts in the dispenser's slots with the aid of a plastic spindle. \$7.50. Address: Electronic Products Equipment Corp., P.O. Box 5238, Manchester, NH 03108

## Rotatable Antenna Digital Readout

The DX-3 kit takes ham or CB antenna rotor control position and converts it to a digital signal for display on a 3 digit, 7 -segment LED readout. Claimed antenna azimuth aiming accuracy is one degree. The kit uses a three-wire interconnect and is adaptable to most rotor systems. $\$ 39.95$. Ad dress: Monitor, Box 55. Agincourt, Ont., Canada M1S 3B4.

## Logic Timing Recorder



The Logic Timing Recorder from AP Products Inc.. is a timesaving device for charting logic timing. The Recorder is fabricated from ABS plastic and measures $113 / 4^{\prime \prime}$ by $81 / 4^{\prime \prime}$ and is $1 / 4^{\prime \prime}$ thick. It consists of 320 slides arranged in eight horizontal rows. Moving vertically between two click-stop positions, the slides represent the two logic levels in a circuit. After the slides have been placed in the position that represents the logic state of the circuit, the board is placed in a conventional copying machine to make a record for filing. After copying, the Recorder can be re-used for other digital circuits whose waveforms must be retained for future reference. Blank spaces are provided for notes, dates, model and drawing numbers, customer and product names, and space for designers initials. \$44.95.

CIRCLE NO. 99 ON FREE INFORMATION CARD

## Low-Range Ohmmeter

The Model RX-2 LOHMETER (lowohmmeter) from Alpha Components Corp., is designed to work in conjunction with a conventional analog or digital voltmeter to make resistance measurements from 0.001 ohm to 100 ohms, in three ranges. The device uses the four-terminal principle to compensate for test-lead resistance. It is powered from the 117 -volt ac line but is available in a 240 -volt version. A pair of LEDs, coordinated with the range selector switch indicates the range. $\$ 39.95$. Address: Alpha Components Corp., 115 Eucalyptus Dr., Box 306, El Segundo, CA 90245

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there lsin't anything l've
heard that improves the sound quality as much as the 801 does. Eepe-, clally for such an economical cost.
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"The 801 makes you feel like you are right in the studio or concert hall. The music totally surrounds you . . . An audiophile's dream come true, SUPER FANTASTIC!!'
J. Capsalis, Newport News, VA
"I have almost $\$ 6,000$ in my stereo. I have time delay, reverb amp, Cqualizer, dynamic range expander, etc. I have never been more surprised or impressed by any piece of equipment than I was by your 801 Omnisonic Imager! Right On!"
B. Nolan, Massillon, Ohio
"When I first turned on the switch I felt a resurgence of spirit-uplifting. The quality of sound-especially in separation and clarity-was greatly improved. When I had my eyes shut I feit surrounded by music In space - very enjoyable feeling."
L. Jensen, Tampa, FL
"I must say before anything alse that it's the greatest piece of equipment I own! Every advertising claim made was true, which is quite unbelievable, untll you hear the Imager in operation. I will never turn it oft.'
C. Zilavy, Vineland, NJ
"On the basls of subjective expert ence, I can attest that the Omnt sonic product works quite woll. It seems to remove virtual sound sources from the plane of the loudspeakers and distribute them at various positions in the Ifstening space, adding a sense of front-toback dimension at the same fime. At times, some sounds appear to come from in back of the listener, which is a startling offect, given that sound ts baing radlated oniy from the front. Another effect the device produces is a greatly increased sense of ambience or 'spaclousness.

Harold A. Rodgers Executive Editor Popular Electhonics July 1980
What better Hollday gift for yourself or your friend(s) than the 80t ouNI:SONIX IMACEAI Call or write today! (See opposite page.)

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## SAE TWO Model Rg

## AM/FM Stereo Receiver

## Features quartz-lock digitally synthesized tuning and 90 watts/channel into $\mathbf{8}$-ohm loads

THE SAE Model R9 is a deluxe AM/FM stereo receiver featuring digitally synthesized tuning and an exceptionally competent power amplifier section rated at 90 watts per channel into $8-\Omega$ loads with no more than $0.05 \%$ total harmonic distortion. Like the other SAE TWO components, the R9 is finished in black, accented by walnut-grain wooden side panels. Accessory handles and mounting adapters are available for mounting the receiver in a standard 19 -inch EIA rack. SAE TWO audio components are manufactured in Japan to SAE's design specifications. Suggested retail price is $\$ 800$.

General Description. The "dial window" of the front panel is quite different from the usual receiver tuning display. Frequency is shown by four large ( $1 / 2^{\prime \prime}$ ) blue digits, flanked by the indicators "FM" and "MHz.," or "AM" and "kHz," according to the selected band. Only the "odd" FM channels ( $88.1,88.3$, etc. MHz ) can
be tuned; AM tuning increments in I kHz steps.

Power to the speakers or voltage to the TAPE RECORDER jacks can be read from a dual horizontal LED display. The function chosen is shown by adjacent LEDs. The scale is calibrated in watts and decibels relative to 90 watts into $8-\Omega$ loads. When showing output to the recorder jacks, the display is calibrated so that 0 dB corresponds to "0 VU" ( 0.77 volts). The DISPLAY selector also has an FM/AM position that converts the upper (LEFT) bar to a relative signal strength indicator for FM and AM, and the lower (RIGHT) bar to a multipath distortion indicator for FM. The LEDs that identify function change accordingly.

A large volume knob is found on the panel, but there is no conventional tuning knob. Instead, a pair of flat pushbuttons to the right of the digital display tune the receiver UP and DOWN in frequency. There are two tuning modes, neither of which is clearly identified in any of SAE's ac-
companying literature. The choice between the two is implemented via the SCAN button. With SCAN disengaged, a momentary touch of either tuning button will step the tuned frequency in the corresponding direction until a signal is acquired. In the other mode, frequency changes as long as the button is held in, stopping at the first FCC-assigned channel reached after the button is released. A momentary touch of the button shifts the tuning by one channel.

After a signal has been acquired, a quartz-lock feature comes into action automatically. According to the manufacturer's description, quartz-lock fine tunes the receiver to the exact station frequency, instead of to the local oscillator frequency reference. (The need for this is not evident, nor is it explained further.) When the station is locked in, a green Q-1.OCk indication comes on below the level display. Below the frequency readout is a red STEREO indicator for FM.

A series of pushbuttons below the display area select the program source (PHONO 1, PHONO 2, AUX, FM, and AM), and engage HI BIEND (for reducing noise on weak FM stereo signals), FM MUTE, and SCAN. FM MUTE is nor-


A voltage-current plot of the output characteristics at clipping of the SAE R9 for various load impedances. Maximum open-circuit voltage is about 34.4 V ; current in excess of 9 amperes to a low-impedance load trips protective system. Both channels driven by a $1 \cdot \mathrm{kHz}$ signal.
mally on and cuts out when SCAN is depressed.

Under the cuning buttons is a similar set of buttons for l. Fil ter. louidvess. and audio wuting (a $-20-\mathrm{dB}$ gain reduction). A similar button below the volume knob is the power switch.

Three knobs act as separate bass. MID and Treble tone controls, and there is a switch that bypasses them all. Other switches provide tape novi. TOR functions for two tape decks and cross-connect the decks for dubbing with either as source. Another switch, marked Ext pros, can connect an accessory such as an equalizer or noise reducer into the signal path, either before or after the tape monitoring and recording circuits.

Normal or reversed stereo or mono operation can be chosen via the mODE switch. Either, both, or neither of two sets of speaker connections can be switched in. and there is a headphone jack that is always active. The balance control has a center detent.

Several internal protective circuits safeguard both the R9 and the speakers connected to it from damage due to failure or careless operation. On initial power-up, there is a delay of 3 to 5 seconds before a relay connects the speaker outputs, to allow internal voltages to stabilize. If a significant dc offiset voltage appears at the output, the speakers are also disconnected. Service is restored automatically when the fault is corrected. This system also protects against large infrasonic transients. Tripping the protection, say the instructions. does not silence the receiver, but reduces output to a low level.

Other protective devices include a thermal cut-out. fuses, and a circuit that limits output current into resistive loads under $2 \Omega$ without restricting the drive into $4-82$ or higher impedances, even with reactive loads such as electrostatic speakers.

On the rear of the SAE R9 are the various signal input and output jacks, insulated spring connectors for the speaker outputs, and small binding post terminals for the antenna connections. There is a hinged AM ferrite rod antenna. Three of the four ac outlets are switched.

The SAE R9 is $181 / 4^{\prime \prime} \mathrm{W}, 175 / 8^{\prime \prime} \mathrm{D}$. and $51 / 4^{\prime \prime} \mathrm{H}$. It weighs 35 pounds.

Laboratory Measurements. The one-hour preconditioning period at one-third rated power left the top of the SAE R9 only moderately warm. Power output at clipping, with both channels driven at 1 kHz , was 110 watts into $8-\Omega 2$ loads, 123 watts into $4 \Omega$, and 166 watts into $2 \Omega$. The receiver is rated only for $8-\Omega$ operation, so its IHF clipping headroom comes out to 0.85 dB . Short-term power output at clipping, using the tone-burst signal of the IHF dynamic headroom test, was respectively 156 and 168 watts for $4 \Omega$ and $8 \Omega$. At $2 \Omega$. dynamic power output measured 0.3 dB less than the continuous clipping output. This seems unlikely in fact, and probably results from a less accurate power measurement in the dynamic test (voltage at clipping must be read from an oscilloscope). The IHF dynamic headroom rating (at $8 \Omega$ ) is 2.4 dB .

Distortion performance casily met and surpassed its ratings (at least 90
watts per channel to $8-\Omega$ loads from 20 to $20,000 \mathrm{~Hz}$ with no more than $0.05 \%$ total harmonic distortion) Measurements with $2-\Omega$ loads were complicated somewhat by interruptions due to the thermal circuit breaker, dc fuses, and overcurrent protective systems, but these, along with the data for $4-\Omega$ loads, fell within the primary ratings.

At maximum gain ( 46 dB ), the amplifiers required a high-level (AUX) input of 14.5 mV for a reference output of I watt: A-weighted signal-to-noise ratio was 74.7 dB . Through the phono input, sensitivity was $0.21 \mathrm{mV}(83 \mathrm{~dB}$ overall gain). with a $73.5-\mathrm{dB}$ signal-to-noise ratio. The lowest phono overload level in the 20 to $20,000 \mathrm{~Hz}$ range (referred to the equivalent 1.000 Hz levels) was 207 mV . Phono input impedance was $52 \mathrm{k}!$ in parallel with 70 pF .

We measured IHF IM distortion using two equal-amplitude input signals at 19 and 20 kHz . Their equivalent peak value corresponded to that of a sine wave output of 90 watts into $8 \Omega$. The lower third-order distortion product at 18 kHz was at a $-70-\mathrm{dB}$ level $(0.03 \%)$ and the second-order difference tone product at 1 kHz was at a negligible $-90 \mathrm{~dB}(0.003 \%)$.

IHF transient overload recovery time was about $20 \mu \mathrm{~s}$. This was measured with an input signal that gave continuous output of 9 watts at 1 kHz and was increased by 20 dB for 20 ms out of each half second

Square-wave rise time was $6 \mu$ s through the at $x$ inputs. and the slew rate was 19 volts per $\mu \mathrm{s}$. HF slew factor was 5.5 , with slew limiting becoming apparent on a "full signal" drive waveform at 110 kHz .

When we drove a rather severe load of $4 \Omega$ in parallel with $3 \mu \mathrm{~F}$. using a $10-\mathrm{kHz}$ square wave, we observed a couple of cycles of ringing at about 40 kHz , but no sign of instability or protective shutdown, even at full power.

The tone controls had the familiar Baxandall shape, with the bass turnover frequency shifting between 100 and 300 Hz , and the family of treble curves hinged at about 3 kHz . The midrange control effect was greatest


Overload recovery. "Glitch" between clipped waveform at left and sine wave indicating return to normal operation lasts about $20 \mu \mathrm{~s}$. Horizontal scale is $100 \mu$ s per division.


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The P34A is our top of the line portable. It heats, it refrigerates and with specially designed electronic circuit control (patent pending) it allows you to dial a complete range of tempera tures from very cold to very warm. Refrigerates weekend supplies for boating, camping, hunting and fishing. Then bring your fish or game home refrigerated. Low battery indicator wams you when you need a recharge and the sensing circuits efficiently control your power consumption to save on battery drain. This is our best-of-theline, recreational fridge and food warmer with all the bells and whistles for the discerning buyer who demands quality, size and complete versath ty. Use from a cigarette lighter receptacle or plugged into house current with our optional 110 volt adaptor.

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Trace A shows input square wave with frequency of 10 kHz . Trace B shows output waveform across 4- $\Omega$ resistor;
$C$ shows waveform across $4-\Omega$
resistor in parallel with $3-\mu F$ capacitor.
between 1 and 2 kHz , with a range of about $\pm 10 \mathrm{~dB}$. Loudness compensation boosted both low and high frequencies, and the low-cut filter had its $-3-\mathrm{dB}$ response at 30 Hz .

RIAA phono equalization was within $\pm 0.5 \mathrm{~dB}$ of the ideal curve from 20 to $20,000 \mathrm{~Hz}$. The phono preamplifier of the R9 uses a lownoise IC, apparently an "op amp," that completely isolates the equalization components from the cartridge input. As a result, phono response was identical whether measured through the inductance of a cartridge or from a resistive source.

The FM tuner had a mono IHF usable sensitivity of $11 \mathrm{dBf}(1.9 \mu \mathrm{~V})$ and a stereo sensitivity of 17 dBf (4 $\mu \mathrm{V}$ ). There was no stereo switching threshold, so that weak signal stereo reception was limited only by noise and distortion. Mono and stereo 50 dB quieting sensitivities were respectively $13.8 \mathrm{dBf}(2.7 \mu \mathrm{~V})$ and 36.6 dBf ( $37 \mu \mathrm{~V}$.)

The FM distortion at $65 \mathrm{dBf}(1,000$ $\mu \mathrm{V}$ ) input was $0.17 \%$ in mono and $0.2 \%$ in stereo. The respective noise levels were -74 dB and -68.5 dB . An IHF-IM distortion measurement of the tuner, using 14 and 15 kHz tones of equal amplitude and a peak deviation of 75 kHz , revealed equal third-order distortion products at 13 and 16 kHz at $-53 \mathrm{~dB}(0.2 \%)$ and a $1-\mathrm{kHz}$ second-order component at $-46 \mathrm{~dB}(0.5 \%), 100 \%$ modulation.

FM stereo frequency response was flat within $\pm 0.5 \mathrm{~dB}$ from 30 to 5,000 Hz , and rose to +3.7 dB at 15 kHz . Lacking a schematic, we could not judge whether this was the result of an incorrect multiplex filter response, or an error in the FM de-emphasis time constant (both channels measured alike). Any "brightness" resulting from this response would probably not be audible except in direct comparison to another tuner. (We did not hear it in regular listening.) Stereo
channel separation was 40 dB in the midrange, 26.5 dB at 30 Hz , and 31 dB at 15 kHz .

FM capture ratio was 1.25 dB at 45 $\mathrm{dBf}(100 \mu \mathrm{~V})$ input; AM rejection was 61 dB at 65 dBf . Image rejection of the varactor-tuned front end was an exceptional 97 dB . Alternate channel selectivity -about 64 dB -was adequate though not especially large, and was associated with a highly asymmetrical i-f response. The asymmetry was also observed in the adjacentchannel selectivity data, which averaged 9.1 dB .

Muting threshold was $10.3 \mathrm{dBf}(1.8$ $\mu \mathrm{V}$ ). In the signal-seeking mode, it was 15.5 dBf or $3.2 \mu \mathrm{~V}$ ). Leakage of the $19-\mathrm{kHz}$ pilot was at a $-59-\mathrm{dB}$ level, and the tuner hum was a very low -75 dB . AM tuner frequency response was very restricted, about $\pm 3$ dB from 45 to $1,500 \mathrm{~Hz}$ and down 6 dB at about 2.5 kHz .

User Comment. We were favorably impressed by the fact that the SAE R9, whose performance is specified with exceptional thoroughness, met most of the ratings we checked.

The only notable exception was FM tuner response. Overall, this is not only a very fine receiver "on paper," but it lives up to its promise in use.

Unfortunately, the instruction manual falls short of the standard set by the receiver. Descriptions of the digital tuning modes were particularly inadequate.

In SCAN, it was very difficult to cover a large part of the FM band and stop on or next to the desired frequency. The tuning always seemed to overshoot the mark by a couple of channels. In this receiver, we find synthesized tuning less convenient than the old-fashioned analog variety.

The power and tuning indicators worked very well, and the multipath display was one of the best of its type we have used. Antenna orientation for minimum length of the bar display always gave good FM reception. Location of the POWER button (hidden by the volume knob) and the fact that it looks like all the other buttons on the panel, can result in some confusion.

We discovered one bothersome aspect of the R9's behavior when we switched between the AM and FM modes. There was a loud transient noise burst that could be, at best, startling, and at worst dangerous to one's speakers. Of course, if you, like many people, rarely listen to AM , the problem is not all that serious. More to the point, SAE indicates that the difficulty arises when, after the switch is made, the tuner section comes out of muting while still seeking a station. SAE advises that future production will have a slower unmuting action.

Happily, the actual operation of the SAE R9 left little to be desired. It is certainly one of the most complete receivers we have seen, able to interface with almost any other components an advanced audiophile might wish to use. FM tuner and audio performance are absoultely first rate, and the power amplifiers are impressively stable with loads that often elicit misbehavior from other units. -Julian Hirsch

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Noise and sensitivity curves for FM section of the SAE HY recelver.


Audiophile Recordings

By Harold A. Rodgera Executlve Editor

J.S. Bach: Three Keyboard Concer-tos-in $D$ minor, $B W$ W 1056, in $A$ Major, BWV 1055, in F minor, BWV 1056. András Schiff, Piano; George Malcolm conducting the English Chamber Orchestra. Denon PCM OX-7182-ND. While it has been reasonably well established by now that there is nothing musicologically incorrect about playing Bach keyboard concertos on the piano, the result, in this recording anyway, seems less than totally satisfying. It seems overwhelmingly probable that Bach would have used a harpsichord (there weren't many pianos available to him) and that he would have had that sound in his mind when composing.

The problem as I hear it is that the main formants of the harpsichord lie higher in the spectrum than those of the piano, and the tones of the former die out more rapidly. This makes the harpsichord sound more transparent and lets the accompanying instruments be heard. A few pianists, notably Gould and Tureck, manage to minimize the piano's masking tendency, but Shiff, unfortunately, does not, and thereby obscures some of the contrapuntal texture. Another possibility is that the manner in which the performance was miked made the piano too prominent, even though favoring the string sections might have been more appropriate. The unfortunate balance, whatever its cause, is the sole notable blemish on this disc. Pressing quality and recorded sound-as well as tempos, phrasing, and dynamicsseem to be right in the ballpark.

Tchalkovsky: Symphony No. 4 in $F$ minor, Op. 36. Lorin Maazel conducting the Cleveland Orchestra. Telarc 10047. This recording, digitally mastered of course, is in every way up to Telarc's usual standard of production. There is a great sense of transparency, a good hall sound, and a pressing essentially free of defects. The orchestral playing is neat and precise, though perhaps excessively so, giving the overall interpretation a somewhat a nalytic cast. One is tempted to conclude that in avoiding the lush overromanticism to which Tchaikovsky is often subject, this performance has leaned too far the other way and comes up just a bit dry, especially in
the second movement. On the other hand, the scherzo and final allegro fairly bristle with energy and contrast. Maazel apparently lets the music stand on its own structure, leaving programmatic influences well in the background

In Concert/Montreux '79. Joe Beck, Bob Brookmeyer, Jon Faddis, Richard Davis with Bingo Miki and The Inner Galaxy Orchestra of Japan. Gryphon G-913. In a note from its producer on the jacket of this album, it is billed as a collector's item, a claim that is pretty well justified if you're a lover of jazz. Considering the
restrictions on microphone placement and mixing inherent in a live recording situation, the engineers have done a phenomenal job. The pressing, unfortunately, does not seem to be up to the same standard, as the sample we received was marred by an unseemly number of minor ticks.

Musically, the album is first-rate. The Inner Galaxy Orchestra is a very pleasant exception to the tendency that many Japanese orchestras have of playing jazz just a little too neatly. The soloists do a fine job, with Jon Faddis-whose tone might be the envy of any Bach clarino player-a special standout.


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# Popular Electronics Tests 



## Panasonics PV1400 Video Cassette Recorder

THE Panasonic PV-1400 video cassette recorder (of which our sample is the first to enter the U.S.) brings with it an interesting array of features. It incorporates a 14-day programmable timer that can be set for seven programs daily. In addition, an eighth program can operate once a week at any time and will not erase itself. The clock has a 60 -minute backup battery (for power outages) that is kept on trickle charge

Other niceties include soft-touch controls (now standard in the Panasonic line) and automatic rewind at the end of record or play. There is also "cue and review," a neat little picture search system that runs at $9 \times$ normal speed. In addition, there is an automatic back-up of a few frames in the pause mode to eliminate gaps in the program. The price for the whole package is $\$ 1,295$.

The heart of all this control flexibility is a powerful little microprocessor. Each switch, when closed, signals the processor to initiate one of various actions, which are executed by integrated circuits, discrete devices, motors, servo systems, modulators, and demodulators under its control. The vocabulary of commands consists of eject, rewind, stop, fast forward, play, record, audio dub, pause, forward and reverse search, VCR/TV viewing selection, timer operations, channel selection, and power on/off; not to mention external memory on/off, counter, SP (standard play), LP (long play), and SLP (super long play) selections.

A push-to-open flap on the front conceals switches for selecting clock, normal, program, and check; day, hour, and minute; and dim/bright for the green clock readout. Beneath the flap and front apron is the remote-
control plug by which forward and reverse search, pause, and channel change can be executed. On the lower left side are video, audio, and microphone inputs, as well as a camera/ tuner switch, and the usual thumbwheel for fine adjustment of tape speed. Tiny rectangular red lamps identify all important functions on the front panel, including the dew sensor lamp and camera connection. Channel tuning and aft are under a plastic flap in the right top, and cassette load is at the left. Although this equipment doesn't have viewable fast scan or pause modes, it really does just about everything else you could want.

On the rear panel are uhf/vhf inputs and outputs, as well as video and audio out jacks, plus the usual Channel $3 / 4$ selective switching. There is an unswitched ac-line outlet that can provide up to 300 watts.
(Continved on page 32)

## Save on Scanners! NEW Rebates!

Communications Electronics," the world's largest distributor of radio scanners, celebrates Christmas early with big savings on Bearcat synthesized scanners. Electra Company, the manufacturers of Bearcat brand scanners is offering consumer rebates on their fantastic line of crystalless scanners purchased between September 15 and November 15, 1980.
We give you excellent service because CE distributes more scanners worldwide than anyone else. Our warehouse facilities are equipped to process thousands of scanner orders every week. We also export scanners to over 300 countries and military installations. Most items are in stock for quick shipment. Do your Christmas scanner shopping early and order today from CE!

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Overseas customers should order the Bearcat 250 FB Overseas customers should order the Bearcat 250 F8
at $\$ 349.00$ each. This model is like a Bearcat 250 , but designed for international operation with 220 V AC/12V DC power supply and $66-88 \mathrm{MHz}$ low band coverage insead o 32 2.50 MH2

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## NEW! Bearcat ${ }^{\circledR}$ 210XL

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## COMMUNICATIONS ELECTRONICS

## We're first with the best."'

This unit has highly wort hwhile, although not obvious, internal advantages over its predecessors. For instance, both head and tape cylinders now use direct-drive motors-for better stability and more compactnessand there are three solenoids, one for play, one for pressure roller, and another for fast forward. In the transport, a separate motor whose speed is directly changed for the LP and SLP speeds powers tape takeup. There is also a loading motor for the tape.

If a tape isn't loaded into the recorder, or if the stop tape tab is missing and there is an unattended recording program under way, the timer/ record LED will flash to indicate an error. If no program is entered, the timer/record LED will not light.

According to Panasonic, there are 10 protective sensors throughout the machine, with two of special impor-tance-a Hall-effect integrated circuit that senses the absence of rotation of the supply reel and an LED indicator and cadmium disulfide (CDS) cell that sense takeup problems and cause the transport to halt. Alse, synchrolock is maintained during que and reverse, even with noise bars in the $9 \times$ forward and $9 \times$ re-

## ". . . a deluxe piece of equipment, responsive to your slightest command."

verse speeds. Furthermore, the LP speed now has a channel through a half-line horizontal delay system that keeps the machine synclocked and picture stable during forward and reverse search. Normal servolock takes over in the SLP mode.

Lab Tests. Initially revealing tests were made on this equipment in the two modes of operation with outputs deliberately taken from two separate sources to see what, if any, differences were apparent. As usual, multiburst from a specially modifed Sencore VA48 generator was used as a signal source.

In the record mode via Ch. 4 (Fig. 1), the top trace shows VCR response through a 10:1 low-capacitance probe connected to the synchronous video detector of a full $4-\mathrm{MHz}$ broadband TV receiver. The bottom waveform is that of the video output of the player/ recorder itself. Signals enter from the source through Ch. 4 and are passed to the recorder

The top trace shows both the 3.02 and $3.56-\mathrm{MHz}$ third and fourth multiburst bars at about half amplitude or 6 dB down (voltage). In the lower
trace, directly out of the VCR's video output without modulator or tuner and TV demodulating stages, 4 MHz shows at less than 6 dB down, and the multiburst between 0.75 and 3.50 MHz maintains virtually the full amplifude.


Fig. 1. Video output (lower trace)
shows full $4-\mathrm{MHz}$ record bandwidth.


Fig. 2. In piayback mode, signal ampilitudes drop rapidly as VCR processes luminance and chroma


Fig. 3. There are substantial difierences in recording and some subtle differences diuring playback.

In the playback mode via Ch .4 (Fig. 2), signal amplitudes drop rapidly as this VCR processes luminance and chroma signals, and the end re-sult--both through the TV and pure video output-are rather less than sensational, delivering hardly more


Fig. 4. Overall response may be usable to as much as 3.5 NHFz .


Fig. 5. Grayscale tracking reveals that eight steps are somewhat erratic though horizontal
portions are fairly even.


Fig. 6. Chroma tracking tetween
TV receiver and recorder's video outout are very similar.

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the MS-650's fecture en effective combination of $f$ rat and lighe order crossover netwo-ks for a smooth. natural transion setween high anc low frequencies. 'ral's a lig difference between the crossover systerrs mos-migi-speakers use.

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than 3 MHz . Overshoots prominently identify the nonexistent $3.56-\mathrm{MHz}$ multiburst bar.
To further test this new VHS recorder we put the same signals at baseband into the video input of the PV-1400, bypassing tuners and demodulators. As you will see in the waveform photograph (Fig. 3), there are substantial differences in recording, and some subtle ones during playback. This would also hold true in record if you were using a TV monitor (without $\mathrm{r}-\mathrm{f}$ and $\mathrm{i}-\mathrm{f}$ ) as your display device instead of a television receiver. In other words, full bandpass!

When recording through the video input jack, the response in the lower
trace at the video output is a maximum 4.08 MHz with no loss of amplitude and excellent staircase linearity. This amounts to little more than straight channel passage through the PV-1400. (In the upper trace, you'll see some dropoff in the amplitudes of this multiburst information through the TV, but most of it's there, and the receiver has proven its $4-\mathrm{MHz}$ capability at the video detector.)

In playback through the video jack (Fig. 4), overall response does extend beyond 3.02 MHz and may be usable to as much as 3.5 MHz . This suggests that video inputs and outputs offer considerably greater bandpass than the various tuners, i-f strips, and mod-
ulator/demodulators. This lesson should not be lost on those who want best resolution and definition in the picture, although the operator would have to use a good camera or some other straight video wideband device to take advantage of it. The upper trace shows a signal that has passed through the TV receiver.

As for grayscale tracking (Fig. 5), the eight steps of the B\&K-Precision NTSC generator, which are supposed to be identical in amplitude, are actually somewhat erratic. All horizontal step portions are pretty much the same, however, even in the video output (lower trace). The receiver waveform at the top dutifully follows all


The tuner section supplies i-f amplifiers and video detector with broadcast information. Sync is taken off after the video input for any camera-pure composite video (baseband) that might be used for servosystem countdown. Video and chroma are converted to frequency modulation for the video output, the $\mathrm{Ch} .3 / 4 \mathrm{r}$ - f converter, and/or the recording amplifier and heads Heads $A$ and $B$ are rotated at a constant 1800 rpm by the direct-drive cylinder motor, while the tape is moved at standard or long-play speed by the capstan motor.

Playback uses the same heads as does record. Head outputs are amplified and switched alternately depending on which is in contact with the tape. Video is recon-
verted from FM to AM, with chroma heterodyned from 629 kHz to 3.58 MHz once again. Taped results can be viewed through any slave television receiver on Ch. 3 or 4, or through the VCR's video output at baseband. If this is a re-recording. the 65- (or 67-) kHz erase head aligns the tape's magnetic field before the new re cording and electronically wipes it clean

There are various capstan control count downs for standard and long play, with servo control feedback and frequency comparison. During operation, $60 \cdot \mathrm{~Hz}$ vertical sync pulses are divided in half so that 30Hz control pulses reach the control head and are recorded on the tape's lateral control track. The stop solenoid actually re-
moves some dc power from the machine and stops it completely when ac power is interrupted or when the interaction of two or more events could cause damage.

When this occurs, all front-panel controls are reset to off. The PV-1400, has four heads instead of two. The larger pair gives better performance at the slower speeds, adequately covering the two diagonal tape tracks that represent two 262.5-line fields of information and constitute a single frame. There are 30 frames recorded or played each second

Audio is processed in standard TV FM, with the same $65-67-\mathrm{kHz}$ frequency that erases previous magnetic video and audio recordings, providing recording bias.

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the video-signal discrepancies of the lower trace.

In analyzing such distortion, remember that the grayscale here begins at blanking, next to the horizontal sync pulse, and extends upward to peak white until it reaches full-range modulation, or 100 IRE (Institute of Radio Engineers) units. Nonlinear staircases originate from quadrature distortion, itself caused by nonlinear diode-type envelope video detectors. These often generate color-subcarrier second harmonics, as well as errors in differential gain. The latter will produce, in aggravated instances, crossmodulation between chroma and luminance, usually seen as a modulated staircase. Normally, it is a product of apl, or average picture level, that relates principally to the brightness of each scene, and often results in actual image displacement. Here, the problems are not severe, but they do have a modest effect on the video output and overall chroma information.

Chroma tracking (Fig. 6) at the receiver (top trace) and from the player/recorder's video output (bottom trace) are very similar in most respects with a fairly squared-off horizontal sync pulse (denoting no lowfrequency rolloff) and a sort of eggshaped burst riding on the back porch of the adjoining horizontal blanking

## MODEL PV-1400 VITAL STATISTICS

pulse. Note, however, that all six color bars from both sources appear somewhat indistinct, without well-defined edges and normal impulse separation. This is due to traditional noise found in many VCRs and contributes to picture degradation. The cause could be poor tape, but more likely it arises in the electronics of the playback mechanism that restore chroma from its "beatdown" narrowband $629-\mathrm{kHz}$ condition to the usual $3.58-\mathrm{MHz}$ center frequency for video and receiver outputs.

Comments. Is this a good machine? Considering the constant evolution in video cassette recorders, of course it is! You don't have a somewhat imperfect stop frame for individ-
ual picture viewing and some other fancy gee-gaws, but there is relief from mechanical switches, a clock that keeps time during power outages, lights to tell what's going on and why, and, overall, a convenient and deluxe piece of equipment, responsive to your slightest command.

Furthermore, its appearance is more than attractive, and the silverwalnut coloring will match many of the television receivers on the market this Fall. Do you actually need 14 days of recording time? Perhaps not now, but in the future when you're taping instructions or shopping information from the cable or late-night broadcasts, this large time span might become quite handy. -Stan Prentiss

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|  | Tape speeds and recording times: |  | 2 hr at $1.33 \mathrm{in} . / \mathrm{s}$ 4 hr at $0.66 \mathrm{in} . / \mathrm{s}$ 6 hr at $0.44 \mathrm{in} . / \mathrm{s}$ |
| :---: | :---: | :---: | :---: |
|  | Ac power requirements: <br> Video bandpass ( -3 dB ): |  | 42 W (record) 45 W (play) <br> 3 MHz (average) |
| $x_{i}$ | Audio bandpass ( -3 dB ): <br> Weight: <br> Dimensions: |  | ```100 to 7000 Hz 27 lb 19"*}\times1\mp@subsup{4}{}{\prime\prime}\times5\mp@subsup{5}{}{\prime\prime``` |

Notes: Plays and records at power inputs between 105 and 130 volts ac.

[^2]
# Radio Shack Shows Surprises 

$\mathbf{F}^{2}$OR more than a year now, a number of industry pundits have conjectured about what the Fort Worth, TX-based Radio Shack would introduce as its next generation microcomputer system. Everything from the mythical TRS-90, which was to be a machine based on 16-bit technology, to a machine costing less than $\$ 500$ was conjectured. The less-than- $\$ 500$ machine was introduced (PE Oct 1980) in the form of a timesharing terminal. It has, however, been reintroduced as a color system called the TRS-80 Color computer.

This unit is the basic Videotex ${ }^{\text {(1) }}$ machine I discussed last month, and has application cartridges that add value in the form of games or useful household programs. Yes, the unit is programmable, too, and has a version of Microsoft BASIC written specifically for the MC-6809 microprocessor. The TRS-80 Color computer, is aimed at the low-end user market.
mable. As an added feature, one can purchase a cassette interface for $\$ 49$ and either load software tapes from Radio Shack or make your own. Initially, Radio Shack is offering eight application tapes ranging from real estate to personal finance. The software tapes are priced from $\$ 14.95$ for a math drill to $\$ 24.95$ for the engineering packages.

Those of you who are expecting the demise of the TRS-80 Model I can guess again. The original Model I is still available and being manufactured! However, owing to FCC requirements, it is being carefully evaluated for continued manufacture, though it will be supported for as long as necessary. The unit that is designed to supplant the Model I is the Modell III. This model is basically the same computer as Model 1, but with some twists in the packaging.

The Model III is a Z80 system with integrated keyboard, CRT, memory


Left to right are Radio Shack's TRS-80 Color Computer,
the hand-held Pocket Computer, and the TRS-8O Model III.

The color unit isn't all that Radio Shack is offering for the early 80 s though. Supporting the idea of computers for the consumer, the Fort Worth marketers have introduced the TRS-80 Pocket Computer. This $\$ 249$ hand-held unit, uses a liquid-crystal 24-character display, incorporates a Level I-type BASIC, and is program-
and expansion facilities. This design upgrades from the add-on bailing wire structure of the Model I. The Model III is base-priced at $\$ 699$ for a 4 K RAM, Level I BASIC system. A fullblown, 32 K RAM, double-density ( 175 K bytes per disk) dual integrated disks, and Model III BASIC goes for \$2495. The Model III has the same
display format $(64 \times 16)$ as the Model I, and is designed for any Model I software (using a conversion program). Radio Shack officials see Model III as the ideal machine for the small office that needs strong computing capability for less than $\$ 5000$.

Even though Radio Shack has broadened its computer market with these introductions, it is also strongly supporting the Model II introduced last year. To enhance the Model II and the other units, they have developed, in conjunction with other unnamed companies, printers that support everything from general data processing to plotting.

The plotting printer is possibly the most exciting entrant. The printer/ plotter is priced at $\$ 1460$ and is designed to interface directly to either the Models I, II, or III computer systems. The plotter/printer uses a pen system, thus eliminating ribbons, and prints caps and lower case.

North Below the Border. Recently, I had the good fortune to visit the magic makers at Microsoft in Bellevue, Washington. Vern Raburn squired me around the growing, yet crowded headquarters of the dynamic software house. He let me view some new developments they are working on, specifically on the system side of the house. That's where they build BASIC, FORTRAN, and other highlevel languages. One product, UNIX, a very high-level operating system from Bell Labs, is being revised for microcomputers by Microsoft and adapted to their product structure. This is an extremely powerful $\mathrm{O} / \mathrm{S}$ and might change the whole picture of microcomputer data processing in the very near future.

Vern is president of the Consumer Products Division of Microsoft, and as such is responsible for the development and marketing of application software and products such as the Z80 card I talked about in the August issue. The application software products sold by the division include items like a typing tutor, an adventure game, and an editor. Vern explained that most of the products are being developed for the Apple and TRS-80, with some plans to support units like the Heath H8 and H89.

Color Next Important Step. While I was visiting Microsoft, I attended the SIGGRAPH conference held in Seattle. This is where manufacturers of graphics machines show their wares. This year color was the name of the game, with companies demonstrating terminals with as many as 4000 displayable hues.

Now these aren't the machines you buy for the home; but they are used by companies like Boeing for computeraided design, and range in price from $\$ 20,000$ on up.
Apple was there in full force, dem- gets you up and running the very first night with your own TV for a video display. 599.95 ELF 1 includes RCA 18028 -bil microprocessor addressable to 64 k bytes with OMA, interrupt, 16 registers, ALU. 256 byte RAM, full hex keyboard. two digit hex output display, stable crystal clock for timing purposes. RCA 186 video IC to display your programs on any video monitor or TV screen and 5 -sfot plug in expansion bus (less connectors) to expand ELF II into a giant' ELF II Explodes Into A Giant!
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## Computer Terminal <br> comat $5149^{95}$

The Netronics ASCII/BA UDOT Computer Terminal Kit is a microprocessor-controlled, stand alone keyboard/terminal requring no computer memory or software. It allows the use of either a 64 or 32 character by 16 line professional display for mat with selectable baud rate, RS232-C or 20 ma . output, fult cursor control and 75 ohm composite video output.
The keyboard follows the standard typewriter configuration and generates the entire 128 character ASCl1 upper/lower case set with 96 printable characters. Features include onboard regulators, selectable parity, shift lock key, alpha lock jumper a drive capability of one TTY load, and the ability to mate directly with almost any computer, including the new Ex plorer/85 and ELF products by Netronics.

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The heart of the Netronics Computer Terminal is the micro-processor-controlled Netronics Video Display Board (VID) processor-controlled allows the terminal to utilize either a parallel ASCII or WAUDOT signal source. The VID converts the parallel data to serial data which is then formatted to either RS232-C or 20 ma. current loop output, which can be connected to the serial $1 / 0$ on your computer or other interface, i.e., Modem.
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 $R S T U V W X Y Z-?: 3 \$ \#() .9014!57: 2168 \cdot$Cursor Modes: Home, Backspace, Horizontal Tab, Line Feed, Vertical Tab, Carriage Return. Twe special cursor sequences are provided for absolute and relative $X$ - $Y$ cursor addressing Cursor Control: Erase, End of Line, Erase of Screen, Form Feed, Delete - Monitor Operation: 50 or 60 Hz (jumper selectable.

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plus $\$ 3$ postage \& handling.
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onstrating that they have color and graphics capability in the same plane as many of the "high-end" manufacturers. Reinforcing the strength of Apple was ABW Corp. of Ann Arbor, MI. This company demonstrated a device called Teksime which is a read-only-memory (ROM) that plugs into the Apple computer. The $\$ 475$ device makes use of the Apple's highresolution plotting capabilities to emulate a Tektronix 4010-type graphics terminal, but with color.

To place this in perspective for you,
the Tektronix line of graphics terminals are those that virtually every company tries to equal. The Tek terminals are of the monochrome type, but exhibit various degrees of gray scale and high resolutions. Consequently, a device that permits emulation of a Tex terminal, on a raster scan system such as an Apple, is definitely an achievement.

There is a caveat, though. The device is only valuable if you can use it. I don't recommend buying the Teksim just to buy it, but if you are doing


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[^3]Novation, Inc., 18664 Oxnard Street, Tarzana, California 91356
computer graphics on the Apple, and need the enhanced functions that Teksim provides, then by all means add it to your system.

Following this philosophy of purposeful products is Godbout electronics. Bill's latest entry into the S-100 world is called Spectrum ${ }^{\text {B }}$. It is a board designed to give you color graphics capability for less than $\$ 400$. It can fit into any memory location you have available and delivers composite NTSC video. Although Godbout doesn't tout it, the board is capable of 2D transforms and simulated motion. Bill sees the Spectrum as an ideal board for use in process-control systems-specifically those controlled by an S-100 bus computer system.

## MORE INFORMATION

For additional information about products and services mentioned here, contact the companies directly.

## ABW Corporation

Box M1047
Ann Arbor, Ml 48106
313-97 1-9364

Dave Nutting Assoc.
527 West Golf Rd.
Arlington Hts, IL 60005
312-956-0710

Godbout Electronics
Bidg. 725, Oakland Airport
Oakland, CA 94614
415-562-0636

Microsoft, Consumer Products Div. 10800 NE 8th St., Suite 507
Bellevue, WA 98004
204-454-1315

Radio Shack
1800 One Tandy Center
Box 17180
Forth Worth, TX 76102
817-390-3700

I was under the impression that the Bally microcomputer system was no longer in existence, but at SIGGRAPH, Dave Nutting Assoc. was demonstrating a Bally with their Grafix image editor. This software was powerful and worked well with digitizer tablets and light pens. Nutting did have a machine called the UV1 ZGrass University Machine that is Z80 based and can work in a timesharing environment. They quote a price of $\$ 2395$, but like many machines of this type it all depends on options. I don't know the price of the Bally or even if it was for sale. If you're interested, you might contact the company directly.

## Record Care, Part 1: Aqueous Cleaning vs. Organic Solvents

Electron microscopy (Figure 1) shows the principal cause of record wear: small particles of microdust, deposited from the air by gravity, are ground along the record groove by the stylus. Surface noise goes up. Sound quality goes down.


Figure 1


Figure 2
Figure 2 shows a drop of the aqueous Discwasher D4 Fluid, literally lifting dust and contamination out of record grooves. The extraordinarily complex D4 Fluid uses water pure enough for kidney dialysis, along with eleven chemically engineered additives that still results in lower dry-weight residue than most tap water. This formula is amazingly high in cleaning activity, uniquely safe for vinyl and vinyl additives, and preferentially "carries" contamination into the new Discwasher D4 pad

In some record care products, organic solvents are used rather than water. Organic solvents such as ozone-gobbling chlorofluorocarbons, petroleum distillates (hexane, heptane) and alcohol concentrates are indeed speedy extractors and delivery solvents. They evaporate fast. Some organic solvents can dissolve vinyl stabilizers, Organic solvents may leave a "slick" looking record by treating the disc with other compounds carried in the solvent mix. In doing so, record contamination may also be dried back onto the disc in a nice even layer. Dust is often "held" to the record surface by "treatment".


Figure 3
Electron micrograph (Figure 3) shows a record cleaned with the Discwasher D4 System. High technology record care leaves only a clean surface.

discwasher ${ }^{\text {o }}$
Discwasher, Inc., 1407 N. Providence Rd., Columbia, MO 65201


## Hardware

Apple Music. The MusicSystem is designed for Apple II computers and produces 16 voices including the generation of the sounds of real musical

instruments. The accompanying Editor program permits graphical input of sheet music using standard music notation. The Player utility permits polyphonic performance of musical compositions. Stereo output is provided. Address: Mountain Computer Inc., 300 Harvey West Blvd., Santa Cruz, CA 95060 (Tel: 408-4298600).

S-100 APU. The MemTech Arithmetic Processing Unit interface features floating-point multiply performed in 56 microseconds, 41 trig and exponential functions, and automatic floating point normalization and error codes. It is jumperable as any pair of I/O ports on an S-100 bus. It provides a considerable speedup for both FORTRAN and BASIC. $\$ 390$ (multiply in $84 \mu \mathrm{~s}$ ), $\$ 460$ (multiply in $56 \mu$ s). Address: MemTech Co., 4891 Clairemont Mesa Blvd., San Diego, CA 92117 (Tel: 714-292-1219).

Cryptograph. The Cryptographic Primer Kit includes an RS-232 Interface Cryptographic Board that attaches to the RS-232 port of a computer and accepts 300 baud data. The user inputs the 64-bit cryptographic key from the terminal. The board contains the WD20001F implementation of the NBS Data Encryption Standard. A Cryptographic Primer that describes the software implementaion, an assembly manual, and the
book "Codebreakers" by David Kahn are included with the kit. $\$ 395$. Address: Western Digital, 3128 Redhill Ave., Newport Beach, CA 92663 (Tel: 714-557-3550).

Aim-65 Enclosure. Made from high-strength ABS plastic, this enclosure for the AIM-65 computer comes with mounting hardware, wire, and switches, and is ready for assembly. All parts are precut and drilled. Room is left for two additional boards. $\$ 49.95$ plus $\$ 2.50$ shipping/handling. Address: Don-El Enterprises, 3261 Michigan Ave., Costa Mesa, CA 92626 (Tel: 714-546-7481).

SS-50 Double Density. The DCB-4 Disk Master is an SS-50 bus double-density controller board that allows 336 K bytes of storage on a single minifloppy, and is capable of handling up to four $51 / 4^{\prime \prime}$ or $8^{\prime \prime}$ drives simultaneously. It includes DOS-68B or 69 D , is fully compatible with harddisk systems, and allows users to select either single- or double-sided operation, single or double density data, stepping rate, 35 - or 40 -track operation, etc. It occupies only 16 bytes of memory space and can read/write a single sector by itself. An on-board buffer allows full interrupt capability and once data has been initiated, no further processor time is required. $\$ 449$. Address: Smoke Signal Broadcasting, 31336 Via Colinas, Westlake Village, CA 91361 (Tel: 213-8899340).

TRS-80 to 488 Bus. The Model 488-80B Interface Adaptor allows any TRS- 80 Model I having 16 K of RAM and level 2 BASIC to be used as a GPIB-488 controller. The ma-chine-language program provided with the device interacts with Level 2, Level 3, or disk BASIC. $\$ 225$. Address: Scientific Engineering Labs., 11 Neil Drive, Old Bethpage, NY 11804 (Tel: 516-694-3205).

S-100 I/O Ports. The Model 4P4S Parallel-Serial I/O board combines four parallel bi-directional data ports with full handshaking and interrupt control. In addition, there are four serial RS-232 I/O ports and room for prototyping. The board is mapped as 16 consecutive I/O ports and will work with any $\mathrm{Z} 80,8080,6800,6802$, 6809, 6502, or 8085 IEEE standard S-100 products. $\$ 199$. Address: MicroDaSys, Box 36215, Los Angeles, CA 90036 (Tel: 213-731-0876).

WH89 CP/M. This plug-in modification for the Heath Z89/WH89 makes it possible to use CP/M as well as HDOS. Up to two additional disk drives can be added to the modified computer allowing a total capacity of 306 K of storage. The modification kit
with CP/M 1.4 Operating System and documentation is $\$ 249$. Address: Magnolia Microsystems, 2812 Thorndyke Ave. West, Seattle, WA 98199 (Tel: 206-285-7266).

Apple Light Pen. The Lipson Light Pen connects to an Apple II game paddle connector and uses a cadmium-selenide cell for light detection. This allows the measurement of varying intensities of light. Besides a 48-page manual, a number of integer and Applesoft BASIC programs are included to make use of the high-resolution graphics, sound, and color. $\$ 24.95$. Progressive Software, POB 273, Plymouth Mtg., PA 19462.

Atari Printer Interface. This Parallel Printer Interface can be used with the Atari 400 or 800 computers and connects via the front-panel controller jacks. The board is small enough so that it does not interfere with normal keyboard operations and a three-foot ribbon cable connects to the printer. A short machine language program directs all LPRINT outputs to the printer interface. The interface works with BASIC, DOS, and Assembler/Debug. \$69.95. Address: Macrotronics Inc., 1125 Golden State Blvd., Suite G, Turlock, CA 95380 (Tel: 209-667-2888).
$\mathbf{5 5 5 0} \mathbf{1 / O}$ Ports. The GIMIX 2 Port Serial ISO board has two independent RS-232 compatible I/O ports with handshaking on a single 30 -pin board. It features independent baud

rate and interrupt for each port and uses the 6850 ACIA. The board is compatible with both the SS50 (4 addresses per slot) and the SS50C ( 16 addresses per slot). $\$ 128.43$ less cables. The GIMIX 8 Port Serial I/O board has eight independent RS-232 ports with handshaking on a single 50 -pin board. It features selectable baud rate for each port, extended address decoding for SSSOC, selectable interrupts, and also uses the 6850 ACIA. \$318.46. Address: GIMIX Inc., 1337 W. 37th Pl., Chicago, IL 60609 (Tel: 312-927-5510).

PROM Programmer. The PP-2532 TMS 2532 EPROM programmer is a complete programming package that connects, via a 4 -foot ribbon cable, to


# Time \& Money. Commodore, Atari \& Apple users get more with VisiCalc" software. 

A financial VP in Massachusetts is cutting the time it takes to prepare month-end reports from three days to three hours.

A California company is replacing most of its time-share computer service with a personal computer and VisiCalc, saving at least $\$ 30,000$ the first year.

Thousands of other personal computer users are also sold on how VisiCalc is increasing their productivity. Besides saving time and money, they're simplifying their work and getting more information that helps them make better decisions. A typical user reaction comes from a New York dentist:
"VisiCalc has become an integral part of my business."
VisiCalc displays an "electronic worksheet" that automatically calculates nearly any number problem in finance, business management, marketing, sales, engineering and other areas. The huge worksheet is like a blank ledger sheet or matrix. You input problems by typing in titles, headings and your numbers. Where you need calculations, type in simple formulas $(+,-, \times, \div)$ or insert built-in functions such as net present value and averaging. As quickly as you type it in, VisiCalc calculates and displays the results.
"I am extremely impressed with VisiCalc's capability, flexibility and orderly presentation of instructions.'

So writes the director of a New York corporation. He appreciates VisiCalc's powerful recalculation feature. Change any number in your model and instantly all numbers affected by that change are recalculated and new results are displayed. You can ask "What if . . ?", analyzing
more alternatives and forecasting more outcomes. It really increases your decision-making batting average!

Wher you finish, you can print a copy of the worksheet just as it appears on the screen and/or save it on diskette.
"I like VisiCalc's ease of use."
That response comes from a Utah businessman using VisiCalc for production forecasts, financial report ratio analysis and job cost estimating. Ease of use is VisiCalc's best-liked feature. It's designed for a non-programmer, and has an extensive, easy-to-understand instruction manual.

Users also like solving a wide variety of problems with VisiCalc . . . and solving them their way. VisiCalc can even justify the cost of a personal computer, according to a New Hampshire financial analyst:

## "VisiCalc is paying for itself over and over."

VisiCalc is available for 32 k Commodore PET/CBM, Atari 800 and Apple disk systems. VisiCalc is written by Software A.ts, Inc.

See VisiCalc at your Personal Software dealer. For your dealer's name, call Personal Software Inc. at 408-745-7841, or write 1330 Bordeaux Drive, Sunnyvale, CA 94086

While there, see our other Productivity Series software: Desktop Plan and CCA Data Management System. They're like time on your hands and

any read-only PROM socket. Data is sent via the eight lower address lines to the programmer. No additional power supplies are required, and all timing and control sequences are handled by the programmer. Each programmer comes with an internal dc-to-dc switching regulator and a zero-insertion-force socket. $\$ 295$. Address: Oliver Advanced Engineering, Inc., 676 West Wilson Ave., Glendale, CA 91203 (Tel: 213-240-0080).

## Software

Statistical Analysis for TRS80. For Level II or disk BASIC, this package consists of 13 programs. Ten describe data sets and conduct statistical data analysis, two are utility programs for preparing, updating, and listing data files, and one aids in the selection of data samples. Program names include Tape Data Files, Disk Data Files, Random Sample, Descriptive Statistics, Histogram, Frequency Distribution, Analysis of Variance, TTest for Matched Pairs, Correlation and Linear Regression, Multiple Linear Regression, Time Series Analysis (two), and Chi Squared Analysis. Available at Radio Shack Computer Stores for $\$ 39.95$.

BASIC Precompiler. EZ-CODER is a BASIC precompiler that can be used with a North Star system. It features a self-documenting BASIC, allows mnemonic variables of arbitrary length, and tables for line references. Complete cross-referencing of all variables, labels, and user-defined functions are permitted. Source files are created using a built-in text editor. The precompiler produces North Star compatible code for use with North Star BASIC. \$79. Address: Demerco Industries, Box 2396, Van Nuys, CA 91404.

Altair CP/M. Used by the Altair 8800 and MITS 3202 disk systems, this approach takes full advantage of the 300 K plus capacity per diskette with no changes to the hardware. The use of CP/M allows languages such as C, COBOL, FORTRAN, PASCAL, etc., to be run immediately. Address: Lifeboat Associates, 2248 Broadway, New York, NY 10024 (Tel: 212-580-0082).

Extended 280 Operations. The PDS Z80/8080 assembly language development system has been extended to include an additional 96 Z80 operations heretofore undisclosed. These implement four additional byte registers by allowing access to the two halves of the index registers. The PDS includes a relocating
macro assembler, interactive editor/ assembler, trace debug/monitor, text editor, linkage editor, and a relocating loader. $\$ 99$ for North Star, or CP/M on 8 -inch soft sector or 5 -inch 10 - or 16-hard-sector diskette. Address: Allen Ashley, 395 Sierra Madre Villa, Pasadena, CA 91107 (Tel: 213-793. 5748).

Language Teacher. Featuring French, Spanish, German and Italian, the Language Teacher operates from a TRS-80 Model I disk system. The drill-learning format features lan-guage-to-English and vice versa and offers hundreds of word combinations, phrases and verb conjugation forms to challenge the student. It also allows print-out of multiple-choice tests and has a full quiz diagnostic routine. $\$ 19.95$. Address: Acorn Software Products Inc., 634 North Carolina Ave., S.E., Washington, DC 20003 (Tel: 202-544-4259).

Flight simulation. The A2-FS1 (Apple II) and T80-FS1 (TRS-80) are visual flight simulators that offer a real-time, 3D, "out of the window" view of the flight. The view is updated

three times per second. The viewed panel contains all the instruments required under Part 91 of the FAA Regulations for visual flight. It also includes a stall warning, turn indicator, radar screen, ammo indicator, and control position indicators. Other (keyboard) controls include throttle, brakes, bomb drop, machine guns, high-low world, and declare war. An airborne battle game is included. Requires 16 K of RAM. Cassette is $\$ 25$, diskette is $\$ 33.50$. Address: Sub Logic, Box V, Savoy, IL 61874 (Tel: 217-359-8482).

6800/6809 DOS. The FLEX Disk Operating System for 6800 and 6809 systems supports such features as dynamic filespace allocation, random and sequential file accessing, user startup facility, user environment control, English error messages, and over 20 disk operation commands. It requires a 256 -byte soft-sectored diskette, and includes an editor and assembler. \$150. Address: Technical Systems Consultants, Inc., Box 2570 , West Lafayette, IN 47906 (Tel: $317-$ 463-2502).

Accounts Recelvable. ACCTM2 for the TRS-80 consists of five programs for almost any small business. The user can specify parameters such as company name and address, late charge policy, etc. It can sort customers by name, and you can add, inquiry, delete, and adjust transactions and customer information. Transaction can be paid or unpaid invoice, credit, debit or payment. Subcommands allow search, display, print, and updating of records. Order entry allows multiple items to be entered and an invoice can be printed if desired. Many other customer related functions are also included. Special features include form input, live keyboard, double-precision arithmetic, fast SHELL sort, audit log, error trapping formatted date and numeric output, I/.O buffering and blocking and a number of management options. \$149. Address: Micro Architect, 96 Dothan St., Arlington, MA 02174.

British PASCAL. Available on IBM 3740 format $5^{\prime \prime}$ or $8^{\prime \prime}$ diskettes to run under $\mathbf{C P} / \mathrm{M}$, this is a full standard PASCAL, and uses as little as 32 K to run. With linker and relocatable files, the package can handle complex problems. This PASCAL was developed in Great Britain. Address: Transam Components Ltd., 12 Chapel St., London, NW1 5DH, Eng.

Utilities. DISK FIX is a generalpurpose disk utility for MITS/Pertec disks that allows any sector of an unmounted diskette to be examined, edited or rewritten. It will copy an entire diskette in less than two minutes. Check sums for each track assure data reliability. It requires an $8080,32 \mathrm{~K}$ of memory, floppy-disk drive (two drives for DISKCOPY). \$95. SELECT/MWP provides for copying selected records from a miniword processing name-and-address file to a new file as determined by the operator. Requires an 8080/Z80 with 48 K and disk. Uses CP/M. \$95. TX/ RX (transmit-receive) provides com-puter-to-computer communication for 8080/Z80 machines at up to 19,200 baud. The receiver is capable of continuous unattended operation and the transmitter operates in the batch mode. Uses CP/M. \$195. Address: The Software Store, 706 Chippewa Square, Marquette, MI 49855 (Tel: 906-228-7622).

O81 Sort. BPSort is a high-speed disk file sort utility for Ohio Scientific OS-65U operating system. It is written in assembly language and uses a Shell-Metzner sorting algorithm. \$124. Address: BPS, 322 W. 57th St., New York, NY 10019 (Tel: 212-7650815).

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# PHONDERPPH PMPBPRK Its' better than you think! 

The vinyl disc, being seriously challenged by new technologies, is still a strong competitor

BY THE DISCWASHER RESEARCH STAFF*

THE vinyl-disc playback system (VPS)—the phonograph record and the components used to manufacture and play it-stands challenged by newer technologies. Therefore, understanding its strengths and weaknesses looms great in importance. How good is the system? How long can the disc be expected to last? How can its original quality be preserved? Can the overall system be significantly improved?

On the basis of research findings so far, the VPS can be said to have the ability to store and redeliver on home high-fidelity equipment with a quality surpassing that of all other systems extant or projected at this time-including digital. For example, certain wellpressed audiophile discs have noise floors of about -76 dB re RIAA zero

[^4]VU . Adding to this the +12 dB or so headroom that most high-quality phono cartridges can successfully track gives a peak $\mathrm{S} / \mathrm{N}$ ratio of 88 dB . Not only does this exceed the $84-85 \mathrm{~dB}$ claimed by a projected 14 -bit digital disc playback system, it rivals the 90 dB claimed for digital tape systems.

A normal European pressing was found to contain recoverable modulations on the order of 0.434 nanometer, about $1 / 450$ the wavelength of violet light. The recorded signal, in this case, was the 19th harmonic of a $900-\mathrm{Hz}$ violin tone ( 17.1 kHz ), at about -70 dB . A normally produced disc of U.S. manufacture was found to have a residual noise level of -68 to 70 dB , for a total dynamic range of about 80 dB .

It may seem surprising that a stylus can sense such fine low-amplitude modulation when its very passage through the groove deforms the vinyl by a much
greater amount, typically 250 nanometers. Perhaps even more surprisingly, the modulation is durable and withstands many passes of the stylus. But vinyl is, in fact, quite elastic and readily returns to its original shape, provided that the contact pressure is not too great. The stylus is in much the same situation as the legendary princess who could feel a pea through a thick layer of mattresses.

Interestingly, a digital recorder does not do a very precise job of capturing low-level signals either. A 16 -bit system with a $50-\mathrm{kHz}$ sampling rate would catch slightly less than 3 samples per cycle of the $17.1-\mathrm{kHz}$ violin harmonic mentioned earlier. Because of its low level, the tone would be encoded to only a 3-bit accuracy, placing it perilously close to the level of quantizing noise, which has a far more annoying spectrum than ordinary white noise.

Bandwidth is another area in which

# fact: <br> five new Shure Cartridges feature the technological breakthroughs of the V15 Type IV 



## the M97 Era IV Series phono cartridges

| Model | Stylus Configuration | Tip Tracking Force | Applications |
| :---: | :---: | :---: | :---: |
| M97HE | Nude Hyperelliptical | $3 / 4$ to $11 / 2$ grams | Highest fidelity where light tracking forces are essential. |
| M97ED | Nude Biradial (Elliptical) | $3 / 4$ to $1 / 2$ grams |  |
| M97GD | Nude Spherical | $3 / 4$ to $11 / 2$ grams |  |
| M97EJ | Biradial (Elliptical) | $11 / 2$ to 3 grams | Where slightly heavier tracking forces are required. |
| M97B | Spherical | $1 / 2$ to 3 grams |  |
| 78 rpm Stylus for all M97's | Biradial (Elliptical) | $11 / 2$ to 3 grams | For 78 rpm records. |

Shure has written a new chapter in the history of affordable hi-fi by making the space-age technological breakthroughs of the incomparable V15 Type IV available in a complete line of high-performance, moderately-priced cartridges: the M97 Era IV Series Phono Cartridges, available with five different interchangeable stylus configurations to fit every system and every budget.

The critically acclaimed V15 Type IV is the cartridge that astonished audiophiles with such vanguard features as the Dynamic Stabilizer-which simultaneousty overcomes record-warp caused problems, provides electrostatic neutralization of the record surface, and effectively removes dust and lint from the record-and, the unique telescoped stylus assembly which results in lower effective stylus mass and dramatically improved trackability.
Each of these features . . . and more... has been incorporated in the five cartridges in the M97 Series-there is even an M97 cartridge that offers the low distortion Hyperelliptical stylus! What's more, every M97 cartridge features a unique lateral deflection assembly, called the SIDE-GUARD, which responds to side thrusts on the stylus by withdrawing the entire stylus shank and tip safely into the stylus housing before it can bend.

## NEW! M97 Series Era IV Phono Cartridges...

 Five new invitations to the new era in hi-fi.

Shure Brothers Inc., 222 Hartrey Ave., Evanston, IL 60204 in Canada: A. C. Simmonds \& Sons Limited Outside the U.S. or Canada, write to Shure Brothers Inc., Attn: Dept. J6 for information on your local Shure distributor. Manufacturers of high fidelity components, microphones, sound systems and related circuitry.
the VPS excels. Digital systems are rigidly limited to signals whose frequencies are less that one-half the sampling rate. At present, the maximum sampling rate that can be provided at reasonable cost appears to be 50 kHz . This limits practical signal bandwidth to about 22 kHz or less, depending on how complicated a low-pass filter is used. The VPS, on the other hand, is known to possess a bandwidth on the order of 45 kHz . While few human beings can hear anything above 15 to 25 kHz , depending on the individual, there is some evidence that trained listeners sense the absence of such frequencies if the audition period is sufficiently long. Studies showing that listeners do not miss anything above 15 kHz have used relatively short auditions.

Weakness of the VPS. Like any in-formation-storage system, the VPS can allow errors to occur. First, it is not certain the groove cut into the master disc will correspond exactly to the information to be preserved. Second, the modulation cut into the master may not be accurately duplicated through all the manufacturing stages leading to the final pressing. Third, the playback equipment may misbehave and not trace the modulation accurately, or, if it accomplishes that successfully, fail to translate it into a correct electric signal. In general, there are more of the errors of the third category.

Playback discrepancies can be perceived as changes in distortion and also as losses of low-level signals. According to previous literature, playback problems can be attributed to tracing distortion, poor stylus scanning radius, incor-


Fig. 1. Oscilloscope trace (above) of cartridge output from a $1-\mathrm{kHz}$ square-wave band of a CBS STR- 112 test record. 'Ringing' at 41 kHz is evident. Fig. 2. (right) shows an electron micrograph of the test record groove. Note how the ripples in the groove correspond to the "ringing" signal observed in Fig. 1.
rect stylus rake angle (SRA) or vertical tracking angle (VTA), poor stylus/ groove interface, and characteristic electromechanical properties of a particular cartridge, such as resonances.

Problems of the playback equipment are usually evaluated by means of test records containing known signals that can be examined via distortion analysis or an oscilloscope after they have been played by the cartridge. It is extremely important to know whether any given cartridge output corresponds to the recorded modulation or is an artifact of the playback process. Research has shown that test records often suffer from inaccuracies in frequency response. (See Stereo Scene, December, 1979.) It was decided, therefore, to "fingerprint" the various test records by analyzing the high-frequency spectra of recorded sine and square waves.


Fig. 3. Varying conditions of contact between stylus and record groove. In soft mistrack (B), stylus force is unevenly distributed between groove walls.


Fig. 1 shows an oscilloscope trace of a cartridge playing a $1-\mathrm{kHz}$ square wave from a CBS STR-112 test record. The trace shows a disruption at 41 kHz , consisting of a characteristic "ringing" that many magazine reviewers have attributed to resonances in cartridges they have tested over the past 10 years. To examine this hypothesis, the disc was played at a speed different from the standard $331 / 3 \mathrm{rpm}$. If the ringing were an artifact of the cartridge, its frequency would remain fixed; if recorded onto the disc, its frequency would change in proportion to the difference in speed.

As the frequency did change, it was concluded that the "ringing" was cut into the disc and the cartridge that reproduced it was actually outperforming one that did not. Subsequent examination of the groove walls by electron microscopy showed the ringing signal clearly. Note how the ripples at the beginning and end of the flat portion of the square wave shown in Fig. 2 correspond to the large displacements in the oscilloscope trace. This ringing is a characteristic of the Westrex lathe cutting system used in the production of the CBS STR112 test record! Test records of other manufacture with square waves show no such ringing.

Another classic misrepresentation of the actual VPS occurs in state-of-the-art playback testing procedures. Most audiophiles know that mistracking should be avoided like the plague. Mistracking, however, is not always a brittle, total disruption of playback events, but may be a gradual nonlinear occurrence. Such "soft mistracking" (Fig. 3) can occur on many passages of a record. Spectrum a nalysis of soft mistracking shows a lowlevel series of harmonics generated at multiples of the fundamental frequency. These are a result of reduction rather than total loss of stylus groove contact. The most frequent cause of soft mistracking is incorrect compensation for
skating force and mild record warps.
Experiments using a spectrum analyzer on a test record with an IM distortion band (a $4-\mathrm{kHz}$ test tone modulated by a $400-\mathrm{Hz}$ tone) provided some interesting results. Sideband frequencies spaced 400 Hz apart on either side of the $4-\mathrm{kHz}$ peak are true IM distortion. Heavily mistracked IM signals produce additional peaks at $400-\mathrm{Hz}$ intervals which appear under spectrum analysis as a "picket fence" of spikes. These spikes (Fig. 4) have been incorrectly termed IM distortion. Actually, it can be shown that these picket-fence side bands are not IM at all, but mistracking of the $400-\mathrm{Hz}$ test tone. These spikes precisely mask and accentuate the true IM sideband products and explain how mistracking has been previously confused with IM distortion. Theoretical models of the oscillographic data of this mistracking event can be developed. A mathematical model based on the veloc-ity-related output of a Denon DL-303 moving-coil cartridge with verification using a Stax CP-X condenser cartridge that directly interprets stylus displacement led to the conclusion that the picket fence of Fig. 4 may occur as a stylus encounters a highly modulated groove.

Figure 5 is a drawing of a monophonic sine-wave groove. The mechanical impedance of the cartridge (stiffness) causes the stylus to ride up the groove wall (A) as the stylus tries to make a "tight turn." Progressively, the stylus loses pressure with the opposite groove wall (B) until a total loss of contact occurs. The stylus tip rides up on the wall until the vertical tracking force (VTF) catches up with the eccentric

0.
20.0 kHz

Fig. 4. Spectrum analysis of $4-k \mathrm{~Hz}$ test tone with a $400-\mathrm{Hz}$
modulation which forms a picket fence of spikes shown in color.
inertial condition, and the stylus literally slams back down into the groove, recontacting both walls (C).

A European test record with high-velocity $300-\mathrm{Hz}$ bands was played 850 times under the conditons of mistracking. The scanning electron microscope clearly shows the path where the stylus rides up one channel and scores it (Fig. 6). Research has shown that the best way to reduce this type of mistracking is to play a given cartridge at the high end of the manufacturer's recommended VTF. This does not increase record wear within the measurement capability of
our systems. Correctly setting the antiskate of a turntable/tonearm system for VTF and groove modulation is very important. A system to accurately make this type of adjustment in the home environment is under development.

The Myth of the Vertical Tracking Angle (VTA). Commonly accepted knowledge about the VPS holds the vertical tracking angle (VTA) between the cartridge/cantilever pivot point and the record (with stylus contact as the angle apex) to be extremely critical in accurate and low distortion playback. (See


Fig. 5. Mistracking of a monophonic sine-wave groove.


Fig. 6. Electron micrograph of a mistracked groove shows ruts caused by errant stylus.

Fig. 7.) Specifically, the VTA of the playback system must match the corresponding angle of the cutting system. To test this hypothesis, it was determined empirically that VTA within a wide range of visual setups was not measurably significant with respect to distortion. Yet it seemed audible at smaller angle differences. Note that four degrees of change in VTA give a visually startling slant to a tonearm, but within plus or minus three degrees (significant visual differences) measured distortion was not dramatically increased.

Since the playback stylus chip (and cutting stylus of the mastering lathe) are fixed to their respective cantilevers or arms, stylus rake angle (SRA) is irrevocably connected to VTA, and the two are often confused. SRA is actually an independent specification, representing the vertical contact profile of the stylus as it touches the record groove. Geometry shows, however, that a spherical stylus will have a constant SRA even though VTA may change. Eliptical and shibata type styli, in contrast, show dramatic alterations in SRA when VTA
changes. This is because of their contact geometries.

As the table in Fig. 8 shows, a spherical stylus has relatively stable distortion levels as VTA changes in the range of two to four degrees. A similar change in VTA for a shibata-type stylus, however, changes SRA as well, and the data show a larger increase of distortion products. There are indications that some cartridges, because of the distance between system pivot and stylus tip (armature length), cannot maintain correct VTA under any circumstances. Even these cartridges can be shown to have minimum distortion (null) points with proper setup.

These data suggest that SRA may in fact be the important setup dimension. This contention has been supported by taking a shibata type of stylus and physically bending the cantilever such that the cartridge VTA is grossly ( $\pm 5$ degrees) out of alignment, while the SRA remains correct. This odd condition produced data not significantly different from that obtained for a correctly aligned shibata.

The conclusions support the idea that VTA may be customarily overemphasized as a parameter of audio quality in the VPS. SRA alone, on the other hand, is extremely critical by both listening
tests and distortion measurements.
Communications with the manufacturers and engineers of cutting lathe systems indicate that the cutting stylus carves the record with a vertical contact profile "tipped" slightly forward (away from the tonearm) pivot by one or two degrees. Thus the SRA of the cutting system is 92 degrees, with a VTA of 16 to 20 degrees. Critical matching alignment of playback-stylus SRA results in audible improvement in the VPS. A procedure to allow SRA alignment to be performed easily in the home is under development. Such alignment is possible now, but it is difficult.

Prospects for the VPS. It is a matter of historical record that the VPS has fulfilled virtually all of the demands made on it, in particular, those for extended playing time and stereo capability. In addition, it has withstood the challenges posed first by open-reel tape and later by the cassette medium. Research into the nature of the VPS leads to the surprising conclusion that, with carefully manufactured software and critical alignment of playback equipment, it can compete successfully even with digital recording systems. In such a contest, the cost-effectiveness of the VPS is undoubtedly one of its strong advantages.


Fig. 8. Distortion levels for spherical and shibata styli as the vertical tracking angle is varied.


Fig. 7. Though in a fixed relation for any one cartridge, stylus rake and vertical tracking angles are independent.


An introductory look at this ubiquitous mass-storage technique

BY LESLIE SOLOMON<br>Senior Technical Editor

FOR large-capacity data storage with fast program access and retrieval, no present computer peripheral can beat the magnetic disk medium. A typical, moderately priced, floppy-disk system can randomly access stored data in no more than a few seconds and can load it into a computer at an extremely high rate. (Hard-disk systems are even faster.) In both these respects it outperforms cassette tape systems handily. For example, a typical $51 / 4^{\prime \prime}$ disk system operates at 15.6 K bytes/second, an $8^{\prime \prime}$ drive works at 31 K bytes/second, while a typical cassette system uses a leisurely 150 bytes/second. Also, in a disk system, most operations are automatic, while with a cassette system, the operator has to manually perform all operations. The cassette medium's sole advantage is very low cost.

The Floppy-Disk System. Three elements make up the floppy-disk system: the flexible diskette on which data is stored; the mechanical disk-drive element; and the electronic controller that interfaces the disk drive to the computer. Let's take a close look at each:

Floppy Diskette. This is the "medium" onto which data is recorded (written) and from which data is played back (read). It consists of a durable, thin, flexible ("flexible," translated into computer jargon, becomes "floppy") plastic disk coated on both sides with a layer of magnetic oxide similar to that used on audio recording tapes. The magnetic medium (the actual "diskette") is sealed into a durable and somewhat less flexible plastic jacket that protects and lubricates it as it rotates.

A large hole in the center of the jacket exposes a smaller circular hole in the magnetic medium, which allows the mo-tor-driven spindle in the drive mecha-
nism to mechanically hold and rotate the diskette against the read/write head. Rotational speed is usually 360 rpm for both $51 / 4^{\prime \prime}$ and $8^{\prime \prime}$ (the two sizes available) systems.

Data is stored as magnetic flux changes in circular "tracks" that are concentric with the center of the diskette. In all, there are from 35 to 77 tracks on a $51 / 4^{\prime \prime}$ diskette and up to 154 tracks on an $8^{\prime \prime}$ diskette. Tracks are subdivided into "sectors," each of which can contain 256 to 1024 bytes of data. In a hard-sectored diskette, there are typically 10 to 16 sectors on a $51 / 4^{\prime \prime}$ diskette and 15 to 30 sectors on an $8^{\prime \prime}$ diskette.

Access of the read/write head medium is through a slot in the jacket. (See Fig. 1). A small circular hole through the jacket is used by the index/sector detector. When a diskette is installed in the drive, a photoelectric detector "looks" through this hole and detects the start of a sector on the diskette when a corresponding hole (or holes, in the case of a hard-sectored disk) is aligned with the index hole in the jacket. Having detected the hole in the diskette, the computer can locate all sectors.

The write-protect notch, always located along an edge of the protective jacket, is sensed by either a photoelectric device or a mechanical switch. Covering this notch on a $51 / 4^{\prime \prime}$ diskette suppresses write operations and protects the recorded data against overwriting. When the notch is open, write operations to the diskette are allowed. The reverse is true of $8^{\prime \prime}$ diskettes: writing is suppressed when the notch is open, and permitted when it is covered. However, some manufacturers use nonstandard write-protect systems. Consult the instruction manual if in doubt. Some diskettes have very small notches on one edge; these are for jacket strain relief and should not be confused with the actual write-protect notch.

Disk Drive. A basic approach to the mechanics of disk-system operation is illustrated in Fig. 2. When the front door of the disk drive is closed, the drive spindle is forced through the hole in the diskette against a stop located above the diskette. A belt connects the spindle to its drive motor. When the diskette is firmly seated on the spindle, a signal from the computer to the disk system's control logic starts the drive motor, which then rotates the diskette. Meanwhile, the write-protect detector will have determined whether or not the diskette can be written to and so inform the read/write logic. Additionally, the index/sector sensor will have detected the correct starting location for the upcoming write or read.

When the start signal is received by the control logic, the head is mechani-
cally stepped to the correct track, where it makes direct contact with the magnetic oxide on the diskette. A pressure pad on the opposite side of the diskette, in line with the head, maintains intimate contact between head and diskette. Most manufacturers include a frontpanel LED that glows to indicate when the disk drive is operating. Also, in most cases, you will hear a click or grinding sound as the positioning mechanism moves the head to the correct track.

Once the disk drive goes into operation and the sensors have done their job, the head "seeks" the correct track and sector and reads the requested data. This data is passed to the read/write logic, where it is processed for use by the computer.

When all operations are completed, the head automatically lifts from the surface of the diskette and the frontpanel LED extinguishes. Then, opening the front door of the drive allows the diskette to be removed from the drive spindle. A darkened LED is not a reliable indicator that all operations have been completed. The drive may simply be pausing between steps and may restart at any time. Only when the "prompt" symbol appears on-screen is it safe to open the drive door and remove the diskette.

Disk Controller. Each manufacturer supplies a DOS (disk operating system)
that is unique to each single- and multi-ple-drive system. In most cases, a disk controller takes the form of a plug-in card that interfaces with a computer. A typical DOS, usually in ROM, will contain enough commands to allow flexible use of a diskette. Though basically similar, DOSes vary in the additional convenience functions that they provide.

Some DOS operations are also used in disk BASICs, which are specialized versions of this popular high-level language specifically written with disk manipulations in mind. A disk BASIC is usually written for a particular DOS or disk system and may or may not work with other disk systems. There may also be a slight difference in syntax used for the same command in DOS or BASIC. In multiple-drive systems, each drive is assigned an identifying code so that it can be "called" to provide data from its diskette.

A well-written DOS with a large number of commands is a programmer's best friend. It enables access to hundreds of thousands of data bytes that need not occupy the relatively sparse amount of RAM usually found in a small computer. Bear in mind that an eight-bit microcomputer can conventionally address 65,536 bytes of memory, and not all of this can be RAM, since operating utilities such as ROM, video plug-ins, and other elements take up


Fig. 1. Configurations of a $5^{1 / 4^{\prime \prime}}$ (above) and $8^{\prime \prime}$ (opposite) diskettes as used in small computers, with makeup of a diskette at far right.
space in the address field. A well-written DOS can function efficiently with as little as 16 K bytes of RAM.

Soft and Hard Sectoring. In general, there are two mutually incompatible types of track sectoring used in modern diskettes. Physical differences make it easy to determine whether a given diskette is hard- or soft-sectored. Soft-sectored diskettes have only one index hole. Hard-sectored diskettes, on the other hand, nominally have 10 or 16 holes. In reality, they have 11 or 17 holes; the extra hole, centered between two of the "official" holes, tells the computer that the next hole identifies the start of sector 0 . Each hole thereafter identifies the start of a sector. Circuits in the disk controller detect the shorter space between the index hole and the hole following it and reset the counter to 0 .

On a soft-sectored diskette, the DOS creates sectors on an ad hoc basis. Conceivably, the entire disk could be a single sector.

To determine the type of sectoring used in a given computer, check either the instruction manual or the diskette itself. If you wish to examine the diskette, hold it by its protective jacket. Being careful to avoid touching the magnetic medium through the head-access slot or bending the diskette, use a slender nonmagnetic tool to lift the jacket
away from the diskette through the large center hole. Look into the opening, and count the number of indexing holes. If there is only one hole, the diskette is soft-sectored. If there are 11 or 17 holes, it is hard-sectored. Make a note of the number of holes used in your diskettes.

In most cases, the manufacturer's label on the diskette's jacket will inform you of the diskette's formatting. For example, Verbatim uses the following three-digit code prefix: 525 for a $51 / 4^{\prime \prime}$ single-sided diskette; 550 for a $51 / 4^{\prime \prime}$ dou-ble-sided, double-density diskette; and 577 for a $51 / 4^{\prime \prime}$ single-sided, double-density diskette. These are followed by a two-digit suffix: $-01,-10$, or -16 , which identify soft-sectoring and 10 and 16 -hole hard-sectoring, respectively. Hence a diskette with a $525-10$ code is a $51 / 4^{\prime \prime}$ single-sided, 10 -hole hard-sectored diskette.

To increase the amount of data that can be stored on a diskette, manufacturers have gone to "double-density" (twice the number of bits per track), "doublesided" (recording the disk on both sides), and "quad-density" (a combination of the above) schemes. All have their advantages, but they can present some problems. For example, because there is no standard for double-density systems, such diskettes often cannot be interchanged. In double-sided systems, there is a read/write head on both sides
of the diskette, each acting as a "pressure pad" for the other. This can exert too much pressure, damage the medium, and cause rapid head wear. New drives do not have this problem.

Generally speaking, a disk controller designed for a double-density system can read either single- or double-density diskettes with appropriate hardware or software changes. However, a singledensity controller cannot read doubledensity diskettes.

Since a diskette is coated on both sides with magnetic oxide, there is yet a nother approach to increasing storage capacity. You can punch an index hole and write-protect notch through the diskette's jacket and make the "other" side of the diskette available for data storage. Bear in mind, though, that in a single-headed drive, the head is backed up by a pressure pad. If this pad becomes contaminated with oxide or particulate matter, it can destroy the soft oxide on both sides of the diskette.

More Details. Data in a disk system is written onto the diskette using a form of frequency modulation (FM), wherein a single data pulse ( 0 or 1 ) is written so that it is centered between clock pulses. The presence of a flux transition indicates a binary 1 , its absence a binary 0 . Double density is achieved by using a modulation technique called modified


FM (MFM) or modified-modified FM ( $\mathrm{M}^{2} \mathrm{FM}$ ). Here, a double-frequency write oscillator is used to maintain the same flux changes per inch. Thus, a bit cell in MFM/M2FM is only half that used in FM. The data-transfer rate is doubled, since a $1: 1$ relationship exists between the flux changes and bits per inch ( $2: 1$ in FM).

In $\mathbf{M}^{2} \mathbf{F M}$, clock bits are written at the beginning of each cell if there is no data bit written into the previous cell and if there will be no data bit written into the present cell. Since a double-density bit cell may not have a clock pulse at its beginning, data separation is more complex and prone to errors.

Transfer rate is the speed at which requested data can be moved from the diskette to the computer. Look out here for the "numbers game," though, since a system that can transfer at 250,000 bits per second is not the same as one that can transfer 250,000 bytes per second.

Error rates in the typical diskette system have been estimated to be one per $10^{8}$ bits during read operations for softsectored diskettes; one per $10^{11}$ bits during read for hard-sectored diskettes; and one per $10^{6}$ bits during seek.

Diskette is installed with notch up or down depending on make of drive.
-


Life of a diskette is estimated to be about two years under "normal-use" conditions. One manufacturer (Shugart) claims a life of 2 million passes on any one track. A track is considered to be worn when its output signal voltage falls by $20 \%$ from its original level.

Estimated MTBF (mean time before failure) in the typical disk system is approximately 8000 hours. Component design life is usually estimated conservatively at five years.

Selecting a Disk System. Before you buy a disk system, determine what you intend to do with your computer. If your computer will be used as a game "player" or for experimenting, learning computer techniques, or anything else that does not require extensive number/


Fig. 2. Basic electronics and mechanical design of a typical floppy diskette system. In this view, the diskette is assumed to be installed and ready to go.
data "crunching," a single $51 / 4^{\prime \prime}$ drive will probably suffice. But if you want a really large-capacity mass storage, you may need a multiple-drive system.

Mass storage requirements will also dictate the choice between $51 / 4^{\prime \prime}$ and $8^{\prime \prime}$ drives. (An $8^{\prime \prime}$ diskette can hold twice as much data as a $51 / 4^{\prime \prime}$ diskette at less than twice the cost.) Note that you never really have all the available disk space for program-data storage. Some is reserved for formatting (file names, headers, etc.).

If your data demands range into millions of bytes (megabytes), a "harddisk" system may be your most economical alternative. Bear in mind, however, that you will have to back up the data on the hard disk with one or more floppydisk drives, just in case the hard disk suddenly "loses" a megabyte of data.

In most cases, a single disk controller board and DOS can handle up to three drives. This allows you to add drives to a minimal system as you need and can afford them.

There is no hard and fast rule that can guide you in selecting the "best" disk system for a particular computer but there are some basics to be observed. Most important is that the electronics and DOS or high-level language provided with the disk system must be compatible with your computer. The DOS, BASIC, assembler, and any other software features supplied on the disk system's diskette must be able to call your particular computer's keyboard, video display, printer, etc. Unless you are very experienced in using software, you will find it extremely difficult to integrate a "strange" DOS or language into your computer's routines.

Make sure that any software you require is available for the disk system you select. While on the subject of software, it is important to note that not all CP/ Ms (a popular, "universal" DOS) are as insensitive to hardware as claimed. If you should have single-density drives and obtain double-density-formatted $\mathrm{CP} / \mathrm{M}$, the disk cannot be read, and CP/M will not run. So, before you buy a given $\mathrm{CP} / \mathrm{M}$, make certain it will run in your system.

Access time (the time taken to move the head to a desired track) can sometimes be important. Long waiting times,
even though measured in milliseconds, can interfere with some sophisticated software or may nullify its advantages. It is also crucial that the disk system be compatible on the hardware level. The North Star hard-sectored system, for example, achieves high transfer by outputting data on the address bus. Naturally, it requires a special interface.

The Future. There is little doubt that the hard-disk ("Winchester-drive") system is the wave of the future for low-cost multimegabyte mass storage. What makes the hard-disk different from the floppy-disk medium is that the former allows no physical access to the disk and read/write head(s), which are hermetically sealed in a protective enclosure. Hermetic sealing is necessary because the read/write head literally "flies" a minute distance above the rapidly rotating ( 3600 rpm ) surface of the magnetic disk. If any particulate matter, even microdust, were to get between disk and head, catastrophic disk damage would almost certainly result.

The hard-disk drive employs a very precise mechanism in which tolerances are extremely tight. Track density, therefore is vastly increased, making it possible for the disk to store considerably more information than is possible with floppy disks.

Although hard-disk systems appear to be very expensive, they are actually much less expensive than floppy disks and RAMs on a cost-per-byte basis. In a typical $51 / 4^{\prime \prime}$ floppy-disk system, average per-byte cost is $0.002 \phi$, while an "inexpensive" 64 K byte RAM system's typical per-byte cost is 0.006 . Now contrast these figures with the 0.0002 c per byte cost for a 26 -megabyte hard-disk system that sells for $\$ 5000$.

Hard disks come in a variety of sizes, ranging from $51 / 4^{\prime \prime}$ to $14^{\prime \prime}$. The latest on the market is Shugart's Model ST506, which offers 6.38 megabytes unformatted ( 5 megabytes formatted) storage capacity on 612 tracks and four read/ write heads at a suggested retail price of $\$ 1000$. With a per-byte cost of only $0.0002 \phi$, the ST 506 may well signify the start of a trend in low-end hard-disk systems for the small-computer system owner.

At present, no industry standard exists for the computer/disk interface. However, because of the large market share garnered by Shugart Associates, this company's Model SA 1000 harddisk system has become the de facto standard used by low-cost hard-disk system manufacturers. (This trend is similar to the "standard" set by the MITS S-100 bus for eight-bit microcomputers.) It is more than likely that the SA1000 interface will soon be modified to handle any floppy-disk drives that
may be used to back up the data on the hard disk.

Whether or not prices for hard-disk systems will drop as dramatically as the prices for computer systems in general is difficult to foretell. But even if prices remain the same as they are now and more hard-disk systems come on the low-end market, many computer owners are likely to move up to the hard disk, especially if they already have minifloppy systems that can plug directly into the hard-disk system for backup.

## FRAGILEHANDLE WITH CARE

Because the magnetic oxide on floppy diskettes is relatively soft and very thin, it can easily be damaged unless special care is exercised during handling, use, and storage. For this reason, we have drawn up a list of 10 things to avoid. Here they are:

- Don't touch the magnetic-oxide surface on the diskette, especially around the head-access slot in the jacket. Body oils in fingerprints can permanently destroy data. .i. V紋Don't leave "a diskette lying around where it can collect dust, dirt, and other contamination. Even a tiny dust particle passing between head and diskette will scratch the oxide medium. Always return each diskette to its storage envelope.
- Don't smoke when you are handling diskettes (or when you are operating your computer, for that matter). Airbome smoke and ash particles have a habit of collecting on diskettes and ruining them.
- Don't write on a label that is already on a diskette with a ballpoint pen. Transmitted pressure from the pen's point can easily damage the oxide surface on the diskette. If you cannot write out your labels off the diskette, use only a soft felt-tipped pen and very little pressurs:
- Don't erase a label while it is on a diskette. No matter how careful you are, eraser and paper particles are almost ertain to contaminate the diskette.

Don't force a diskette into a disk drive or its storage envelope. If you encounter resistance, back out and try again.

- Don't bend or fold a diskette. If you do, tiny pieces of oxide will flake away and render the diskette useless.
- Don't store diskettes in locations where temperatures are likely to rise beyond $110^{\circ} \mathrm{F}$ or where there is danger of stray magnetic fields.
- Don't store diskettes lying flat and 0 top of each other. Like phonograph records, store them vertically, in dust-tight containers.
- Don't power up or power down a disk system with a diskette in the drive. If you do, stray magnetic fields generated by the drive's motors may alter the data on the diskette making it incorrect.

If you follow these rules religiously and keep everything scrupulously clean, you should obtain the maximum operating life from every diskette you own.

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BASIC programming. The machine has 24 different programs statements and commands printed at the top of the keyboard. You can enter these 24 into your program without retyping them every time you use them. Instead of typing out "PRINT", for example, you just press two keys and the word appears on the screen. The system helps prevent typing errors and can speed up entering programs.

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## PART II

# TOW YOU CAN BUILD MICROPROCESSOR PROJECTS USING AN INEXPENSIVE PRODUCT DEVELOPMENT SYSTEM! 

BY CEORCE MEYERLE

IN May 1980, our first article on designing with microprocessors, "It's a Whole New Ballgame," showed how, with an understanding of just a few instructions and very modest hardware, one could build a microprocessor-based combination lock. However, to implement more generalized microprocessor product design with confidence, additional skills in programming and with hardware are required. Development of these skills will be the theme of this and future articles.

This article describes the use of input and output ports and how the microprocessor interfaces with peripheral hardware such as random access memory (RAM) and read-only memory (ROM). We will go on to the construction of an 1802 microprocessor product development system. This is an inexpensive system (professional models cost thousands of dollars) that allows the processor to be programmed, tested, and debugged for a particular application. The unique property of this system is that it is contained on two separable circuit boards: the product board and the programming board. The former becomes part of the finished project-whatever that may be; the latter is used to program other product boards.

The product or application board we will describe includes a CPU (central processing unit), a crystal-controlled system clock, 1024 bytes of RAM, 2048 bytes of ROM, one 8 -bit input port, one 8 -bit output port, address latches and decoding, buffered CPU flag inputs, Q output, interrupt/DMA (direct memory access) request input, a POWER on clear circuit, and battery backup.


The product board is shown at top, and the programming board below.

The board uses bus connectors to plug in the programming board as well as additional product expansion cards or boards such as additional I/O, memory, A/D. graphics keyboard, cassette $1 / O$, etc. The programming board that we will describe includes a hex keyboard to input programs, six 7 -segment displays that show the status of the address and data busses, a variable-speed single-step system used to step through the program, LEDs on the mode and state code lines of the CPU to indicate what the processor is doing, and a series of switches to completely control the action of the CPU. Functions include resft. RUN/ exfecute, wait. load. input and memoRY PROTECT. (This programming card is not necessary if you already own an Elf

II; the product board plugs into an Elf II and can be programmed directly.)

Product Board Operation. The central component of the product board is the microprocessor (CPU), which controls the activities of all the peripherals. For the time being, we will not be concerned with what happens inside the CPU, only with external effects and signals. The following is a description of events and signals present at the CPU during various operations. (See Fig. 1.)

Clock. The clock signal required by the processor is generated by an external oscillator. It is used by the CPU to produce internal and external timing signals needed to control the peripherals as well as to transfer signals and data internally between parts of the CPU.

CPU Mode Control. Two processor mode-control pins completely control the CPU action. They are labelled WAIT and CLEAR. The line (vinculum) over the name indicates that a low-logic level ( 0 volts) at that point will cause the stated effect. To change the CPU mode, we simply have to present the $\overline{\text { Clear }}$ and $\overline{\text { WAIT }}$ lines with the logic levels shown in Fig. 2. When we want to put the processor in the RESET mode ( $\overline{C L E E A R}$ low, WAIT high) certain internal registers are set to predictable states. It will suffice for the moment to realize that the program counter is set to 0 . This insures that when the mode is changed from RESET to run or load, the first address issued by the CPU will be 0000 . When the CPU is put into the LOAD mode (must be preceded by reset), it is possible to load a program into memory via the directmemory access line of the CPU. The


Fig. 1. Events and signals present at the CPU during various functions.
program is loaded directly into memory from the hex keypad during this mode. When the CPU is in the walt or pause mode, the internal timing generator does not function. The clock continues to run but it is ignored. Note that the data, address, and other control lines may not have valid data or status in the walt mode.

The run mode (both $\overline{\text { CLEAR }}$ and $\overline{\text { WAIT }}$ high) may be entered from a paUSE or RESET mode. Remember that the first machine cycle following a reset to RUN is followed by a memory fetch at address 0000 . This is extremely important and simplifies the hardware interface.

State Code Lines. When in RuN, the CPU can perform only one of four types of operations. They are: instruction FETCH. also called so ( S an abbreviation for "state"), extcute, si, a direct memory access, sz or interrupt, s3. Ignoring dMA and interrupt for the moment, we can say that the CPU is simply either fetching instructions or executing them. A FETCH requires one machine cycle (each machine cycle is made up of eight clock pulses). EXECUTE requires one or two machine cycles, two required only during long-branch operations.

The State-Code lines labelled SC1 and sco are used to tell the peripherals which type of operation is being performed by the processor. (See Fig. 3.) Note that during FETCH, both SCI and SCO are low; during execlete. SCl is low and SCO is high.

Timing Pulses. (In addition to the state code lines, there are timing pulse $A$ and $B$ (TPA, TPB). These signal the peripherals when the address, data and 1/O COMMAND lines are valid and other internal operations are completed.

MEMORY READ and MEMORY WRITE LINES. These, designated $\overline{M R D}$ and $\overline{M W R}$, are both active with logic low. $\overline{\text { MRD }}$ is present when the processor wants data from the memory. $\overline{\text { MRD }}$ must occur during a FETCH when the processor is reading memory for its next instruction. It must also occur during an OUTPUT exec-
ution when the data is being transferred from the memory to an output port, as well as during branch, and other memory read operations. $\overline{M W R}$ is present only when the processor is writing data into memory. Data may originate in the CPU or from an input port.

Input Flags. The input flag lines, $\overline{\mathrm{EFI}}$, $\overline{E F 2}, \overline{E F 3}, \overline{E F 4}$ are tested by the microprocessor and often used as single-signal input ports. The flag lines are tested during an execute. Remember that these lines are active low.

Q -Line. The Q -line output can be set high or low by a program instruction. The status of the Q-line can be tested by conditional BRANCH instructions. $Q$ is a single-signal output port and can control relays or other devices
memory adiress Lines. These eight lines are multiplexed. The high-order eight bits of the 16 -bit address are present first; at the end of the TPA the loworder bits are presented. If the system memory is larger than 256 bytes, additional hardware is needed to record (latch) the high-order bits so that all 16 are presented simultaneously to the memory. The trailing edge of the TPA can be used to signal the latch when the high-order addresses are valid.
data bus Lines. These eight-bit-bidirectional lines transfer data between the memory, CPU, and $1 / \mathrm{O}$ devices.
(Continued on page 64)

Fig. 3. State code lines define operation


Fig 4. The timing diagram is used to show signal relationships.
Shaded areas indicate undefined state when multiple transition may occur.

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Fig. 5. The various parts of the circuit on the Product Board are shown schematically above and on the opposite page. Numbers in circles indicate jumper pads.


Power Supply

correct hardware I/O signalling devices. Note the eight clock pulses, 0-7, and the synchronization between them and the signals on the CPU lines. state codes become valid just before the leading edge of TPA and continue valid until the trailing edge of TPB. External hardware is responsible for reading CPU signals during valid periods only. Each machine cycle outputs a 16 -bit address of which the high-order eight bits are valid at the trailing edge of TPA and are followed by the low-order eight bits which are valid

## PARTS LISTPRODUCT BOARD

$\mathrm{C} 1-2.2-\mu \mathrm{F}, 16-\mathrm{V}$ electrolytic
$\mathrm{C} 2, \mathrm{C} 3, \mathrm{C} 4-0.01-\mu \mathrm{F}, 50-\mathrm{V}$ disc ceramic capacitor
C5,C6-10- $\mu$ F, 16-V electrolytic
D1 through D6-1N4 148 switching diode
D7-1N4001 rectifier
IC 1,IC2-2114 random-access memory
IC3-2716 ROM
IC4.IC5-1852 I/O port
IC6-CD4050 hex noninverting buffer
IC7,IC8-74LS00 quad 2 -input NAND gate IC9.IC 15.IC 16-74LSO4 hex inverter
IC 10-74LS74 dual D flip-flop
IC11-1802 CPU
IC 12,IC 13-74LS174 hex D flip-flop
IC 14.IC17-74LS30 eight-input NAND gate
IC 18-7805 5 -volt regulator
Q1-2N4384 transistor
The following, unless otherwise specified, are $1 / 4$-watt, $10 \%$ tolerance, fixed car-bon-composition resistors.
R1,R2,R5,R11,R17-4.7k $\Omega$
R3,R4-1k 2
R6,R7-100 k $\Omega$
R8,R9,R10,R12,R13,R14,R15,R16,R18, R19,R20,R22,R24-22k $\Omega$
R21-2.2k $\Omega$
R23-47 $\Omega, 1 / 2 \mathrm{~W}$
XTAL $1-3.579-\mathrm{MHz}$ quartz crystal
Misc.-Suitably etched and drilled, doublesided printed circuit board with platedthrough holes and gold-plated edge-connector contacts; one normally open, momentary-contact pushbutton switch IC sockete (one 40-pin, three 24-pin, two 18-pin, ten 14-pin) or Molex Solder-cons; three 14-pin DIP headers; heat sink for IC18 measuring $1^{\prime \prime} \times 1 / 2^{\prime \prime} \times 1 / 8^{\prime \prime}$ or similar; hookup wire; solder; etc



Fig. 6. How to determine load resistances on output ports. In circuit above, minimum relay coil resistance is $2.1 \mathrm{k} \Omega$. The relay is active when the output is high. In the lower circuit, minimum relay resistance is 8202. The relay is active when the output is low.


Fig. 7. Programming board circuits (A through F) are shown here and opposite
through the trailing edge of TPB. When the CPU reads from the data bus, data is latched into the CPU during TPB. I/O COMMAND lines $N 0, N$, and $N 2$ are valid from the beginning of TPA to the end of TPB. The CPU tests the flag lines during an SI cycle; the CPU responds to control signals $\overline{\text { WAIT }}$ and $\overline{C L E A R}$ during TPA, and samples DMA and INTERRUPT during TPB. Some conditions are valid only during certain types of machine cycles. For example, DMA is sampled only during machine cycles $\mathrm{S} 1, \mathrm{~S}_{2}$, or $\mathrm{S}_{3}$.

Interfacing Memory. Having studied the signals present at the CPU, let's look at the schematic of the product board shown as Figs 5A through 5G. Data, in this case the high-order address bits, are latched into the $Q$ outputs of the flip-flop during a low-to-high transition on the clock input. This low-to-high signal is produced through inversion of TPA

## PARTS LISTPROGRAMMING BOARD

C1,C5- $1-\mu \mathrm{F}, 16-\mathrm{V}$ electrolytic
C2,C3-0.15- $\mu \mathrm{F}$ Mylar capacitor
C4-2.2- $\mu \mathrm{F}, 16-\mathrm{V}$ electrolytic
C6,C7-22- $\mu \mathrm{F}, 16-\mathrm{V}$ electrolytic
C8-0.1- $\mu$ F disc ceramic capacitor
D1 through D13,D15-1N4148 switching diode
D14-1N4001 rectifier
DIS1 through DIS6-Common-cathode, seven-segment LED display
IC 1-74C923 CMOS 20-key encoder
IC2-74C173 CMOS TRI-STATE quad D flip-flop
IC3,IC4-CD4016 quad bilateral switch
IC5,IC15-CD4013 dual D flip-flop
IC6,IC 14-74LSO8 quad 2-input AND gate
IC7 through IC 12-MD 4368 hexadecimal-to-seven-segment decoder
IC 13,IC 19-74LSO4 hex inverter
IC 16,IC17,IC 18-74LS 10 triple 3-input NAND gate
LED1 through LED5-Red light-emitting diode (HP5082 or equivalent)
The following, unless otherwise specified, are $1 / 4$-watt, $10 \%$ tolerance, fixed carbon composition resistors.
R1-15 $\Omega$, $1 / 2 \mathrm{~W}$
R2,R6,R7 through R10,R18,R19-4.7k $\Omega$
R3-1-M $\Omega$, linear-taper trimmer potentiometer
R4-22k $\Omega$
R5-1 M $\Omega$
R11,R14 through R17-470 $\Omega$
R12-200 』
R13-47k $\Omega$
Misc.-Suitably etched and drilled, doublesided printed circuit board with platedthrough holes and gold-plated edge-connector contacts; 86-contact edge connector with gold-plated contacts; 22 normally open, momentary-contact pushbutton switches (this includes those comprising the hexadecimal keypad); two spdt toggle switches; IC sockets (seven 16-pin, eleven 14-pin) or Molex Soldercons; suitable hardware; hookup wire; solder; etc.

(C)

(D)
by IC9. The high-order address lines A8 to A 15 are therefore present at the Q outputs of ICl2 and ICI3. Now, with the full 16-bit address present on the memory address bus, we can look at how the RAM $I C I$ and $I C 2$ and ROM IC3 are actually addressed. RAM $I C 1$ and $I C 2$ contain 1,024 different memory cells or addresses; while ROM IC3 contains 2,048 different cells. The CPU is capable of addressing 65,536 different cells or addresses. To uniquely position our blocks of memory in the 65,536 address field, we must decode the upper address lines. If we want to address a RAM (ICI or IC2 for example), we need 10 address lines, A0 through A9 $\left(2^{10}=1024\right)$. Note that on the 2114 s , pin 8 is a chip-select line, active when low. The pin, when active, connects the memory cells to the data bus.

The 1,024 block could be addressed in any of 64 different blocks in the 65,536

(F)



Fig. 8. Use this circuit to connect an 8-volt dc supply to the Product Board.
address field. Address lines A 10 through A15 are decoded to select a specific field. If all the inputs to eight-input NAND gate $I C 17$ are high, the output on pin 8 will be low and the RAM memory will be selected. The six address lines can be connected to the inputs of the NAND gate either directly or through hex inverter IC16. The use of inverters at selected address lines allows 64 (26) different possible locations for the block of memory.

To determine which address lines require an inverter, write the binary equivalent of the first address in the block of memory. Label the high-order six digits A 15 through A10. An inverter must be used at all locations with a 0 binary digit. To locate the memory block at F000 (hex), we would use an inverter on address 11 and connect A12 through A 15 directly to the NAND gate. If only one memory block is used and it is to be located at 0000, the chip-select $\overline{\mathrm{CS}}$ line can be grounded and IC's $14,15,16$, and 17 can be removed.

The same principle is used in the 2 K bank-select, using IC14 and IC15 to locate the position in the memory field for IC3.

To finish our discussion on the memory, let's look at the memory read and memory write lines. $I C 1$ and $I C 2$ deposit their data on the bus whenever the CS line is low so there is no need to have the CPU READ signal connected. The $\overline{\mathrm{MWR}}(\mathrm{P})$ is connected to pin 10 . When the CHIP SELECT and $\overline{M W R}$ lines are low, the information on the data bus is copied into the memory cell address. Data flows from IC3 to the data bus only when both the $\overline{C S}$ and $\overline{M R D}$ lines are low. Although the position of IC3 is principally intended to be filled by a 2716 EPROM (which requires that pin 21 be tied high), a 2 K RAM can be used instead by connecting its pin 21 to MWR. If you plan to use a 2 K RAM at IC3, check the specs carefully for signal requirements. Finally the memory write line, pin 10 (IC1 and IC2), is labelled $\overline{M W R}$ ( P ). The P means that the memory can be protected against accidental written data by grounding pin 9 of IC8, which prevents $\overline{M W R}$ from reaching the memory. This line, $\overline{\mathrm{MP}}$, is also connected to pin 43 on the 86 -pin bus,

Interfacing I/O Ports. Integrated circuit IC5 has pin 1 tied low and acts as an input port. It has eight input lines that can be connected to external equipment via plug $P 2$. These inputs, typically, would be connected to the status switches on a robot, alarm system, etc. When the data is valid and to be read, IC5's clock line (pin 11) is externally made high. This latches the data into the input side of the port. A high-to-low transition of the clock line causes the input data to be latched into the input port's output register and simultaneously sets the SERvice request line, pin 23, low. At this point, the output drivers of IC5 are disabled and the data is not yet on the bus. The service request line of $I C 5$, pin 23 can be connected to the CPU flag EF3, $\overline{\text { INTERRUPT, }}$ or $\overline{\text { DMA. If }}$ you use $\overline{\mathrm{EF3}}$, your program must interrogate $\overline{E F 3}$ and issue an instruction that results in the transfer of data from the port to a memory location designated by the program, as well as to the D register in the CPU. Actual data transfer takes place when IC5's CS1 and CS2 both go high. This enables the output drivers and puts the data on the bus. At the same time, SERvICE REQUEST is reset. Voltage at the inputs must be in the range of -0.5 to 5.5 volts dc. This is true of all the gates and inputs that we will discuss.

Output port IC4 is identical to input port IC5. Tying pin 2 high converts the chip to its output mode. Data is latched

| To Output Port |  |  |  |
| :--- | :--- | :--- | :--- |
| 0000 | OUT 1 | 61 | Output |
| 0002 |  | FF | All byte |
| 0003 | IDL | 00 | Idle |

nto the port when $\overline{\mathrm{CSI}}$ is low during a AEMORY READ and $\overline{\mathrm{CS} 2}$ is high during an sutput instruction, and when TPB is high. Data is immediately transferred to the output lines and is available at plug P3. A service request pulse is generated by the port after MRD line goes high. This pulse can signal the receiving device that new data is available.

The output lines can be connected to low-current relays or other digital devices. The 1852 will deliver up to 2.3 mA when the output is high or will sink up to 6 mA when the output is low. See Fig. 6 to determine the maximum and minimum resistances that may appear as loads on this port. Input and output commands can be changed by connecting pin 13 to a decoded N line (decoder must be on an expansion board). The output instruction for this board is 61 and the input is 6B. A final detail regarding the $I / O$ ports is pin 14 CLEAR. A low on this line resets the port's registers and the SERVICE REQUEST output.
interrupt/dma Interface. As shown in the CPU timing diagram, DMA and interrupt lines are only sampled for a short interval by the CPU. Therefore we must hold the request low until the CPU acknowledges that it has been received. This is done by flip-flop ICIOB. A nega-tive-to-positive pulse at pin 11 (clock) sets pin 8 low. Pin 8 can be connected to either the $\overline{\text { INT }}$ or DMA OUT line on the CPU. If an interrupt is chosen, pin 8 of IC7 will go low because the state code lines Sco and SCı will both be high. (See Fig. 4.) This will reset $I C I O B$ and signal the device requesting the interrupt that the CPU has responded. Jumper 7, A to C, must be connected. If $I C 10 B$ pin 8 is connected to DMA OUT, $I C 7$ pin 11 will go low when the state code lines indicate that DMA is in progress, again resetting the flip-flop and acknowledging the dma request via jumper 7, A to B. These examples illustrate the importance of understanding timing diagrams.

Fig. 9. Use the program at left to output a byte and the onc below to input a byte.
power on/clear Circuit. This automatically resets the CPU in the event of a power failure. Its usefulness depends on the retention of data in memory despite power loss, which means that the memory or program memory must be in ROM. When the CLEAR line on the CPU is held low with the wait line high, the CPU is reset. This condition is met on a power up because the CLEAR line is held low by $I C 9$ until $C l$ charges. This delay lets the onboard crystal oscillator stabilize before program execution begins.

The crystal clock generator ( 2 gates of $I C 9$ ) generates a $3.578-\mathrm{MHz}$ square wave. The D-type flip-flop IC10A is used to divide the frequency by two. The 1802 clock input is connected to pin 5 of IClOA. A ClOCK signal is provided to drive a color video system in an expanded setup.

Buffers and Power Supply. Input and output buffers in IC6 isolate the external flags and $Q$ lines from the CPU. Buffers are far less costly than CPUs and can serve as protection against voltage transients and short circuits that may occur in the outside world. Additionally, they are capable of sourcing and sinking larger currents than can CPU lines.

The power-supply regulator $Q 2$ will deliver up to 500 mA dc . The programming board also gets its power from this source. The power requirement of the product board, fully loaded, is 110 mA . An optional NiCd battery pack can be added (four 1.5 -volt cells) to power the product board for about 2 hours in the event of a power failure.

Programming Board. The purpose of the programming board is to load a program into the memory on the product board and then test the program and hardware. It can be disconnected from the product board when the programming and testing are complete. The programming board (Figs. 7A through 7F) is connected (via the 86 pin bus) to the CLEAR and WAIT lines on the CPU; therefore it can place the CPU in any of its four modes, displaying the mode selected on $L E D 1$ and $L E D 2$. Two LEDS (LED3 and LED4) on the STATE CODE lines identify the type of CPU machine cycle in progress.

The hex value of the entire 16 -bit address line is displayed on 7 -segment displays DISI through DIS4. Two 7-segment displays (DIS5 and DIS6) perform the same function for the data bus. Data bus displays are also configured as an output port responding to a 64 -output instruction. A hex keyboard is connected to the data bus via two 4116 gates. ICl converts the hex value of the key press to its 4 -bit binary equivalent. When a second key is pressed, the value
(Continued on page 74)


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## microprocessor

of the first key is stored in $I C 2$. This puts a full 8 -bit number on the input side of the 4016 gates $I C 3$ and IC4. If the CPU is in the LOAD mode, pressing the INPUT button will load the binary value of the two keys previously pressed into the program memory. This is accomplished by pin 2 of IC5 pulling the DMA IN line low, causing the contents of the data bus to be directly loaded into the memory location specified by the register 0 in the CPU. The CPU will acknowledge the $\overline{\mathrm{DMA}}$ by issuing a high on SCl. This is used to reset flip-flop $I C 5$. The input switch is also connected to the $\overline{E F 4}$ line.

The hex keyboard also can be used to input data during a program. It is configured to respond to a 6 C instruction. The RUN. LOAD and RESET buttons are connected to three NAND gates of ICl7. These gates are flip-flops that apply the appropriate logic levels to the $\bar{C} \overline{L E A R}$ and $\overline{\text { WAIT }}$ lines which control the CPU mode. The walt switch will cause the CPU to pause. It is arranged so that the pause will only occur between TPA and TPB to insure that valid data and addresses are displayed when the walt is issued. The single-step mode allows the operator to execute the program one step at a time, observing the STATE coDes, $Q$ line, data and address lines, as well as the action taken by external relays and components, etc. Jumper $J 1$ can select the stopping point during the single-step mode. If $J l$ is connected $\mathbf{A}$ to $C$, then the program will stop only in a FETCH cycle. If $J 1$ is connected B to C , the stopping point will be each machine cycle at the trailing edge of TPB. You may want to try this initially to get a better understanding of the CPU operation. However, eventually you will probably connect $J l$ to HALT only in the FETCH mode.

Remember to consult the timing diagrams to interpret the contents and meaning of the ADDRESS and DATA bus signals.

Slow-Step Clock. Formed from IC19, the speed of the stepping or toggling clock is controlled by $R 3$. Hold the STEP toggle button depressed, and adjust $R 3$ for the clock speed desired.

Test Procedure. To test the combination of the product and programming boards, install the six 1 K bank-select jumpers (IA to G). This will locate the 1 K memory at 0000 hex. Connect the input ports so that jumper 1 connects from terminal $A$ to $\overline{E F 3}$ and jumper 2 between $A$ and $B$, selecting instruction 61. Connect the output port jumper 3 A to B , the 1 K memory-select jumper 4 A to B. Jumpers 5, 6 and 7 are not required at this time. Install the following jumpers on the programming board: $J 1$ A to C, J2 B to C. Now connect an

8 -volt dc supply between the 8 -volt input and ground on the product board. Use the circuit shown in Fig. 8. Apply the power with no ICs installed. Check the output of the 7805 regulator $Q 2$ on the product board. The voltage should be +5 volts, $\pm 5 \%$. If not, correct the problem before proceeding.

Remove the power, install the ICs, and plug the programming board into the product board. If you are using an Elf II to program the product board, remove the 1802,2102 's and the 1861 video chip. (The 1861 will interfere with the I/O port allocation unless you use your giant board to reallocate $I / O$ instructions to IC4 and IC5 on the product board.

When power is applied, the 7 -segment displays should light up. With the latching switches up, press the RESET button. The reset LED should be on, as well as SCo. PAUSE. SCi and Q LEDs should be off. Press the load button. The reset LED should go off. Enter a 7 on the hex keyboard, followed by a $B$, then depress the input button. The display should read 00007 B , indicating that a 7 B is located at memory location 0000 . Now input 7A, followed by a 3 A and 00 . The display should read 000300 . This program first turns the $Q$ light on, then turns it off and branches back to the beginning, running in a loop.
Now press reset, followed by the EXecuterun button. Both the reset and pause lights should go on, indicating that both the $\overline{\text { WAIT }}$ and $\overline{\text { CLEAR }}$ lines on the CPU are high or that the CPU is in the RUN mode. The Q light should glow. It is being turned on and off by the program. Now press the wait button. The pause light should stay on, the RESET go off. The program can now stop at any point in the loop. Remember, it stops in any machine cycle, so it may stop in a fetch or execute.

Depress E again. The program will start up where it left off. Try it a few times, noting that the program stops only at address 0000 through 0003 and that the Q. SCO and SC। LEDs may or may not be on. Try to estimate where the CPU has stopped. Now depress the $S$ (STEP) button. Then depress E, holding the $T$ (toggle) button down. Rotate $R 3$ until you can follow the program.

Note that the addresses displayed are one step ahead of the action on the data bus display and the Q LED. This is because the SINGLE-STEP stops at the beginning of a FETCH, while the DATA display and the Q LED are indicating the previous machine execute cycle. Refer to Fig. 4 and follow the timing diagram through the single-step mode.

To examine memory, press RESET, LOAD and P (Memory Protect). Remember to release the SINGle-step button. Now, pressing input will allow you to
step through your program. Pressing and holding the togale button will allow you to review it at a rate determined by $R 3$. The M button is not used at this time. Provisions have been made to add a system monitor which will be activated with this button. To test the I/O ports, write a small program such as those in Fig. 9 using them.

Construction. Assembly of the development system is basically straightforward, but because of the complexity of the circuitry, use of pc boards is virtually a necessity. The boards are doublesided and very difficult to make, and their purchase is recommended. Because of their size, the foil patterns are not given here. The patterns and component layout guides may be obtained by sending a self-addressed 8 -inch by 10 -inch envelope with two units of postage to Editorial, Dept. MP, Popular Electronics, One Park Ave., New York, NY 10016. Several of the ICs are MOS devices and require the standard handling precautions.

Once you have assembled and checked out your development system, you will be well on the way to proficiency in designing with microprocessors. The scope of projects you can design and build is limited only by your imagination and ingenuity. Considering the vast possibilities offered by microprocessors, you may want to order a supply of several of the product boards.

## PARTS AND KIT AVAILABILITY

The following items are available from Netronics Research and Development, Ltd., 333 Litchfield Road, New Milford, CT 06776: complete Product Board kit including all components appearing in Product Board parts list, \$59.95; complete Programming Board kit including all components appearing in Programming Board parts list, $\$ 79.95$. The following are also available individually: Product Board printed-circuit board only, $\$ 25.00$; Programming Board printed-circuit board only, \$29.00; 86-contact edge connector with gold-plated contacts only, \$5.70; 8 volt power supply including plug-in transformer/bridge rectifier/filter capacitor assembly, $\$ 8.95$. Postage, handling and insurance charges for U.S.A. orders: complete Product Board kit, \$2.00; complete Programming Board kit, \$2.00; Product Board pc board only, \$1.50; Programming Board pc board only, \$1.50; 86 -contact edge connector only, $\$ 0.50$; 8 -volt power supply, \$0.80. Connecticut residents, add state sales tax. Toll-free telephone number for ordering purposes: 1-800-243-7428.


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# INEXPENSIVE <br> AUTO BATTERY TESTER 

BY HANK OLSON

## Simulates 200-ampere starter-motor load

AN AUTOMOTIVE battery works very hard, especially when cranking the engine, and if you have a plethora of electrically operated accessories that often draw more power than the unaided alternator can deliver, it may not have a full charge to work with. Even a battery that loafs most of the time may age to the point where it can no longer start the engine on a cold day, so it's a good idea to check your battery's health now and then.

Numerous tests can be made on a battery, and all of them give some indication of its condition. But none is as conclusive as checking its performance under load. To do that you need a professional battery tester, an inexpensive version of which you can build, as described in this article.

The Circuit. The battery tester, shown schematically in Fig. 1, assumes the test current to be 200 amperes at 12 volts. (To determine appropriate load current, refer to the box.) Using Ohm's Law and assuming a 12 -volt battery, you can readily see that load resistor $R 2$ 's value would have to be a very low 0.06 ohm ( $R=E / I=12$ volts/ 200 amperes $=0.06 \mathrm{ohm}$ ). Furthermore, its power rating would have to be a whopping 2400 watts ( $P=I E=200$ amperes $\times$ 12 volts $=2400$ watts). Clearly, you're not going to find a resistor with these ratings in your local electronics parts store. Fortunately, however, you can fabricate your own power resistor from available inexpensive materials.

Continuing with our example of 12 volts and 200 amperes, you'll need about 12 feet of $1 / 2$-inch wide, 0.025 -inch thick steel banding strap (used to cinch wooden packing cases) to fabricate $R 2$. Connect the strap in series with an ammeter that can handle at least 2.5 amperes across a variable power supply capable of delivering up to 1 volt at more than 2

amperes. Adjust the power supply for a 2-A output and measure the voltage across the load. If it is over 0.12 volt, trim the strap until it equals 0.12 V .

Turn off the power supply and disconnect the test setup. You've now determined the length of steel strap to use for a 0.06 -ohm load resistor. (You can use the same test setup to determine the length needed for any other battery voltage/power ratings simply by changing
the voltage or/and current to the appropriate values in the formulas that are provided in the box.)

You're not likely to find a switch that can handle 200 amperes in an electronic parts store, but a conventional 12 -volt automotive starter solenoid ( $K 1$ in Fig. 1) will fill your need. Operating current for the solenoid is controlled by normally open pushbutton switci $S I$.

Meter M1, resistors $R 3$ through $R 6$.

and diodes $D I$ and $D 2$ make up a 0 -to6 -volt de voltmeter. When connected in series with 10 -volt zener diode $D 3$, this meter circuit becomes an expandedscale 10 -to- 16 -volt de voltmeter. Diode D2 protects the meter against reverse polarity, while diode $D /$ protects against overvoltage when the meter is connected in proper polarity.

When selector switch $S 2$ is set to poLARITY, $L E D I$ glows green if the tester is connected to the battery in proper polarity, red when the connection's polarity is incorrect. Note that Fig. 1 shows and the Parts List specifies an integrated red/green LED assembly for LEDI. If you wish, you can replace this with discrete red and green LEDs, connecting them into the circuit as shown for the integrated unit.

Construction Hints. As shown in Fig. 2, the best way to mount the steel strapping that makes up the load resistor, $R 2$, is on a $3 / 4$-inch plywood board, using No. 6 metal-not plastic-spacers and machine hardware. Start by drilling a $1 / 4$-inch hole spaced $1 / 4$-inch in from each end of the strapping.

Next, drill two rows of $1 / 8$-inch holes through the board, spacing the rows about 8 inches apart and the holes within each row about 1 inch apart. Then mount a metal spacer at each hole location with a $6-32 \times 1$ " machine screw, placing a large flat No. 6 washer under
the head of each screw. Mount another large flat washer on top of each spacer with a $6-32 \times 1 / 4 \cdot$ machine screw.

Mount the starter solenoid at the right rear of the plywood board and fasten one end of the steel strapping to one of its terminals. Then route the strapping back and forth from spacer to spacer. (The washers prevent the strapping from slipping off the spacers.)

Fasten a large $L$ bracket to the free

Fig. 2. U'sing this diagram as a guide and following instructions in the text, you can make up your own load resistor from $1 / 2$-inch steel banding strap.
end of the strapping with $1 / 4$-inch hardware. Then secure the L bracket and one- and two-lug terminal strips to the wood base with $3 / 4$-inch round-head wood screws.

For the front panel, you will need a sheet of 16 -gauge aluminum. Trim it to the width of the plywood base. Then, if possible, bend a $90^{\circ}$ lip, about 1 inch wide, along the panel's bottom edge (alternatively, use three large $L$ brackets) and drill three or four $1 / 8$-inch holes along the length of the lip to permit mounting the panel to the plywood base.

Machine the panel and mount on it the meter movement, integrated LED assembly (or discrete LEDs), switches, and two three-lug terminal strips. This done, mount the panel to the top front of the plywood base with $3 / 4$-inch-long roundhead wood screws.

Wire the circuit as shown in Fig. 1. Note that separate \#24 wires are used as voltage sensors and are run in parallel with the large \#4 cables that carry the actual current. The \#24 wires are used to measure the voltage at the battery before any voltage drops in the cable resulting from the high-current flow through $R 3$. When installing the \#24 wires, route them along the \#4 cables and use either lacing cord or tape to bind wire and cable together. Finish the assembly by attaching large Mueller clips or jumper-cable clamps to the free ends of the \#4 cables.

Use. To use the tester, connect the two Mueller clips (or clamps) to the battery/ charger system (at the battery's terminals) in the vehicle you wish to test and set $S 2$ to Polarity. If the LED glows green, the tester is properly connected, but if the LED glows red, reverse the connections to the battery.

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## SELECTING A LOAD

Battery testers used by professionals have built-in load resistors specifically selected for testing a range of typical automotive battery power-delivery capabilities. As a general rule, load-resistance values are calculated from a simple formula that states that the load resistor should draw half of the battery's maximum current during a voltage measurement. Since automotive batteries are usually rated in watts, rather than current-delivery capability, it is necessary to first convert to current before you can calculate the load resistance.

Using the standard power formula $P=I E$ ( $P$ is rated battery power, $l$ is unknown battery current, and $E$ is battery voltage), we obtain $I=E / P$. Now, let's assume the battery is rated at 12 volts and 4800 watts. First, we divide the power rating by 2 , obtaining 2400 watts. Plugging these values into the formula, we get $I=P / E=$ 2400 watts $/ 12$ volts $=200$ amperes.

Now, use Ohm's Law to calculate the resistance of the load: $R=E / I$, where $R$ is load resistance, $E$ is battery voitage, and / is test current (calculated above). Continuing our example, we obtain $R=12$ volts/ 200 amperes, or 0.06 ohm. Therefore, for a typical 12 -volt, 4800 -watt automotive battery, the load resistance should be 0.06 ohm at 2400 watts.

Using the procedure described above, you can calculate the required load resistor's parameters for any battery voltage/ power ratings.

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# BUILD A SUPER MUSIC MAKER 

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BY TOM CAUDLE

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here, is perhaps the most powerful and flexible music synthesizer IC on the market. Actually a microprocessor with a tone-generation program in an internal memory, it stores short segments of 25 popular tunes and three chime se-
quences, each of which is individually selectable. While this alone makes the AY-3-! 350 unusual, there's more. The chip can also address an external, preprogranmed ROM (read-only memory) (Continued on page 85)



Fig. 1. The circuit uses three ICs: the microprocessor, an eight-input NOR gate, and an optional EPROM.

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IC1-AY-3-1350 (General Instrument)
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R13-47 k $\Omega$
R14- $1.5 \mathrm{k} \Omega$
R16-180- $\Omega, 1 / 2-$ W, $10 \%$ resistor
R17-330 $\Omega, 1 / 2-$ W, $10 \%$ resistor
R19-478
R26-2.2 M $\Omega$ horizontal potentiometer
S1-Eight-section DIP switch
S2 -Five-section DIP switch
S3, S4, S6, S7-Spst normally open pushbutton switch
S5-Part of S2
SPKR-Miniature loudspeaker
T1-12-V, 1-A transformer
Misc. - PC board, sockets, etc.

Note 1: The following are available from Bullet Electronics, Box 401244, Garland, TX 75040: complete kit of parts, less IC3, SPKR, and switches, for $\mathbf{\$ 2 3 . 5 0}$. Also available separately: drilled, plated, and silk-screened pC board for \$5.00; AY-3-1350 IC (IC1) for \$12.00; 8-section and 5 -section DIP switches for $\$ 2.00$.
Note 2: Master Music, P.O. Box 448, Mt. Vernon, MO 65712 will supply the for lowing Items: sample music 2708 EPROM with more than 800 notes of popular music for $\mathbf{\$ 1 2 . 5 0 ;} 2708$ EPROM music albums with 1024 bytes containing 25 selections (more than 500 tunes avallable; write for information) for $\$ 15.00$; sample music 2716 EPROM with more than 1600 notes of popular music for $\$ 27.50$; double music albums in $\mathbf{2 7 1 6 s}$ for $\$ 32.00$.
and, by using the appropriate address, play any tune in the ROM's "catalog."

Circuit Description. As shown in Fig. 1, the Super Music Maker project consists basically of three ICs: IC1, the microprocessor/synthesizer chip; IC2, a 4078 CMOS eight-input NOR gate; and IC3, an optional EPROM (erasable programmable read-only memory) into which an "album" of tunes is programmed. The remainder of the circuit includes an audio amplifier for driving a small speaker (or the input of a power amplifier) and a power supply.

During program execution, $/ C I$ sets its address lines at pins 10 through 17 to 00 . Closing the Ext ROM-enable switch (part of DIP switch $S$ l) initiates the following sequence. When all lines are at logic low, the output of IC2 goes high and sends $Q 7$ into conduction. In turn, Q7 applies a ground to the tune address lines through D4 and D5. The output of $I C 2$ is also applied to enable pin 20 of IC3, where a logic high turns off this IC and presents a high impedance on the IC's I/O lines at pins 9 through 11 and 13 through 17. This allows the address data to be read from the same lines on which the EPROM's information appears. The tune address is then read and stored by $I C 1$. On the next cycle, $Q^{7}$ cuts off and enables $I C 3$. If $/ C 1$ 's internal tunes are selected, IC3 is disabled by pull-up resistor $R 2 I$ on enable pin 20 and $Q^{7}$ conducts continuously.

During operation, $I C l$ tests for the presence of external PROM IC3 by going to address location 377 octal and testing for octal 125. If the proper code is sensed, an internal flag in $I C l$ is set. The tune address lines are interrogated and then that particular memory location in IC3 is played. If no PROM is sensed, $I C l$ plays the internal tune at the preset tune address. The program then tests for the "next tune" switch closure; if it senses it, ICl steps to the next tune and plays it. This operation works in either the internal or external tune mode, permitting all tunes in $I C l$ and IC3 to be played. (Note that the chimes cannot be played when IC3 is selected.)

TEMPO control $R 26$ sets the speed at which ICl plays the notes, while PITCH control R15 sets the reference frequency. The waveform at pin 26 of $I C l$ is one quarter of the oscillator frequency; it can be varied over a two-and-a-third octave range, from 50 kHz to 250 kHz , with the PITCH control.

Dual in-line package (DIP) switches $S 1$ and $S 2$ provide the letter/number address selectors required for addressing a given page of tunes in $I C I$ and $I C 3$. Also part of $S I$ is the Ext ROM-enable switch, and $S 5$ is part of $S 2$. Normally open spst switches are used for RESET and next tune switches $S 3$ and $S 4$ and

| G | F | H Page Number |  |
| :---: | :---: | :---: | :---: |
| 1 | 1 | 1 | 1 |
| 0 | 1 | 1 | 2 |
| 1 | 0 | 1 | 3 |
| 0 | 0 | 1 | 4 |
| 1 | 1 | 0 | 5 |
| 0 | 1 | 0 | 6 |
| 1 | 0 | 0 | 7 |
| 0 | 0 | 0 | 8 |

Fig. 2. Page-select settings for S 1.
Shaded area is used only for the 2716. Zero indicates open switch; one, closed.
optional door-chime switches $S 6$ and $S 7$. Finally, switches F and G at pins 22 and 23 of $I C 3$ are part of $S l$.

The audio amplifier section, at the lower right in Fig. 1, consists of a pair of Darlington circuits, $Q 3 / Q 4$ and $Q 5 / Q 6$. With the arrangement shown, the circuit's output has a certain amount of exponential decay, which gives it an or-gan-like sound. By eliminating the QS/ Q6 circuit and jumpering the 12 -volt line, as indicated by the dashed line, to the top terminal on the speaker, output volume can be increased, but the tones will have faster attack and decay times, producing a sound more like a piano. If you wish, you can soften this tone by installing optional capacitor $C X$.

Programming External Memory. The system is designed to use either a $2708(1 \mathrm{~K} \times 8)$ or, with a few simple modifications, the newer $2716(2 \mathrm{~K} \times 8$ ) EPROM. Since $I C l$ can address only 256 bytes at a time, however, the high-
order address bits must be manually switched to play all the memory by closing the F and $G$ toggles in SI. Each 256byte block comprises a "page" of memory (see Fig. 2 for details on how to select individual pages), which means that a 2708 has four pages and a 2716 has eight pages of memory. Since the frequency and duration of each note are defined by an eight-bit code, a 2708 four-page ROM can accommodate a combination of 1024 notes and rests, while an eight-page 2716 ROM can accommodate 2048 notes and rests. The only limitations here are that no single tune can contain more than 256 notes and rests, and no page can have more than 25 different tunes.

The 2708/2716 series of EPROMs are static memory devices that can be programmed, erased, and then reprogrammed numerous times. Widely used in the computer industry, they are supplied by several major manufacturers at prices that make them relatively inexpensive for nonvolatile memory.

The EPROMs are programmed ("blown") by selecting an address location and applying a 24 -volt pulse to the desired program pin. Articles on building and using devices to program EPROMs with tunes of your choice have appeared in Popular ElectronICS (see August 1980). If you have the capability to program EPROMs, you can use the programming charts in Fig. 3 as a guide. The three least-significant bits (LSBs) in Chart A define the length

| Musical notation | Octal | Binary | Name | Frequency | IHZ) | Octal |
| :---: | :---: | :---: | :--- | :---: | :--- | :--- | Binary



Fig. 4. PROM memory allocations for a simple programming example.
of the note ( 0 through 7), the shortest, 0 , being a sixteenth note and the longest, 7 , being a whole note. In Chart B, the upper five bits define the actual musical pitch. Octal 077, for example, would be a middle C of whole-note duration. Depending on the system being used, the code may have to be converted to hexadecimal or binary format.
Required with each tune is an end-oftune marker of octal 377 or binary 11111111. Also required in the PROM is octal 377 at location 0 and octal 376 at the end of the last tune. The end of each page must be marked with octal 125. In a 2708, octal 125 ( 55 hex ) is required at octal locations 377 (FF hex), 1377 (1FF hex), 2377 (2FF hex), and 3377 (3FF hex). Figure 4 gives a simple programming example.

If you don't have an EPROM programmer or prefer not to blow your own PROMs, you can buy preprogrammed PROMs at reasonable cost (see Note 2 in Parts List).

Construction. Since only low-frequency digital signals are present in the Super Music Maker, the component layout is not critical. The use of a printed circuit board and sockets for the ICs are highly recommended. An ac-tual-size etching-and-drilling guide for the board and a component-placement diagram are shown in Fig. 5.

When assembling the project, be


Fig. 5. Actual-size etching and drilling guide for a printed circuit board for the project is shown above. Below is a guide for component layout.

careful to observe the polarities of diodes and electrolytic capacitors and lead locations on the transistors. Do not install the ICs in their sockets until told to do so in the following procedure.

If you decide to use a 2716 EPROM for IC3, some minor changes must be made in the circuit. Pin 19 of IC3, the +12 -volt supply-line connection for the 2708, becomes the eleventh address bit (A10) for the 2716 . An auxiliary switch with pull-up resistor ( $S 5$ and $R 24$ in Fig. 1) are used at point $M$ to allow this additional address to be switched. Then, remove R19 and D8 and jumper from point M to the R19 pad whose trace goes to pin 19 of IC3 and remove D9 and RI7 and jumper from pin 21 to pin 24 on IC3. With these modifications, there will be +5 volts on pins 21 and 24 , and pin 19 becomes address bit A 10 on IC3 for the 2716 EPROM.

After assembling the circuit, and before the ICs are installed in their sockets, apply power and check the power supply section to make sure the proper voltages are present. If all voltages are present at the night places, turn off the power and install the ICs in their respective sockets, making certain that they are properly oriented.

Exercise the usual safe-handling procedures to prevent damaging the CMOS ICs with static electricity. That is, ground yourself and tools before handling the ICs to drain off any charge. Once you pick up an IC, holding it by its narrow edges only, don't put it down until it is installed in its socket.

Checkout and Use. Set ROM-select switch Ext in the SI assembly to off, and check that all address switches are also off. Pressing RESET switch 53 should then cause the Westminster chime to play. Adjust TEMPO and PITCH controls R26 and RI5 for the most pleasing sound.

## USING THE SUPER MUSIC MAKER <br> IN A CAR, VAN OR RV

The project can be operated on a vehicle's 12 -volt dc electrical system if a negative bias is provided for the 2708 EPROM. A separate 9 -volt battery will do fine and will last a long time, since the current drawn is only 20 mA . (CAUTION: Never operate the project without negative bias or the 2708 will be damaged.) No negative bias is required when a 2716) not a Texas Instruments TMS2716) EPROM is used because only a single 5 -voit is required. Circuit changes for using the 2716 are detailed in the text

```
AO Toreador
BO William Tell
CO Hallelujatr Chorus
DO Star Spangled Banner
EO Yankee Doodle
A2 America, America
B2 Deutschland Leid
C2 Wedding March
D2 Beethoven's 5th
E2 Augustine
A4 Hell's Bells
B4 Jingle Bells
C4 La Vie en Rose
D4 Star Wars
E4 Beethoven's 9th
A1 John Brown's Body
B1 Clementine
C1 God Save the Queen
D1 Colonel Bogey
E1 Marseillaise
A3 O Sole Mio
B3 Santa Lucia
C3 The End
D3 Blue Danube
E3 Brahm's Lullaby
```

Chime $X$ Westminister Chime
Chime $Y$ Simple Chime
Chime $Z$ Descending Octave Chime

Fig. 6. Tunes programmed into the AY-3-1350 (IC1) microprocessor.

Check to see if the system is operating properly as follows. First, set section A in $S /$ to on and all other sections in both $S 1$ and $S 2$ to off. Pressing the reset switch should cause the "Toreador" song to play (see Fig. 6 for a list of the tunes programmed into the AY-3-1350 and their addresses). Randomly select a few more addresses and operate the RESET switch to determine if the proper tunes are played.

If you've installed a preprogrammed PROM in IC3's location, you can play it by simply switching from internal to external ROM with the EXT switch in the $S I$ assembly. The tune address selected by $S /$ and $S 2$ will then be activated when the RESET switch is operated and that tune will play. While the chime sequences in $I C l$ will not play with the EXT switch closed, the NEXT TUNE Switch will allow you to step through all the tunes programmed into $I C 3$

Applications. The versatility of the AY-3-1350 gives it a broad range of uses. It can be interfaced with doorbell buttons, toys, and telephones and could make a clock capable of playing a different tune every time it strikes the hour. It could also form the heart of a novel annunciator system.


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# electronic <br> BY JERRY AND ERIC EIMBINDER 

## Part 2: Comparing what's available today.

THERE are so many hundreds of electronic games on the market today that buying decisions are generally made rather quickly on the spot. In most instances, the buyer has never seen the game before and has no idea if a particular desirable feature might be available in a competing brand. So here's a guide to electronic games to help you prepare yourself before you visit a dealer.

A 10 -factor weighted rating system can be employed in evaluating electronic games available in today's marketplace. This system assigns a different number of points from 10 to 25 to each factor, depending on the importance to the user. The highest weight, 25 points, is given to "interest retention" (long-term popularity).

The rating system or "Electronic Game Evaluation Guide" does not include price. Therefore, a game with a high rating is not necessarily a good value. For this reason, ratings we compiled were used mainly to determine the games to be reviewed in this article and actual ratings are mentioned sparingly.

The same system should be helpful to you in evaluating games prior to purchase (if you get an opportunity to try the games). It is described in Table I.

Board Games (Table II) include electronic versions of old, familiar games (chess and backgammon), modified versions of conventional board games (such as "The Generals," a computerized game based on "Stratego") and new games specifically designed to take advantage of electronic technology ("Electronic Detective").

Fidelity emerged as a leader in electronic game technology in 1978 when it captured first place in a San Diego microcomputer chess tournament. Its "Chess Challenger" went undefeated in 12 games (winning 10 and drawing 2) against "Micro-Chess 1.0 " (Heath H8),
"Micro-Chess 1.5" (Radio Shack TRS80), "Micro-chess 2.0" (Commodore PET), "BORIS" and "SARGON 1" (Radio Shack TRS-80).

The newest chess product in Fidelity's line is "Voice Sensory Chess Challenger." Its artificial voice lacks the natural qualities of "Speak \& Spell," but that's about its only flaw. With a suggested retail price of \$360, "Voice Sensory


Chess Challenger

TABLEI-ELECTRONIC GAME EVALUATION GUIDE

| No. | Consideration | Comments | Weight |
| :---: | :--- | :--- | :---: |
| $\mathbf{1}$ | Interest retention | What is the probability that the game will still be <br> popular months after its initial use? | 25 |
| $\mathbf{2}$ | Player skill <br> required | How much skill and judgment is needed to play? <br> Is it too easy or too difficult? | 20 |
| $\mathbf{3}$ | Design creativity | How unusual is the game? In the case of sports- <br> based games, does it simulate on-the-field <br> action in an enioyable manner? | 20 |
| $\mathbf{4}$ | Competitiveness | Can two or more players feel active competitive <br> involvement? | 15 |
| $\mathbf{5}$ | Display realism/ <br> packaging/con- <br> trols | How clever are the graphics? How lively is the <br> action on the screen? Are the controls easily <br> manipulated? | 15 |
| $\mathbf{6}$ | Time needed to <br> learn the game | Is the game tricky to learn? If it is, an experi- <br> enced player has an advantage not provided <br> by skill but by familiarity with the system. | 10 |
| $\mathbf{7}$ | Computer as <br> opponent | Is the computer smart enough to perform as a <br> worthy adversary or to challenge the ingenuity <br> of the player? | 10 |
| $\mathbf{8}$ | Sound effects <br> Has voice or sound been used effectively to <br> enhance game play? | 10 |  |
| $\mathbf{9}$ | Game variation/ <br> random genera- <br> tion | Are a number of interesting variations of the <br> basic game offered? Is random generation <br> used, and if so, is it used effectively? | 10 |
| $\mathbf{1 0}$ | Overall execution | All factors considered, how well have the pre- <br> vious nine factors been combined to form an <br> appealing game? | 15 |
|  |  |  | 150 |

Challenger" makes good use of its 50 word vocabulary and its 224,000 bits of ROM. For the inexperienced player, it can tell how to set the board and illuminate paths to illustrate how each piece is moved. If the player needs help, the computer will recommend a move. Should the player violate the rules of chess it announces "illegal move."

The new Fidelity game can duplicate 64 of the world's greatest chess games including matches played by Morphy, Capablanca, Spassky and Fischer. The player can assume the role of either of the original participants; the computer will award points for correct moves and reveal how the game was played by the original opponents. It can, of course, also play a mean game of chess when operated at its highest skill level (ten levels are provided).

To move a piece, the player first touches its present position and then touches the new position. This is a great improvement over using the usual separate built-in keypad. A light at each position indicates that the move has been registered with the computer. The computer announces both its own moves and the player's moves. According to the manufacturer's promotional literature, the "Voice Sensory Challenger" calls out every capture; actually, it only announces the computer's captures.

Blending the Old and New. Somewhere in the city, a thief is about to commit a crime. You're a private detective armed with the latest development in sophisticated crime detection equipment, an electronic "Crime Scanner."

You press the "Clue" button on your crime scanner and the display reveals the street on which (or the building in which) the crime is taking place. But that's not all your scanner does; it also monitors the scene of the crime for sound. You hear the thief as he breaks a glass window.

The chase is on. You head for the jewelry store across the street from Ma's Ice Cream Shop. And you're not alone. Other private detectives, who are also after the reward money, are joining in the pursuit.

Now you hear the thief running along the street but you're still too far away to apprehend him. You and the other detectives continue to close in as he heads for a nearby subway station.

You're playing "Stop Thief," an electronic cops-and-robbers game, controlled by a four-bit microcomputer. Eventually, you or one of the other players will track the thief down successfully and move in for the arrest. You'll collect at least $\$ 800$ in reward money as the police arrive with sirens wailing and guns firing, unless the thief slips away. In that case, all you'll get is a
parting, "Na-na-na-an-na-na," from the thief as he escapes
'Stop Thief" is a modern-day board game; it has buildings and streets, and it's played with dice and paper money. If this description is reminiscent of "Monopoly" it should be pointed out that Parker Brothers, the creators of "Stop Thief," also developed "Monopoly" 45 years earlier.

Are they trying to rework the same formula? Both games have decks of chance cards and both involve moving player pieces along streets (although the streets in "Monopoly" have famous names such as Boardwalk and Park Place; whereas the streets in "Stop Thief' merely have numbers).

No, "Stop Thief" is not "Monopoly," updated. It is uniquely a product of the microcomputer era. It uses a complex software program, imbedded in a Texas Instruments TMS 1000 microcomputer IC by a single-level mask technique. Three parts of the microcomputer configuration are programmed simultaneously: read-only (instructions) memory, instruction decoder, and output encoder. These three sections control information storage and processing.
"Stop Thief" can be thought of as a blend of the old and the new. It retains the competitive nature of the basic board game, while introducing comput-
er-controlled challenge (finding the thief), computer-assisted play (for example, if a player forgets some clues, he can recall the last 10 clues from the microcomputer's memory), and comput-er-generated sound.

A recently published review comparing Ideal's "Electronic Detective" with Parker Brothers' "Stop Thief" came to four conclusions: (1) the two games are similar, (2) "Electronic Detective" is much more complicated, (3) "Electronic Detective" is a greater challenge, and (4) "Stop Thief"' is more appropriate for


Ideal's Electronic Detective

| TABLE II-ELECTRONIC BOARD GAMES |  |
| :--- | :--- |
| Game | Description |
| TRYOM <br> Omar V <br> Super System III | Computer employs all backgammon strategies <br> against player. <br> Moves shown on LCD display window. |
| FIDELITY <br> Sensory Chess Challenger <br> Voice Chess Challenger <br> Voice Sensory Chess <br> Challenger | Moves automatically entered/indicated on board. <br> Moves shown on LED window and announced. <br> Indicates and announces moves automatically. |
| Checker Challenger <br> Backgammon Challenger | Moves shown on LED window. <br> Moves shown on LED window; conventional dice. |
| IDEAL <br> The Generals <br> Electronic Detective* | Electronic equivalent of Stratego. <br> Killer's identity is deduced through clues. |
| PARKER BROTHERS <br> Stop Thief <br> Code Name Sector | Part "Monopoly," part "Clue," but mostly new. <br> "Aging" search-and-destroy submarine hunt. |
| EPOCH <br> Detective Game* | Sound clues, search-and-arrest buttons. |
| MILTON BRADLEY <br> Electronic Battleship | Electronic version of grid-position, sea battle. |

younger players. We disagree on all four counts.

The games are not similar even though they both employ versions of the TMS 1000 microcomputer. "Stop Thief," like "Monopoly," is a board game; that is, apprehending the criminal depends upon being in the right place at the right time. Getting there is a combination of luck (resulting from the roll of the dice) and skill (knowing where to move and when). "Electronic Detective" is not a board game; it is a game of deduction, careful scrutinization of alibis and weighing of evidence.
"Electronic Detective" is no more complicated than "Stop Thief" and every bit as easy to learn. "Stop Thief," because the criminal is constantly on the move, presents a different kind of challenge. In one case, the players are trying to overtake a fleeing criminal; in the other, they are trying to determine his or her identity.

As "Electronic Detective" begins, a murder takes place. Now it's up to each player to determine who the killer is through questions keyed into the computer. Alibis of suspects must be carefully checked because even innocent suspects may sometimes lie.

As suspects are interrogated, evidence is uncovered. The player wins if he correctly identifies the killer. But, be care-
ful, a false accusation removes the player from the game. The computer plays the funeral dirge for each detective who is eliminated.

More than 130,000 different crime situations are possible. Players can choose from three levels of skill: gumshoe, sleuth or master detective.

Anyone who likes to solve a mystery, should enjoy playing either "Electronic Detective" or "Stop Thief." The two games differ not only in the type of criminal being tracked down (in "Electronic Detective," he's a murderer; in "Stop Thief," he's just a crook) but in basic concept and method of play. Both games are appropriate for younger players once an experienced player has explained the rules and briefly discussed strategy considerations.

Generals Battle It Out. Ideal's "The Generals" is a computerized version of Milton Bradley's board game "Stratego," which, in turn, was an American version of a game developed in Holland. The marshall in "Stratego" is now a five-star general and the nearly powerless spy has been replaced by an extremely dangerous agent, but the cast of players is generally the same. So is the object, which is to capture the opponent's flag.

In both games, the players are supplied with armies and move their troops

| No. | Consideration | Electronic Detective | Stop <br> Thief | The Generals |
| :---: | :---: | :---: | :---: | :---: |
| 1 | Interest retention | 23 | 24 | 21 |
| 2 | Player skill required | 17 | 18 | 12 |
| 3 | Design creativity | 18 | 19 | 14 |
| 4 | Competitiveness | 15 | 15 | 15 |
| 5 | Display realism/packaging/controls | 15 | 13 | N/A |
| 6 | Time needed to learn the game | 6 | 7 | 10 |
| 7 | Computer as opponent | 10 | 10 | N/A |
| 8 | Sound effects | 9 | 10 | 7 |
| 9 | Game variations/random generation | 10 | 10 | N/A |
| 10 | Overall execution | 15 | 15 | 15 |
| Total Points |  | 138 | 141 | 93 |
| Max. Possible Points |  | 150 | 150 | 115 |
| Score (\%) |  | 92 | 94 | 81 |

N/A $=$ Not Applicable

# TABLE IV-ELECTRONIC ROAD GAMES 

| Game | Description |
| :--- | :--- |
| BANDAI <br> Champion Racer | 5-gear racing game with separate starting line. Two- <br> button steering control. |
| KENNER <br> Redline | 4 gears. No lanes to drive. Player controls engine speed. |
| MATTEL <br> Auto Race | 4-speed gear shift. Sliding control for steering. Zings <br> for victory. Buzzes for defeat. |
| TIGER <br> Raceway | 4-gear racing game. Counts number of collisions. Trum- <br> peting sound for victory; buzzer sound for defeat. |

across a battlefield. But the addition of an electronic judge in "The Generals" does have its benefits. In an encounter between two battle pieces, the computer identifies the winning piece without revealing its rank. This is a major improvement over "Stratego," where opponents must reveal the ranks of their pieces in each confrontation. Not knowing the ranks of an opponent's remaining pieces forces a player to make more assumptions and use more risky strategic planning.

The role of the electronic arbiter is limited to revealing which of two pieces, engaged in battle, has the higher rank. "The Generals" is strictly a two-player game; one player cannot oppose the computer.

When scored by using the rating system described earlier, "Electronic Detective," "Stop Thief," and "The Generals," each tallied over $80 \%$, as shown in Table III. A score of $65 \%$ or better is considered acceptable.

Road Racing (Table IV). You might call Kenner's "Redline" a dashboard game. It is designed as a replica of a dragster steering wheel and has a digital console with gear and gas controls, gear and rpm readouts and a string of engine status lights.

The idea of the game is to control the engine so as to win the race without jumping the start signal or exceeding the safe limit (redline) for engine speed.


Tiger Raceway
If the player "blows" his engine while shifting gears from neutral through fourth, his foul indicator lights up and he must restart the race. Steering along a raceway is not involved. The sound effects are impressive and include "revving" of the engine, engine blowout, and screeching tires.

Mattel's "Auto Race," Tiger's "Raceway," and Bandai Electronics' "Champion Racer" are all based on driving on a track. The Mattel and Tiger games are very similar. The object of
both games is to cover four laps in as short a time period as possible. If the four laps haven't been driven after 99 seconds have elapsed, play ends.
"Raceway" has several nice touches. Its miniature steering wheel and throttle are more realistic than would ordinarily be expected from a car-racing game. Both provide the equivalent feel of fullscale arcade games as the player shifts gears and changes lanes. If the driver successfully completes the required four laps, "Raceway" displays both elapsed time and the number of collisions with oncoming vehicles along the way. Mattel's game only displays elapsed time (or the 99 -second time limit).
(another way in which "Champion Rac-


## Bandai Racer

In both games, the computer sends vehicles into the same driving lane being used by the player; constant reaction by the player, therefore, is needed. Each collision knocks the player's car back one space. If the player doesn't move his car from its lane immediately after a collision, the other car will continue to push him backwards. The cars are displayed as lines, not graphic images. The line representing the player's car is a brighter red than the other lines, which is sufficient for distinguishing between the player's car and other vehicles. However, the pictorial representation of moving cars, such as used by Bandai Electronics in "Champion Racer," is visually superior.

In "Champion Racer," the player has 90 seconds to cover as many miles as possible. He can shift through five speeds (as compared to four with Tiger's "Raceway") and he can move into reverse (not possible with "Raceway").

To start "Champion Racer," the player moves his car from the pit onto the racetrack. He must be careful as he works his way up the field because cars can come at him from either direction
er" differs from "Raceway"). Points are scored by avoiding collisions on the track; the faster the driving speed, the more points that can be accumulated. Each time a collision occurs, the player must start again from the pit.

If a player covers more than 99 miles, the two-digit light-emitting-diode display switches from digital to alpha/ numerical readout ( $\mathrm{Al}=101$ miles, $\mathrm{Cl}=121$ miles, etc.). The display flashes with 10 seconds left to play alerting the player that the game is ending.

Electronic Games of Chance (Table V). Picture yourself playing Gin. Your opponent says "no thanks," as you discard the ten of clubs. It's his last decision. On your next move, you draw the card you need for Gin. You've just beaten Mattel Electronics' "Computer Gin" at the low-skill setting. Now you're ready to play it at the high-skill level.

Besides two skill levels, Computer Gin also can oppose you in Go Draw or 33.

In Gin, you receive 11 cards, face up, on the display. Your hand is unknown to the computer; its hand is unknown to you. You dispose of the least desirable card by pushing the discard button. The discarded card then disappears from the screen; the computer either accepts it with "thanks" or picks a fresh card from the deck. It's your turn again and, automatically, a new card from the deck flashes on the screen. If you want it, you push the select button and again dispose of the least desirable card. If you don't want it, you hit the draw button and receive a different card. And so on, until either you or the computer gets Gin.

Don't get too excited if you draw a great poker hand against the dealer (the


Entex Poker

TABLE V-CARD AND CHANCE GAMES

| Game | Description |
| :--- | :--- |
| BAMBINO <br> Blackjack | Has bet, hit, stay buttons and keyboard. Includes sound <br> effects. Also plays poker and bridge. |
| ENTEX <br> Electronic Poker <br> Jackpot Gin Rummy | Plays 5-card draw poker. Has deal, bet and call buttons <br> plus keyboard. Provides sound effects. <br> Plays Gin and Vegas-rules blackjack. |
| FIDELITY <br> Voice Bridge <br> Challenger <br> Bridge Bidder | Announces bids in accepted bridge terminology. Can <br> be partner or opponents. <br> Learning aid. Player enters hand. Bid is suggested by <br> computer. |
| MATTEL <br> Computer Gin | Computer can oppose you in three different card games: <br> Gin, Go Draw, and 33. |
| TRYOM <br> Goren Bridgemaster | Uses 40-character keyboard. Has 8-character alphanu- <br> meric display. Programmable. |
| UNION MAJOR <br> Yacht Four | Rolls dice numbers electronically. Provides musical <br> cheer for winner. |
| UNISONIC | Has insurance, splitting, doubling down, hit, stand, bet <br> and play buttons; 12-digit display. |

computer) when playing Entex's "Electronic Poker." The chances are good that the dealer will fold quickly.

Both hands are dealt face down on an LED display. As in conventional draw
poker, the player can discard existing and draw new cards. He can then bet or fold. After the first bet, two dealer cards are revealed. The player can call or bet again, and another dealer card is re-

TABLE VI-WARFARE GAMES

| Game | Description |
| :--- | :--- |
| ATARI <br> Space Invaders | Hand-held version of popular arcade game. |
| BAMBINO <br> Space Laser Fight <br> UFO Master Blaster | Robots shoot it out on outstanding visual display. <br> Simplified version of Space Invaders. |
| BANDAI <br> Missile Invader <br> Super Galaxy Invader | The enemy doesn't try to land in this version of Space <br> Invaders. <br> Deluxe version of Missile Invader. |
| ENTEX <br> Space Invader | Well-executed version of the arcade game. |
| EPOCH <br> Invader From Space | Three skill-level version of Space Invader. |
| KENNER <br> Star Wars | Object is to determine grid position of enemy and <br> destroy. |
| MATTEL <br> Space Alert | Mini Space Invaders. Also called Battlestar Galactica <br> and Flash Gordon. |
| Sub Chase | Drop depth charges while dodging torpedos. |
| Armor Battle |  |$\quad$| Duel enemy tank while avoiding line mines. |
| :--- |

table vil-follow the leader games

| Game | Description |
| :--- | :--- |
| ATARI <br> Touch Me | Offers three games requiring repeating of lights, sounds <br> and colors. |
| CASTLE <br> Einstein | Player repeats colors and sounds. |
| MILTON BRADLEY <br> Pocket Simon <br> Super Simon | Miniature version of Simon. <br> Player duplicates colors and sounds. |
| TIGER <br> Copy Cat | Sequences of lights and sounds. Speed up as game <br> progresses. |

TABLE VIII-CODE-BREAKER GAMES

| Game | Description |
| :--- | :--- |
| INVICTA <br> Electronic Mastermind <br> Supersonic Master- <br> mind | Break the secret code set by the computer. <br> Deluxe Electronic Mastermind with sound. |
| LAKESIDE <br> Computer Perfection <br> Le Boom | Four games based on turning lights on in sequence. <br> Search for clues to defuse the bomb. |
| MILTON BRADLEY <br> Comp IV | A correctly guessed number is indicated by flashing <br> lights. |

vealed. The player can bet again, or call and the computer will reveal the winning hand, display the winnings or losses and the accumulated totals.

Union Major's "Yacht Four" is an electronic 5 -dice game similar to "Kismet," "Yahtzee," "Poker Dice," and "Casino Games" (trademarks of SpareTime Products, Inc.; E.S. Lowe Co., a Milton Bradley Company; Aurora Products Corp.; and Invicta Games, respectively). The readout displays the most strategic move to make after each roll.

Land, Sea, Air, and Space Warfare Games (Table VI). Most of the handheld space-warfare electronic games are similar in concept to the popular arcade game, "Space Invaders." Exceptions are Kenner's "Star Wars," which puts the player in command of the "X-Wing Fighter" in combat with a "Tie-Fighter," and Bambino's "Space Laser Fight," a space version of a shootout in the Old West.

In "Space Invaders," an alien squadron is heading toward the earth. The player defends his planet by destroying the attackers as they attempt to land.

The Entex version is remarkable in its duplication of most of the features of the arcade and video cassette games. The pace of the attack, the rapid fire by the enemy ships, the destruction of the defense shields, the cruising mother ship and the landing on earth are expertly programmed. The visual display provides spaceship replicas not lines as several other games offer.


Follow-the-Leader Games (Table VII) are simply games that test the memory of the player in some way. He might be asked to duplicate a string of numbers or a series of musical notes. Some of the products listed in the multifunction category include follow-theleader games among other types.

Code-Breaker Games (Table VIII) require the player to identify unknown information generated by the computer. Trial-and-error or deduction techniques are employed.

To be continued next month.

# Pulse Amplitude Reference 

## Easy-to-build circuit amplifies input

 pulses when calibrating an oscilloscope.BY IMRE GORGENYI

WHEN working with very narrow, low-amplitude, low-energy pulses, it is advantageous to calibrate the scope you are using at actual working conditions with probes and attenuators in place. This is not always easy since pulse amplitude calibrators usually have low-level outputs-often under one volt.
The simple, all-FET circuit shown here provides pulse amplitudes to 25 volts, if desired, from pulse inputs of 5 to 20 volts. Since the two FET pairs are driven to their open-saturated condition, output pulses have the same amplitude as the power supply voltage. Absolute amplitude of the output can be monitored by a DVM at all times.

The circuit can handle narrow pulses


The circuit can be assembled on a perforated board.
and slow repetition rates. It can be assembled on a small piece of perforated board using BNC connectors for the input and output and can be connected directly in the 50 -ohm line between the
pulse generator and scope. The circuit, including power supply and digital voltmeter connectors, is "floating." Circuit protection is formed by the fuse and zener diode.

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Frankly, it wasn't easy. But the MX-80 could only have come from the world's largest manufacturer of print mechanisms. Epson.

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 be reliable like all Epson Printers, and to be produced on a scale that would allow us to charge less for each one. The MX- 80 is our proof that it can be done.

Among its features, the MX-80 prints 96 ASCII, 64 graphic and eight international characters in a tack-sharp $9 \times 9$ matrix. It prints bidirectionally at 80 CPS with a logical seeking function to maximize throughput. And it has the world's first disposable print head.
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 could do it all at a price you could afford, you've got to see the Epson MX-80.

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- FOR/NEXT loops nested up 26.
- Variable names of any length
- BASIC language also handles full Boolean arithmetic, conditional expressions, etc
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- Timer under program control
- PEEK and POKE enable entry of machine code instructions, USR causes jump to a user's machine language sub-routine.
- High-resolution graphics with 22 standard graphic symbols
- All characters printable in reverse under program control
- Lines of unlimited length


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system, and monitor. And the MicroAce 1 K byte

RAM (expandable to 2 K on board) is roughly equivalent to 4 K bytes in a conventional computer - typically storing 100 lines of BASIC. (Key words occupy only a single byte.)

The display shows 32 characters by 24 lines
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No other personal computer offers this unique combination of high capability and low price.

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By Forrest M. Mims

## For Sale: Free Energy from the Sun

SOLAR energy proponents sometimes become so enthusiastic over the free availability of sunlight that they overlook the cost of purchasing and maintaining the equipment necessary to tap and store it. Recently, for example, a specialist in the solar heating of homes appeared as a guest on a popular radio talk show. In response to a listener's question, the guest ventured into an aspect of solar energy conversion about which he knew very little: photovoltaics. He made some elaborate claims about how roof-top panels of solar cells could supply all the electrical needs of a home but failed to mention the enormous expense at today's prices of the panels, storage batteries, voltage regulators and inverters.

If you've ever looked into the subject, you know that the current price of solar cells is a long way from the goal of $50 ¢$ or less per watt set by the U.S. Department of Energy. Unless you can afford to buy in large quantities, the average experimenter will have to pay at least $\$ 15$ for enough solar cells to generate one watt of electricity!

Many companies that sell parts to experimenters also sell solar cells, and the price per watt for these cells varies widely. To illustrate some typical variations, here's a table comparing the current output ( $\mathrm{I}_{\text {out }}$ ) and cost for a $3^{\prime \prime}$ circular silicon solar cell having an open-circuit voltage of 0.45 V .


Fig. 1. Ferranti ESC-3 solar-cell module.
The extraordinary price of the Ferranti cell, which is shown in Fig. 1, is due to its unique protective housing and electrical connection pins. All the others are bare cells without connection leads.

The parts dealers who sometimes sell manufacturers' culls may buy their solar cells from more than one source. I learned this the hard way a few years ago upon receiving an order of $3^{\prime \prime}$ cells. They appeared to have come from two different sources. Most of them had cosmetic blemishes, and only some were $3^{\prime \prime}$ cells. The others were slightly smaller.

You can avoid this problem, if top efficiency is your quest and money is no problem, by purchasing the more costly first-run cells or buying them from a retail electronics parts store so you can inspect them before buying.

Another tip: Since dealers may buy cells from different sources, always compare prices on any size cell you plan to buy. A dealer whose $3^{\prime \prime}$ cells are very

| Company | Stock No. | $\begin{aligned} & I_{\text {out }} \\ & \text { (A) } \end{aligned}$ | Coll Cost (\$) | \$/Watt |
| :---: | :---: | :---: | :---: | :---: |
| Chaney Electronics | C24159 | 0.75 | 8.60 | 25.19 |
| EDI | E. 43 | 1.0 | 6.88 | 15.29 |
| Edmund Scientific | 42,270 | 1.2 | 19.96 | 36.94 |
| Ferranti | ESC-3 | 0.8 | $23.50{ }^{*}$ | 58.75 |
| Newark | 69F 121 | 1.2 | 17.00 | 31.48 |
| Poly Paks | $92 \mathrm{CU3862}$ | 1.0 | 7.95 | 17.67 |
| Radio Shack | 276-123 | 1.0 | 9.99 | 22.20 |

The cells in this table are made by several manufacturers. The more expensive ones have no defects, while some of the cheaper ones may include culls or seconds. For example, some may have cosmetic blemishes which might reduce slightly the cell's power conversion efficiency (the ratio of solar power striking the cell to electrical power produced by the cell). Others may be inherently less efficient ("below spec") than standard cells due to manufacturing defects.
economical may sell very expensive 2 cm $\times 2 \mathrm{~cm}$ cells.

Practical Solar Cell Applications. By now you probably have the impression that I'm not a solar-energy enthusiast, but quite the contrary is true. Even at today's ridiculously high prices, solar cells can be used in costeffective power supplies.

For example, you're probably familiar with several different widely adver-

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## Easy to read

The natural side-view display lets you tell the time, day and date without twisting your arm into an uncomfort able position
Numbers always visible
Four varying light intensities are built into the viewing display, allowing the Sunwatch to adjust automatically to any light This means you can always read it, even in the brightest sunlight.

## 10 Display functions

The Sunwatch is capable of displaying the following information: hours • minutes • seconds • months • date - day • leap year • speed calibration • AM/PM indicator - seconds count-off

## Extreme accuracy

Unlike other electronic watches using tuned crystals to control timing accuracy, the Sunwatch incorporates a unique, programmable, microcircuit synthesizer to make it the first watch in history that is accurate to less than 1 second per month. Thats 5 times more accurate than the latest quartz Accutron.

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Tiny silicon power cells, which are constantly be'ng energized by natural sunlight, daylight or an ordinary light bulb keep the Sunwatch energy storage system charged Should the watch not be exposed to light, it will continue to operate for months on stored power
The most indestructible watch in the world The workings of the watch solar panels, energy cells quartz crystal, computer on a chip, etc., are all perma nently sealed in a Lexan module. This module is so unique its protected by U.S. and foreign patents

## Completely waterproof

Leave the Sunwatch in salt water for months. Dive with it in depths up to 750 feet. There are no openings magnetic slide bars activate all functions. With Sun watch's exclusive, permanently sealed Lexan module, there are no " D " rings or seals to leak


You can crash it into a rug-surfaced brick wall at 90 mph with no noticeable effect Wear it while doing heavy work, exercise or any strenuous activity

## Temperature resistant

Put the Sunwatch in boiling water for 30 minutes freeze it in a block of ice for a year. Extreme temperatures will not damage your Sunwatch

## Pressure resistant

There are no air spaces inside the Sunwatch. There fore, it is not susceptible to high pressures such as might be encountered diving to great depths.
The perfect watch for a lifetime
Imagine split-second accuracy for the rest of your life Sunwatch is a virtually indestructible beautifully styled, space-age timepiece, and it's avalable in three exciting finishes Brushed stainless steel, Gold tone stainless steel, or a Durable black finish on stainless steel. All Sunwatches come with a matching stainless steel band with removable links and adjustable clasp.

## Made in the United States

The Sunwatch, designed by Roger Riehl, was being worn by its inventor nearly a year before the first electronic digital watch was even available to the general public Since that time constant engineering evalu ations and design improvements have been made on the Sunwaich to incorporate the latest in digital microcircuit and solar power technology Thus the Sunwatch today represents state-of-the-art electronics technology It is built to the same rigid standards practiced by the manufacturer in creating sophisticated computer micro circuits for the U S Government and other major users of these components.

## * A word about other "Solar Watches

hoger hiehl, designer of the Sunwatch, states that there 15 no other completely solar powered watch on the market today Claims of solar power by other watch manufacturers are based on the use of a small solar cell. Due to their limited size, these cells can be proven, in technical terms, to be of virtually no significant value in extending the life of a watch battery. For this reason, all other so-called "solar watches" must have replace able batteries. The Sunwatchs power storage system however, need never be changed and is, in fact, permanently sealed to withstand abuse and the elements


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tised solar-powered watches and calculators. These devices aren't gimmicks. The solar cells provide a trickle charge to one or more nickel-cadmium storage cells, even in subdued light. The solar cells used in these products are so small that their cost is perfectly reasonable for the purpose they serve.
A typical reader cannot begin to afford to purchase enough solar cells to power his home. (Nor can the typical columnist.) Nevertheless, there are some very practical household and recreational applications for solar-cell power supplies, the most obvious being battery charging.

Some boat owners use small solar-cell

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panels to maintain a charge on their rig's storage battery. The battery can then be used to start an engine, operate a bilge pump or power the running lights.

Hikers and campers can also benefit from small solar-cell battery chargers. Several companies sell small solar-cell panels specifically designed to charge small batteries that can then be used to power flashlights and radios.

If you're a do-it-yourself enthusiast, you may be able to save some money by building your own solar panels. The November 1976 installment of "Experimenter's Corner" (Popular Electron. ICS, p. 110) described a couple of homemade solar cell battery chargers I once assembled for use on a bicycle tour in the mountains of New Mexico. Those two panels are now four years old and are still working well. They've certainly paid for themselves several times over, and, hopefully, will see many more years of service.

Their latest assignment was to recharge the lighting system on my bicycle during a recent bike tour to the Texas Gulf Coast. One panel charged the NiCd cells in a homemade miniature xenon strobe light that clips onto my cycling shorts while night riding. The other panel charged a 6 -volt light mounted on a headstrap.

Figure 2 is a photograph showing both these solar chargers hard at work atop a chunk of volcanic rock in New Mexico's Jemez Mountains. The small panel at left recharges two NiCd AA cells installed in a holder mounted on the panel's back. A small blocking diode prevents the batteries from discharging through the solar cells during periods of darkness.

The larger panel at right is charging a series-connected module of four AA batteries attached to the headband of a 6 -volt headlight. When fully charged the batteries will power the light for about 55 min . continuous operation.

The small meter in the photo monitors the charging current of the larger panel. Thanks to the very clear New Mexico sky, the panel was generating more than 90 milliamperes when the photo was snapped. Here at my home in


Fig. 2. Solar-cell arrays shown here are being used to recharge batteries on long bicycle trips.
south central Texas, almost exactly four years later, this same panel is charging a NiCd pack with a current of slightly more than 85 milliamperes as this is being written.

How to Build One. If you want to try building your own solar panel, your best plan of action is to collect as much manufacturer's literature as possible before selecting and buying the cells and storage cells. The latest edition of McGrawHill's Electronics Buyers' Guide lists 39 companies that sell or make photovoltaic cells. Here are some companies that have recently supplied this column with good data sheets and applications information:

- Edmund Scientific Co. (150 Edscorp Building, Barrington, NJ 08007)
- Motorola Semiconductor Products (Box 20912, Phoenix, AZ 85036)
- Sensor Technology, Inc. (21012 Lassen St., Chatsworth, CA 91311)
- Solarex Corporation (1335 Piccard Drive, Rockville, MD 20850)

The applications literature from Solarex is particularly good, although I've yet to see an applications note that covers thoroughly all the considerations involved in the design of even a simple solar-cell battery charger. You can com-

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## solid-state

bine applications guidelines from Solarex and other firms with the specifications for various solar panels sold by these same companies to design your own custom panel. Here are a few important points to remember:

1. A typical solar cell has an opencircuit voltage of 0.45 volt. Connect cells in series to boost the voltage
2. Connect cells in parallel to boost the current output.
3. Make sure the finished panel does not deliver too much current to the batteries it is intended to charge. Big 1 ampere cells may be fine for charging large storage batteries, but they are totally unsuited for charging AA, C and other small NiCd batteries.
4. Solar cells are very brittle. Use care when handling them. Protect them from damage with a clear protective housing.
5. Use care when soldering connection leads to solar cells. See the August 1980 issue of Popular Eiectronics (p. 90) for tips about soldering leads to solar cells.
6. For best results, use four seriesconnected cells for each NiCd cell you plan to charge. One additional solar cell per array will be needed if you use a blocking diode (and you should). Therefore you will need nine cells to charge two series-connected NiCd batteries (see Fig. 2).
7. Make sure all the cells in an array are equally illuminated. Blocking the light to most or all of but one cell in a series-connected array will reduce the output to almost nothing.

## New Solar-Cells Developments.

In recent years several breakthroughs in photovoltaic technology have occurred. One is the growth of continuous ribbon or film solar cells from silicon. Another is the development of potentially inexpensive solar cells made by coating thin films of polycrystaline silicon on a graphite substrate.

Many other kinds of silicon photovoltaic devices are also undergoing development, but many solar cells of the future may be made from gallium arsenide or cadmium sulfide, not silicon. Galliumarsenide solar cells are more expensive to make than silicon cells, but they have demonstrated a much higher efficiency ( $26 \%$ vs. $22 \%$ )

Cadmium-sulfide photovoltaic generators have a maximum possible power conversion efficiency of only $16 \%$. So far more than $9 \%$ has been achieved in the laboratory. While this isn't nearly as high as silicon or gallium arsenide, cad-mium-sulfide cells are potentially very inexpensive. Some researchers predict a cost for such cells of only 10 c to 30 c per watt by 1990 ! One scientist has predicted a cost of $35 ¢$ per watt as early as 1982. Should that happen, people can begin thinking seriously about deriving some of their household electrical power from sunlight.

Miniature Solar Cells. As noted earlier, solar cells are being used to power

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electronic watches and calculators. Surprisingly small cells can be employed, thanks to the ultra-low power consumption of the CMOS circuits used in these products. Actually, small cells must be used since the tiny NiCd batteries that power these products must be charged at very low current levels.

I learned about the latter point firsthand a few months ago after building a miniature ( $1^{\prime \prime} \times 3^{\prime \prime}$ ) radiation detector. The detector is powered by a single $1.2-$ volt NiCd button cell, and I wanted to design a midget solar charger for it. Unfortunately, none of the solar cells in my
limited benchstock were small enough to deliver less than the maximum allowable current to the cell!

Several companies make miniature solar cells and panels. Three new entries from Japan's Sharp Corporation are se-ries-connected arrays designed to power watches with a liquid-crystal display. These tiny arrays contain 5,6 or 8 cells and produce $1.5,1.8$ and 2.4 volts, respectively. The cells, which measure 2 $\times 5 \mathrm{~mm}$, are slightly smaller than a 3digit number on the reader's service card in this magazine. The cells are mounted on a thin Kapton film.

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Probably the most interesting miniature solar-cell developed thus far was announced earlier this year by NASA. Conventional silicon solar cells must be soldered together in series to produce voltages higher than the 0.45 volt available from a single cell. NASA's new development, which is not yet available commercially, allows an array of seriesconnected cells to be formed on the surface of a monolithic silicon substrate.

In one version, the silicon substrate includes a sine-wave generator integrated adjacent to the solar-cell array to provide an ac output. Future uses for integrated circuits with on-chip solarcell arrays include chips which are powered solely by light.

Super Capacitors. Two Japanese companies, NEC Electron and Panasonic, have announced a new kind of electrolytic capacitor with up to severalthousand times the capacity per unit volume of conventional electrolytics. Panasonic will soon be selling a complete line of 1-to-100-farad (that's farad, not microfarad!) capacitors small enough to fit on a pe board. Panasonic will be selling a line of "Supercaps" ranging from 0.1 to 1 farad.

There are some exciting applications for small capacitors with such huge capacities. Super long timing and power backup for CMOS memory chips are two possibilities.

Size? One of NEC's 0.47-F Supercaps comes in a squat cylinder about the diameter of a $C$ cell and a fourth as long. I've not yet seen prices for these new capacitors, but they should be about the same as conventional electrolytics.

Midget ICs. Figure 3 shows a new IC package Signetics offers as an option for 13 of its standard analog chips. The new microminiature package is one-fourth the size of a standard 8-pin mini-DIP.

Fig. 3. Microminiature IC package now available from Signetics for

## 111

Signetics for
thirteen of its
analog chips.

The new package has exciting possibilities for circuit miniaturization. A bonus feature of its size is much better ac specifications than standard DIPs.

Are the new chips too small for experimenters to use? Based upon my experience with flat-pack ICs, I suspect a careful experimenter could solder these new chips to a pc board by using reflow soldering. This is a soldering method wherein solder is applied to both halves of a prospective connection. The two solder-coated terminals are then placed together (perhaps secured by a small clamp or with tape) and the connection is reheated. The solder on the two halves of the connection melts together to effect a sturdy bond. A magnifying lens and a soldering iron with a very small tip will be essential.

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System Monitor (Terminal Version): 2 k bytes of deluxe system monitor ROM located at Fhem. leaving \%反6f free for user RAM/ROM. Features include tape load with labeling ... examine/change contents of memory . . . insert data . . . warm start . . . examine and change all registers . . . single slep with register display at each break point. a debugging/training feature...go
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System Monitor (Hex Keypad/Display Version): Tape lodd with labeling... lape dump with labeling
examine change contents of memory ... insert data
warm slart . . examine and change all registers
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single step with register display at each treak point go to execution address. Level " $A$ " in this version makes a perfeod controller for industrial applications. and is programmed using the Netronics Hex Koypad/ Display. It is low cost. perfect for beginners.
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| 115 | LM 4250CH | 95 |
| 495 | CA 3130I | 225 |
| 98 | LM 4136N-14 | 95 |
| 135 | LM 383I | 125 |

SOCKETS

| SOCKETS |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Low Wire Wrap \#3 |  |  |  |  | Low Wre Wrap \#3 |  |  |
|  | Protile | Gold | Tin |  | Prodie | Gond | In |
| 8 Pin | 14 | 49 | 29 | 22 P17 | 28 | 99 | 69 |
| 14 Pim | 16 | 57 | 37 | 22 PII | 16 | 99 | 70 |
| 16 Pin | 20 | 61 | 41 | 24 PIn | 36 | 110 | 85 |
| 18 P\% | . 10 | 89 | 59 | 28 PIn | 39 | 139 | 110 |
| 20 Pin | 28 | 99 | . 69 | 40 Pin | 55 | $\dagger 75$ | 140 |

## SPECIALS OF THE MONTH While Supply Lasts

ECL 10146L RAM 1024 X) 29 ns
AM 3705 PMOS $8-\mathrm{Ch}$ Multiplexer
MM 500H Dual 25-8it Shift Register
TR1671 Asyn/Sync. Receiver - Transmutter
95 H 90 H -Speed Div $10 / 11$ Prescater
MCT6 Dual Opto. Isolator 1500V
ICL $7107 \mathrm{CPL} 31 / 2$-Digir A/D Conv LED
AY-5-2376 Keytoard Encolder. 88 Keys
AY-5-3600 Keyboard Encoder 90 Keys
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# Hobby Scene 



By John McVeigh, Technical Editor

## Video Bandwidth Limitations

Q. I recently modified a solid-state, black-and-white television receiver for use as a terminal in a TRS-80 computer system. My problem is that the dots which comprise the displayed characters are smeared horizontally to such an extent that neighboring dots overlap. This slight overlap makes the horizontal portions of displayed characters too bright. Therefore, legibility is poor-even when the receiver's brightness and contrast controls have been adjusted for "optimal" performance. I applied the video signal from the computer interface to the base of the video amplifier. Removing the 4.5-MHz sound trap did not improve the
situation. Any suggestions? -Dennis Kehoe, Flint, MI.
A. It is obvious from a glance at one of the schematics you included with your letter that you consulted the TV TYPEWRITER COOKBOOK by Don Lancaster. I really can't offer you much advice that does not appear in Don's book. The major techniques for improving video resolution (which is to a large degree a function of video bandwidth) that Don suggests are careful adjustment of brightness and contrast controls (lowest possible brightness and contrast are best), removal of the sound trap, mini-

## Adding a Center Tap Revisited

Q. In reference to the item that appeared in the July 1980 Hobby Scene column, a center tap may be successfully obtained by simulation (no "surgery" required). The procedure involves placing two resistive or reactive components in series across the winding in question. Examples are shown in the accompanying figure. The choice of component types and their values are subject to several variables. Unfortunately, there has been little written on this specific technique, but available material on voltage dividers and

dual-polarity power supplies can serve as a starting point in arriving at a suitable design for a specific application.

Resistors are probably the most frequently used components for this purpose and should be satisfactory in certain power and audio applications. Nonpolar, low series-inductance capacitors are acceptable for r-f work, as are high-Q inductors. If the impedances of the components employed are equal, the voltage drop across each of them will be half the total voltage induced across the secondary winding. This gives the simulated "center tap" the desired symmetry.
Some inefficiency is the price to be paid if this solution is adopted. Any cen-ter-tap current has to flow through (at
any one time) one of the two devices forming the voltage divider. Also, the divider itself places a load on the transformer. In some applications, these effects are acceptable, and the cost of a center-tapped transformer can be avoided if necessary. -Norman M. Monro, Gadsden, AL.
A. Thanks for your suggestions. (Thanks also to readers Harford R. Post, H. Johnson, Hal Knippenberg, and H.L. McFann for similar responses.) I was

aware of this technique, and have employed it in op-amp and r-f applications. I did not mention it because I inferred that the intended use was in a relatively high-current, bipolar or full-wave dc supply. In that case, the inefficiency introduced by the resistors would most probably be unacceptable. However, I stand by my original answer. I know of no way of adding a center tap to the secondary of an existing transformer without rewinding it. The technique described by Mr. Monro and the other readers who were kind enough to write in effectively center-taps a transformer or simulates the addition of a center tap-but it doesn't add a true center tap, and cannot be used in every application.
mizing stray capacitance, adding more peaking, and "running hot" by increasing the operating current of the video output stage.

Having already eliminated the sound trap and adjusted the brightness and contrast controls for best performance, you must look into the other approaches. Stray capacitances can be suppressed by keeping all video-signal-carrying leads as short as possible. If the video output stage is the bandwidth-limiting factor, it will sometimes respond to increasing its operating current. Before doing this, make sure that the stage can dissipate the additional amount of heat and handle the extra current, that the power supply can cope with the increased demand to be placed on it, and that the vid-eo-output stage is not already operating at its gain-bandwidth peak.

In all probability, adding additional peaking will make matters worse. As Don says . . . Generally, too little peaking will give you low-contrast dots, too much will give you sharp dots but will run dots together and shift the more continuous portions of the characters objectionably. Peaking is changed by increasing or decreasing the series inductor (peaking coil) from its design value. If the peaking is excessive, the circuit might "ring." This can cause smearing of the trailing edges of the dots.

## Solving the TV "Whistle" Problem

In our February, 1980 column, reader Jon Dattorro asked if we knew the possible source of a high-frequency "whistle" coming from his TV receiver. We replied that the noise was probably due to magnetostriction in the horizontal output transformer and that there was not a very satisfactory solution. However, we have received some letters since then with hints that may be helpful. Here are two suggestions.

Frank J. Burris, Fallbrook, CA, says that any open mounting supports on the flyback transformer should be examined, and frame holding screws tightly secured. A piece of electrical tape can be inserted between the doped lamination bundles (does not apply to molded pow-ered-iron cores) and the frame elements. Small wooden wedges (toothpicks?) can be slipped between the winding core and the laminations. This tightens up the laminations that usually produce the 15 kHz audio noise.

Jim Gieseke, Garden City Park, NY, suggests the use of liberal coats of "corona dope" to the laminations of the transformer to hold them together.

Thanks to those readers who took the time to send me their tips.

Have a problem or question in circuitry compo nents, parts availability, etc? Send it to the Hobby Scene Editor, popular electronics. One Park Ave. New York, N Y 10016. Though all letters can't be answered individually, those with wide interest will be published


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# Hip iํ <br> Experimenter's Corner 

By Forrest M. Mims

## More On Shift Registers

IN LAST month's column, we introduced the shift register as one of the most versatile of all digital-logic circuits. Shift-register operation and several shift-register applications were then covered. This month, we'll complete our discussion of shift registers by examining some specific chips and experimenting with some application circuits we think you'll enjoy.

Integrated Shift Registers. As we mentioned last month, you can custom-design a shift register by using any number of cascaded flip-flops. Fortunately, this procedure is rarely necessary. Semiconductor companies have designed and manufactured in integrated form most of the popular shift-register configurations.

Many different TTL and MOS shift registers are available, often for very reasonable prices. Even if you don't have immediate plans for incorporating one or more shift registers in a project of your own design, you'll be far better prepared to tackle a future project if you're aware of some of the chips that are now available.

| Type | TABLE I-TTL SHIFT REGISTERS |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Function | Bits | Freq. (MHz) | Shift <br> Right | Shift Left | Load | Hold |
| 7491 | SISO | 8 | 10 | Yes | No | No | No |
| 7494 | SISO | 4 | 10 | Yes | No | Yes | No |
| 7495 | PIPO | 4 | 36 | Yes | No | Yes | No |
| 7496 | PIPO | 5 | 10 | Yes | No | Yes | No |
| 74L99 | PIPO | 4 | 3 | Yes | No | Yes | No |
| 74164 | SIPO | 8 | 25 | Yes | No | No | No |
| 74165 | PISO | 8 | 25 | Yes | No | Yes | Yes |
| 74166 | PISO | 8 | 20 | Yes | No | Yes | Yes |
| 74178 | PIPO | 4 | 25 | Yes | No | Yes | Yes |
| 74179 | PIPO | 4 | 25 | Yes | No | Yes | Yes |
| 74194 | B/PIPO | 4 | 25 | Yes | Yes | Yes | Yes |
| 74195 | PiPO | 4 | 30 | Yes | No | Yes | No |
| 74198 | B/PPO | 8 | 25 | Yes | Yes | Yes | Yes |
| 74199 | PIPO | 8 | 25 | Yes | No | Yes | Yes |
| 74LS295 | PIPO | 4 | 25 | Yes | No | Yes | No |
| 74LS299 | B/PIPO | 8 | 35 | Yes | Yes | Yes | Yes |
| 74LS323 | B/PIPO | 8 | 35 | Yes | Yes | Yes | Yes |
| 74LS395 | PIPO | 4 | 25 | Yes | No | Yes | No |



Tables I and II list most of the TTL and CMOS shift registers available to experimenters. These tables, which were compiled from Motorola, National, RCA and Texas Instruments data books, list only some of the more important shift register parameters. Also, some specifications (such as maximum clock frequency) can vary somewhat within the same chip type if different manufacturers are involved. Therefore, you should check the specifications provided by the manufacturer of the chip you are thinking of using in a project for more specific information.

Here is a brief explanation of the table headings:
Function. The function of each shift register is identified by a four- or five-letter code. S means serial, P parallel, I in, O out, and B bidirectional. Therefore, a shift register listed as B/PIPO can provide bidirectional, parallel-in/parallel-out op eration. Incidentally, many of the shift registers listed in the tables will provide more than one operating function. Most PIPO registers, for instance, also provide SISO operation.
Bits. This parameter specifies the number of register elements in the listed device.
Frequency. The maximum shift (clock) frequency is given. Frequency specifications for CMOS shift registers vary with both the $V_{D D}$ supply voltage and the manufacturer. Consult the manufacturer's literature for the specific operating frequencies for a given chip.

Modes. Four operating modes are given. Those whose functions are obvious are shift left and shift right. Load is normally found on parallel-input shift registers. Depending upon the status of the load input, upon receipt of the next clock pulse, the register will either ignore or accept the data present at its input(s). Hold means that the clock input can be inhibited or disabled so that the shift register will store its contents like a memory register.
Several other operating modes not listed in the tables may also be available. Preset or clear enables all the register elements to be cleared to logic 0 . Recirculate causes the data at the output of a shift register to be cycled back to the input.

Exotic Shift Registers. Several kinds of elaborate integrated shift registers are available for such serial-memory applications as refreshing cathode-ray tube traces and storing characters to be printed by high-speed printers. A typical example is Synertek's SY 1404A 1024-bit MOS dynamic shift register. This family also includes a dual 512 -bit register (SY1403A) and quad 256-bit register (SY1402A). The maximum clock rate for these chips is a relatively slow 2.5 MHz , but by means of a multiplexing technique, data can be accepted at a $5-\mathrm{MHz}$ rate. Even more capability is provided by 2048-bit shift registers such as the SY2401 and SY2827.

Bubble memories and charge-coupled devices (CCDs) are among the most exotic shift registers available. Bubble-memory capacity can exceed a million bits per chip, making this exotic register a strong contender in the search for a solidstate replacement for the disk memory.

Application Circuits. Now let's try experimenting with some readily available shift registers to see how they work and what they can do. The circuits that follow use both CMOS and TTL shift registers. Once you see how easy it is to use
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these various chips, you'll want to consider experimenting with other shift registers as well.

Pseudorandom Sequencer. Figure 1 is the schematic of a pseudorandom generator whose operation is patterned after the S2688/MM5837 shift-register noise generator that was described in the March 1980 installment of this column. The circuit consists of two 8 -bit 4021 CMOS shift registers (IC2 and IC4) cascaded to form a single 16 -bit register.

The 4021 is a PISO register with the bonus feature that the outputs of the 6 th, 7 th and 8 th stages are available. This means it can be used as a 6 -, 7 - or 8 -bit shift register and makes possible the pseudorandom sequencer circuit. Such a circuit requires that the outputs of two stages in a shift register be coupled back to the input via an exclusive-OR gate.

By connecting the exclusive-OR gate's inputs to the final two outputs of the shift register, the sequencer will generate a pseudorandom sequence that recycles after 255 clock pulses. The bit pattern within a single 255 -bit cycle is essentially random, but the periodic recycling of the pattern compromises its randomness over the long term.
$\mathrm{V}_{\mathrm{PD}}$ or $\mathrm{V}_{\mathrm{SS}}$ to set up any desired sequence of logic 0's and 1's. Then toggle $S l$ from run to initiate and then back to run to load the parallel data inputs. Repeat this procedure as desired to create many different bit sequences.

Incidentally, if you intend to use this circuit for commercial purposes, you should first write the U.S. Patent and Trademark Office ( 2021 Jefferson Davis Highway, Arlington, VA 22202 ) and request a copy of U.S. Patent $4,191.175$. The fee for a single copy of a patent is 50 d . I've not yet seen this patent myself. However, after the March 1980 "Experimenter's Corner" appeared, William L. Nagle, president of Paratronic Systems, Inc. (Honeybrook, PA 91344) wrote to this magazine that, ". . . your readers should be cautioned that use of this for any other than private purposes would be an infringement of the patent our company holds for such devices and their applications." The complete letter was reproduced in the Letters column of the July 1980 issue.

I am interested in seeing this patent because its number indicates an issuance date late in 1979. Publications describing pseudorandom sequence generators date back to at least 1973 when Fairchild Semiconductor's The TTL Appli-


Fig. 1. Schematic diagram of a pseudo-random sequencer made from CMOS shift registers.

Two of the gates in $I C I$ form a simple clock for this circuit. This is a CMOS chip, so be sure to connect the unused inputs (pins $8,9,12$ and 13) to $\mathrm{V}_{D D}$ or $\mathrm{V}_{S S}$ to keep the chip from drawing excess current and overheating. You should also connect the unused inputs of IC3 (pins 5, 6, 8, 9, 12 and 13) to either $V_{D D}$ or $V_{S S}$.

This circuit has several useful applications. Connect its output to an audio amplifier through the low-pass filter composed of $R 2$ and $C 2$ to convert the pseudorandom bit pattern into audible noise. For best results, speed up the clock by reducing the value of $C l$ to $0.01 \mu \mathrm{~F}$ or so. At very fast clock speeds, the output will sound like a pure tone. At slower frequencies, the repetitious pattern present in the output signal can be heard.

An interesting LED flasher can be made by connecting the output of the circuit to the cathode of a LED whose anode is connected to $V_{D D}$ through a 1000 -ohm series resistor. Slow the clock rate to a few hertz by increasing the value of Cl to $10 \mu \mathrm{~F}$ or so. The LED will then flash on and off in an irregular, seemingly random pattern. This LED-flasher application illustrates how the circuit can be used to strobe (actuate) another circuit at a pseudorandom rate.

As you alter the clock rate or change the connections to the shift registers, the pseudorandom sequence generator might shut down. Make sure the clock is working by monitoring its output with an audio amplifier and loudspeaker (a tone should be heard) or with an oscilloscope (a square wave should be displayed). If the clock is running, try resetting each shift register by switching its pin 9 (the parallel/seRIAL CONTROL input) from $V_{S S}$ to $V_{D D}$ and then back to $V_{S S}$. The circuit should then resume normal operation.

You can control the pattern of bits moving through the shift registers. Connect the parallel data inputs of IC4 to
cations Handbook (a truly outstanding book) described two such sequencers on pages 8 through 21.

Incidentally, one of the Fairchild circuits employed one 4 -bit and seven 8 -bit shift registers to generate a truly long, nonrepetitive output bit sequence. According to the descriptive text, ". . . even at a frequency of 20 -million states per second the counter would not repeat until more than 18 centuries had elapsed." (!) For more information about pseudorandom sequencers, see Don Lancaster's indispensable CMOS Cookbook (Howard Sams \& Co., 1977). Don discusses the topic and presents two circuits on pages 318 through 323.

Pseudorandom Voltage Generator. Connect a suitable resistive ladder network to the outputs of a SIPO or PIPO shift register set up as a pseudorandom sequencer and you get a pseudorandom voltage generator. The resistor network serves as a digital-to-analog converter.

Figure 2 shows such a circuit designed around a 74164 or 74LSl64 SIPO 8-bit shift register. The four NAND gates (IC2A through IC2D) form an exclusive-OR gate. You can substitute one-fourth of a 7486 quad exclusive-OR gate in the circuit if you prefer.

Use a 555 timer IC connected as an astable oscillator like the one shown in Fig. 3 to provide clock pulses for the circuit. The amplitude of the pseudorandom, stepped output voltage can be changed by connecting pin 2 of $I C 2 A$ to any of the six other output pins of $I C I$.

An interesting application for this circuit is a pseudorandom tone sequencer that can be made by connecting its output to the control input of a voltage-controlled oscillator or volt-age-to-frequency converter. One suitable vco is the 4046, a chip that was highlighted in "Experimenter's Corner" for July and August 1980 . Suitable V/F converters include the


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9400 and LM331-see "Experimenter's Corner," for October 1979 for details.

8-Bit Serial-to-Parallel Data Converter. Here's a circuit that you can use to load serial data onto an 8 -bit data bus. Referring to Fig. 4, clock pulses are applied simultaneously to
experiment with the circuit as I did when working with the prototype, connect the cathode of a LED to each output pin of IC2. Connect the anodes of the LEDs to 1000 -ohm resistors which are in turn connected to +5 volts. You can use smaller resistance values (as small as 270 ohms) for the current-limiting resistors if you want the LEDs to glow brighter. I prefer


Fig. 2. Schematic of a pseudo-random voltage generator.


Fig. 3. A 555 clock pulse generator.

1000 ohms to keep current consumption down to 2 or 3 milliamperes per LED.
An additional LED and series resistor connected between pin 8 of ICl and ground will allow you to monitor the status of the shift register. You can then manually test the circuit by slowing down the clock to about one pulse per second and applying input data bits to pin 1 of IC3.

This circuit can be used as the receiving portion of a digital data-transmission system. A good design exercise would be to devise a suitable transmitter for converting 8 -bit bytes into a serial bit stream. Hint-use a PISO shift register and a divide-by- 8 counter.

Programmable Sequence Generator. Figure 5 schematically shows an 8 -bit programmable sequence generator made from two series-connected 74194 shift registers (ICI and IC2). In operation, any desired bit pattern is first selected by means of switches $S l$ through $S 8$. A normally closed pushbutton switch, $S 9$, is momentarily opened to load the selected bit pattern into the two shift registers. When clock signals are

counter $I C l$ and shift register $I C 3$. The data to be loaded on the bus should be applied at the clock rate to the serial input of the shift register.

The 7490 (IC1) is configured as a divide-by-eight counter. It generates a load pulse that causes IC2, a 74LS374 octal D flip-flop, to accept the data appearing at the parallel outputs of shift register IC3.

Summarizing, after 8 bits have been loaded into the shift register, a pulse from the counter causes the data to be transferred to the octal flip-flop. The flip-flop is updated with new data from the register after eight additional clock pulses.

You can use the 555 oscillator shown schematically in Fig. 3 as a clock circuit for the data converter. If you want to
applied simultaneously to pin 11 of both shift registers, the bit pattern will be shifted one position for each clock pulse. Because the serial output of the second shift register (pin 12 of $I C 2$ ) is connected to the serial input of the first shift register (pin 2 of $I C l$ ), so long as the clock pulses are received the bit pattern will recirculate through the registers. It will remain unchanged until it is modified by means of the dataselect and load switches.

Use the oscillator shown in Fig. 3 to generate clock pulses for this circuit. The output LEDs shown in Fig. 5 are optional, but including them allows the circuit to double as an atten-tion-getting programmable light flasher. The LEDs can be arranged in various patterns to enhance the effect.

A more practical application can be accomplished by connecting a resistor ladder network (see Fig. 2) to the outputs of the shift registers. The circuit then will function as a programmable waveform generator. Without the ladder network, the circuit can be used to strobe various circuits in any programmed sequence. More shift registers can be added for even longer sequences.

If you build this circuit, be sure to include the $0.1-\mu \mathrm{F}$ powersupply decoupling capacitors. Without these capacitors, the shift registers will be affected by power-supply transients that can arise during the switching sequence. A typical effect of such a transient is an unwanted change in the sequence.
stage goes low, the bit resulting from the gating of the first and last bits is a logic 0 .

Because this bit is fed back to the input of the first register (pin 2 of $I C 1$ ), it might at first glance appear that all of the register outputs would remain at logic 0 after the first cycle of clock pulses. Note, however, that the output of gate IC3C is connected to pin 10 of both registers. When the output of this gate is logic 0 , the shift registers ignore the data presented to their parallel inputs. When its output switches to logic 1, the shift registers load the data present at their inputs. This, of course, is what occurs when the ENABLE INPUT is brought to logic 0 to start the circuit.


Fig. 5. A shitt-register 8-bit programmable sequence generator.


Bargraph Generator. Figure 6 is the schematic diagram of an unusual bargraph generator made from a pair of 74194 shift registers. In operation, the circuit's ENABLE INPUT is. brought momentarily to logic 0 to start the circuit. This causes seven of the eight bit positions to be loaded with logic 1 's, because their inputs are left unconnected and therefore assume a high state. The output of the first stage of $I C l$ goes to logic 0 because its input (pin 3) is grounded.

The bits in the first and last stages, logic 0 and logic 1 , respectively, are combined in AND fashion by IC3A and IC3B. The result is presented to the serial input of the first shift register. On the first clock pulse, therefore, the outputs of the first two positions go to logic 0 while all of the other oútputs stay at logic 1 . This pattern continues as subsequent clock pulses are received until each of the shift-register outputs switches in turn from logic 1 to logic 0 . When the final

This circuit has such practical applications as strobing various external circuits in a sequential fashion. It also makes a very interesting visual display.

Going Further. Shift registers are ideally suited for experimentation. The bargraph generator shown in Fig. 6 is a good example of this. I began with the remnants of the circuit in Fig. 5 (the input switches were removed) and tinkered with the basic circuit by adding a single 7400 quad NAND gate IC. Within a few minutes, the bargraph effect was achieved. You can do the same kind of experimentation on your own by selecting different register outputs to be connected to NAND gate IC 3 A in Fig. 6. You can also try adding additional stages for more complex effects. The cost of integrated shift registers is very reasonable, and you'll learn a good deal about these very versatile logic circuits by experimenting with them.

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By Glenn Hauser

## Business Programs

DX LISTENERS can keep up with worldwide economic developments through financial, economic and business programs on certain stations. Here is a selected listing of such programs. All times and days are GMT

AFRTS-Washington conveniently groups six different U.S. network business reports into a 25 -minute block, Tuesday-Saturday at 0035, repeated at 0435 (times one hour earlier until Oct. 27). Among the frequencies are 21570 and 6030 kHz

Radio Australia has a "Business Report" Monday-Friday at 0820 on 15115 , 11740, 9570, repeated at 1220 on 9580 , and also every two hours through 2220 when $R$. Australia is more difficult to pick up here. There's also "Week in Business," Saturdays at 0212 on 21740 and 17795 and 0810 on the same frequencies as above.

BBC World Service excels in this programming area, as in so many others. "Stock Market Report" is Monday-Friday (except bank holidays, which also affect some of the programs below), at 1939-1943; try 15070 and 12095. "Financial News" is Monday-Friday 2230 2239 on 15260. 15070, and 9410, repeated at 0445 on 9510, 9410, and 6175.
$B B C$ also has two weekly business programs: "Financial Review," Sundays at 0445 to 0454 on 9510,9410 , and 6175 , repeated at 1709 on 17830 and 15260 . And the half hour show, 'Business Matters', Thursdays 1130 on $11775,11750,9740,9510$, and 6195; 2130 on 15420, 15260, and 15070; and Fridays 0330 on 9410,6175 , and 5975.

Canada's "As It Happens" newsmagazine once included a Tuesday segment called "Easy Money," and may still do so, sometime between 2330 and 0100 on 5960 (one hour earlier until Oct. 27)

South Africa: Radio $R S A$ has business programs at these approximate times: "Business Front," Mondays 1130 on 25790, 21535 , and $15220 ; 2135$ on 21535; Tuesdays 0235 on 11900 and 9585. "Economic News" is MondayFriday at 1245 on 25790,21535 , and 15220. "Economic Review," Saturdays 0410 on 11900; 9585, and 5980; 2110 on 21535 and Sundays 0210 on 11900 and 9585.

South Africa also has a Monday-Friday "Market and Meat Prices" on its
domestic services at 1607 , last reported on 4880, though not likely to be heard in North America. But keep it in mind if you're further east.

Austria. Thursday broadcasts of ORF's "Report from Austria" include a segment on the Austrian economy 0130 and 0330 on 9770 and 5945; 0430 on 12015 ; 1230 on 21655

All the programs mentioned so far are in English, but for business programs from two other countries we of necessity go to their native languages. France has a "Bourse" spot Monday-Friday 1158 1200 and 1705 on 21645,21595 and 17775 and at 1820 on 21620,21580 , 21515,17720 , as well as many other frequencies

West Germany's Deutsche Welle has a Monday-Friday stock market quotation report ("Börsenkurse") at about 2255 on 15410, 9735, repeated at 0255 on 9735.6145 and others

## Country by Country

Anguilla. This tiny Caribbean island may gain a shortwave voice if an American gospel broadcasting organization follows through on plans to establish "The Caribbean Beacon" here.

Austria. $O R F$ is working to upgrade its signal into North America by 1982 , with a new $250-300-\mathrm{kW}$ transmitter and two new antennas (a fixed but slewable curtain, and a "turntable" antenna)

Costa Rica. Radio Noticias del Continente survived a challenge about irregularities in its licensing. The government of Argentina and other Latin American military-led administrations have brought pressure against Costa Rica for allowing this private left-wing voice to be heard (almost 24 hours a day on 9615 ). One solution proposed was to let it continue broadcasting, but cut its power from 50 kW to one kW so it could no longer be heard well in Argentina.

Cuba. Though a country which is (barely) in the tropics, post-revolutionary Cuba has never used the tropical bands deliberately (though its prolific mediumwave harmonics fall in this area). One pre-revolutionary station, $C O C W$, had a frequency of 5045 kHz . More than twenty years later Radio Moscow World Service and The Voice of Cuba were heard testing on that frequency between 1000 and 1100 GMTseemingly with a transmitter much more


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Meanwhile, the growing Cuban population in Florida has led to an increase in clandestine broadcasting, headed by $L a$ Voz de la Junta Patriotica Cubana, heard most evenings on 7400 .
A Hialeah man accused of broadcasting illegally as "Comandante David" was busted by the FCC in July.

Ecuador $H C J B$ may be middle-of-the-road in its gospel outlook, but it's extremist in broadcast power. A nominally 100 -watt experiment on 26020 kHz may be dropped to only 10 watts; on the other hand, its homemade 500,000-watt transmitter, under construction for several years in Elkhart, Indiana, has been installed in Ecuador and should be testing on the air now. Meanwhile, a nother faith is on the air from Ecuador-Radio Baha'i, on 2340 kHz during the daytime. We'll have to wait for its projected morning and evening broadcasts on 60 meters in order to hear them.

France. Radio France Internationale continues to ignore its great potential audience in North America, as it announces plans to begin using new relay facilities in Gabon, French Guiana and Morocco to improve reception in Africa and Latin America.

Guatemala. The V. of Guatemala, 6180 kHz , has begun a brief English feature, "Guate-Scope," heard at varying times late at night.

Iran. Besides the Soviet-based National Voice of Iran, which has been on the air for many years from Baku, three other anti-Khomeini clandestine radio stations went on the air this year. A report in the Financial Times placed The Free Voice of Iran in Baghdad on behalf of Gen. Gholam Ali Oveissi; Radio Iran in Basra. Iraq on behalf of former prime minister Shapur Bakhtiar; and Radio Homeland in Cairo, connected with supporters of the late shah. The last station is also tied in with Radio of the Mujaheddin (Islamic guerrillas) of Afghanistan. Frequencies for these often change. Radio Homeland may still be on 15555 kHz at 1705 to 1800 .

Ireland. $R$. Condor is testing for North America during October at 0600 to 0800 on 6243 kHz ; in November, the time will change to 0700 to 0900 and 11463 kHz will be added.

Korea, North. Radio Pyongyang has a well-deserved reputation for far-out broadcasting, both in program content and frequency choice far outside the designated bands (such as 9977 kHz ). But now, a great many frequencies inside the SWBC bands have been registered by North Korea with the ITU Few of them so far have actually been heard, so this may just be for the sake of appearances.

Mexico. Super-power border station $X E R F, 1570 \mathrm{kHz}$, is now carrying "The Mexican Hour" in English, GMT Mondays, at 0400 . A new station, $X E V J$, in Linares, N. L. 5980 kHz , has been ad-

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vertising in a Monterrey newspaper.
Nicaragua. Clandestine radio played a major role in the victory of the Sandinistas. Last May, the tables were turned with the appearance on 6015 kHz of "FESGA"-the Voice of the Special Forces of Anti-Communist Guerrillas, possibly a mythical organization. Embarrassed Honduran authorities quickly located the station in San Pedro Sula and supposedly closed it down. Press reports said it was transmitted from Radio Swan, which in the past had operated as an anti-communist outlet.

Papua New Guinea. Night owls should check 9520 kHz after 0600 GMT for sporadic broadcasts from $N B C$, and on special occasions, 9575 kHz .

Portugal has an external service of its own, and serves as a relay site for several other countries and religious broadcasters. Now, the Lisbon Catholic station, Radio Renascença, is adding shortwave, perhaps by the end of the year, to reach expatriates.

Solomon Islands. Like Papua New Guinea, this country operates irregularly on the 31 -meter band after 0600 GMT. Try 9545 kHz , and if you don't hear them there, 5020

Surinam. Radio Apintie, a tiny commercial station, put a 50 -watt shortwave transmitter on 4795 kHz in order to reach remote regions of that country. Surprisingly enough, the station was heard elsewhere, including Holland, where many Surinamese live. Now $R a-$ dio Apintie plans to go about international broadcasting in a big way, with a ten-fold power increase to a hefty 500 watts. The frequency may have changed to 4950 .

Sweden. Radio Sweden acquired some 500 -kilowatt transmitters a few years ago, but was chagrined to learn that its antennas would handle no more than 350 kW . Six new curtain antennas are under construction at Hörby now, so the station should soon be able to put a stronger signal into North America.
U.S.A. With unrest in the Philippines, the Voice of America has been investigating other countries for shortwave relay stations. Among the possibilities are Darwin and North West Cape, Australia; and U.S. territory in Guam, Hawaii, and the Aleutian Islands.
U.S.S.R. Radio Moscow uses a great many sites throughout the country, but either through incompetence or for security reasons, the sites it acknowledges seldom correspond to sites determined by other evidence. In his USSR HighFrequency Broadcast Newsletter. Roger Legge reports on a study by Douglas Johnson and Olle Alm of "Operational Navigation Charts" issued by the U.S. National Ocean Survey, which show where broadcasting towers are located in the U.S.S.R. An 18 -transmitter complex, previously believed to be near Zhigulevsk, was determined to be near Kuybyshev instead. Ten transmitters at "Armavir" are actually near Krasnodar. Four transmitters designated as at "Or-
sha" are actually at Moghilev. Frunze, listed as a site, does not have any suitable towers. Five 500 -kW transmitters are at Nikolayev in the Ukraine, not at other sites in that S.S.R. However, sites in Alma Ata, Dushanbé and Tashkent were confirmed.

Vanuatu. The independence controversy here produced some interesting listening. Radio Vanuatu, on 7260 could be heard with English news at 0830 GMT, and live coverage of independence ceremonies at the end of July. The breakaway group on Espiritu Santo, led
by Jimmy Stevens, revived its broadcasting station, but on a new name and new frequency: Radio Vemarana, on 3522 and 3576 kHz , broadcasting in the 0730-1130 GMT period for several weeks before and after independence.

Information Sheet. We'll be glad to send you a listing of many other sources of information about shortwave and DX listening if you send a self-addressed stamped envelope to: Glenn Hauser, University Radio WUOT, Knoxville, TN 37916.


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## Low-Cost Crystal Oven

When a stable frequency source is needed, an oscillator designed around a quartz crystal is the logical choice. There are some applications, however, in which a standard quartz crystal oscillator is simply not stable enough. The principal problem in such a situationsay, the timebase of an accurate frequency counter-is that changes in ambient temperature cause small variations in the dimensions of the crystal and hence frequency drift.

One way to reduce thermally caused drift is to keep the temperature in the vicinity of the crystal as constant as possible. This can be done by means of a crystal oven, a small, temperature-controlled environment in which a piezoelectric crystal can be kept for maximum stability. In this article, we shall describe a simple crystal oven that can be built from a handful of low-cost components. It's very possible that you have all of the components in your junk box, which will reduce the effective cost of the oven to zero. Because the oven's circuit is simple and uses readily available parts, it provides an especially attractive way to enhance the stability of a planned or existing crystal oscillator.

A schematic diagram of the crystal oven circuit appears in the figure. The heart of the circuit is TDR1, a negativecoefficient thermistor (temperature-dependent resistor) chosen so that its resistance is approximately 1000 ohms at room temperature. This thermistor, together with resistor $R l$, form a voltage divider which supplies base drive to Q1.

When power is first applied to the circuit, the resistance of TDRI is approximately 1000 ohms. Transistor Q1 is approximately 1000 ohms. Transistor Q1 begins to conduct and sinks base current for Q2. As these transistors conduct, they dissipate heat which is coupled to the crystal and to $T D R 1$. This warms up the thermistor, causing its resistance to decrease. The decrease in thermistor resistance results in the application of less base current to $Q 1$ and hence to $Q 2$. With less collector current flowing through the transistors, less heat is generated, causing increases in its resistance and base current through $Q 1$.

The process continues in this manner, with $T D R 1$ acting as a thermostat and the transistors as heating elements. Once equilibrium is espablished, temperatures of the crystal and the thermistor remain at fairly constant values. Heat is generated by the transistors and coupled to the crystal and thermistor as needed to make up for radiated energy.

The circuit of the Low-Cost Crystal Oven is very simple, so it can be easily assembled using point-to-oint wiring techniques. The author's prototype was assembled by cutting a small strip of copper flashing so that its short dimension equalled the height of the crystal case. The transistors have TO-92 plastic packages with one flat side. The author affixed the flat side of each transistor case to the copper strip using epoxy cement. He also epoxied TDRI to the strip.

Next, silicone thermal compound was applied to the crystal case and tightly wrapped the copper strip around it. He temporarily held the strip in place with a large alligator clip and soldered the point where one end of the strip overlapped the other using a small soldering pencil, he made the necessary connections between the transistors, resistors, thermistor and power supply. Finally, he took a small block of styrofoam and cut out a slot in it so that the crystal/ovencircuit assembly could be slid into the slot. The styrofoam is a good insulator and helps stabilize the crystal temperature once power has been applied to the oven circuit.


Any one of several thermistors can be used for $T D R 1$, as the choice of a specific component is not critical. In general, the device should be a small glass-bead or glass-probe type with a negative temperature coefficient and a resistance at room temperature approximately equal to 1000 ohms. The value of the other member of the voltage divider, $R 1$, is not shown in the schematic. It is intentionally omitted because the exact value depends on both the thermistor employed and the temperature at which the crystal is to be kept. A good ballpark figure is 10,000 ohms. Substituting the series combination of a $1000-\mathrm{ohm}$ fixed resistor and a 10,000 -ohm or 25,000 -ohm potentiometer for Rl allows adjustment of the oven circuit for best performance.

An AT-cut crystal has, in its normal range of ambient temperature, a nega-
tive temperature coefficient. That is, as temperature increases, the frequency of oscillation decreases. At higher ambient temperatures (usually between 70 and $75^{\circ} \mathrm{C}$ ), the oscillating frequency stops decreasing and is about to start increasing. This is the turning point of the crystal. Commercial ovens are often adjusted to keep the crystal at its turning point. This is done by allowng the oscillator to run for a while and then sampling its output in such a way as to disturb the operation of the circuit as little as possible. The sample is then measured using a very accurate frequency counter and the oven's temperature control adjusted so that the oscillator is at its lowest operating frequency

You can follow this approach to the adjustment of the potentiometer if that component is included in the oven circuit. However, if you don't want to bring the crystal up to its turning point, you'll find that the oven still enhances the stability of the oscillator over what it would be if the ambient temperature of the crystal was not controlled.

The most common application for the crystal oven is the stabilization of an oscillator in an existing piece of equipment. Thanks to its compact size, the finished oven will fit easily inside just about any existing equipment enclosure, such as one housing a frequency coun-
ter, receiver or transceiver. If the equipment employs more than one crystal whose oscillating frequency is critical, several ovens can be built. Each one could be fitted over a given crystal and then the crystal returned to its socket.

The operating voltage for the oven circuit should be regulated and can usually be furnished by the host components power supply. An operating voltage other than the 12 volts specified can be used, but some changes in component values might be necessary.

The low cost, simplicity, and compactness of this crystal oven make it ideal for installation in existing or planned equipment employing a quartz crystal oscillator, or for experimental work with very stable oscillators. If you intend to experiment, keep the following points in mind. All quartz crystals suffer to some extent from aging. In general, highquality crystals are more expensive but are made from select material and are cut in such a way as to minimize aging. Finally, keep in mind that power input to the crystal should be kept as low as possible if a low aging rate is to be maintained. This means that oscillators teaming up quartz crystals to TTL gates are to be avoided in preference to those employing CMOS gates or very-lowpower discrete devices.-Jack Rutherford, Burlington, N.C.

## Drill Speed Control

If you don't have a variable-speed electric drill, you probably wish you did. There are a number of situations in which low-speed drilling yields the best results-drilling such brittle materials as Bakelite and Plexiglass, drilling in tight spaces, and where a slip of the drill
network that delivers gate drive to the SCR. The drill is plugged into socket SOl and its speed controlled by the setting of the potentiometer. Diode DI protects the circuit from reverse voltage spikes.

The SCR and the modular bridge rec-

would be disasterous, such as drilling holes in a finished front panel or project enclosure. Medium-speed drilling is often employed when working on nonferrous metals such as aluminum and brass. The circuit presented in the figure will allow you to vary the speed of a standard fixed-speed drill without performing any modifications to the tool.

Modular bridge rectifier RECTI converts the ac power waveform into fullwave rectified pulsating dc. A portion of the rectified current passes through the 10,000 -ohm potentiometer to the $R C$
tifier should have current ratings of 25 amperes and PIV ratings of 600 volts. Diode $D I$ should have a current rating of 2 amperes or more and a PIV rating of 600 volts. A small aluminum utility box can function as the circuit's enclosure. A power tool or other appliance (soldering iron, lamp, etc.) drawing up to 10 am peres or so can be controlled by the circuit. If the tool or appliance plugged into $S 01$ draws four to eight amperes on a continuous basis for more than 10 minutes, the SCR must be heat sinked. Harry J. Miller, Sarasota, FL.

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## Electronics Library

## Archer Semiconductor Replacement Guide (1980 Edition)

This is a useful publication for those who need to cross-reference an OEM semiconductor type number to a component that is available at the retail level. According to the publisher, the book contains cross-reference/substitution listings for more than 100,000 semiconductor devices. These listings are said to have been generated by computer and to be the result of an analysis of the key parameters of each device. In addition, information is included on the care and handling of semiconductors, on case styles and dimensions, and on testing transistors and diodes. There is also a glossary of terms, symbols and abbreviations. Pertinent electronic and physical characteristics of each member of Radio Shack's line of semiconductors are presented in data-sheet form.
Published by the Radio Shack Div. of Tandy Corp., 1300 One Tandy Center, Fort Worth, TX 76102. 224 pages. $\$ 1.99$ soft cover

## Programming the $\mathbf{Z 8 0}$

by Rodnay Zaks Contained in this book is a wealth of information that can help you get started programming a Z80 computer at the machine/assembly-language level. Each chapter proceeds from the simple to the complex, and overall treatment covers elementary through intermediate programming techniques to get you started programming on your own. Flow charting and other data presentation methods and Z80 hardware organization are introduced in the early chapters. Then almost $50 \%$ of the book is devoted to fully detailed description of the Z80's powerful instruction set. The final chapters cover addressing techniques, I/O techniques, I/O devices, application examples, data structures, and program development
Published by Sybex, Inc., 2020 Milvia St., Berkeley, CA 94704. Soft cover. 624 pages. \$14.95.

## Radio Enters the Home

by Harry F. Bart This book was originally published in 1922 by the Sales Department of the Radio Corporation of America. It was intended, "For those who desire to be entertained with concerts, lectures, dance music-as well as for the radio amateur," and cost 35 cents. The book will be of interest to old-time radio buffs as well as those who would like to learn about the early days of wireless commu-
nications. Included are descriptions of radio phenomena, receivers (crystal sets, primitive vacuum-tube designs), early vacuum-tube transmitters, and a catalog of components and assembled units. Radio amateurs could consult the book for schematics, a summary of Radio Laws and Regulations of the U.S., National Electrical Code Radio Rules, a Morse code table, vacuum-tube precautions, a glossary of technical terms used in radio, and various useful data. The book is profusely illustrated with photographs and line drawings of broadcasting studios, receivers being used in the home, and commercial radio products.
Reprinted and published by the Vestal Press, Box 97. Vestal, NY 13850. Soft cover, 128 pages. \$12.50. Postage and handling charge $\$ 0.50$ for U.S. orders.

## The Radio Amateur's License Manual

edited by Wyland Dale Clift, WA3NLO This latest edition of The Radio Amateur's License Manual (77th Edition) is a radical departure from those of the past 50 years. Gone are the question-and-answer format and coverage of theory for the Novice Class license exam Theory for the Technician, General, Advanced and Extra Class license exams is now presented in easy-to-read essay form. There is a short chapter dealing with the Novice Class license and exam, but a note directs readers interested in becoming Novices to employ the ARRL's code-and-theory package for the Novice test entitled Tune In The World With Ham Radio. There are separate chapters for amateur licensing procedures, the station license, Technician/General Class theory, Advanced Class theory, Extra Class theory, International Regulations, and where and how to take an amateur examination. At the end of each theory section are sample questions, answers to which are presented in the back of the book. The text in the theory sections is illustrated with many schematics and line drawings. Published by the American Radio Relay League, 225 Main Street, Newington, CT 06111. Soft cover, 168 pages. $\$ 4$ in the U.S.

## How to Make Printed Circuit Boards

by Joel Goldberg

## How to Build Electronic Projects

## by Douglas R. Malcolm, Jr

 These two books make an excellent training course in project building for newcomers to the electronics hobby. Printed Circuit Boards details every aspect of single- and double-sided pcboard fabrication, from initial design through final assembly. Photos and drawings help describe how to make pc boards from scratch using direct, photographic, and silk-screen techniques. Electronic Projects details with numerous photos and drawings procedures for converting schematic diagrams and components into finished projects. Leading off with component descriptions, it proceeds to soldering techniques andproject layout and circuit board construction and closes with a selection of circuits to build, using the information contained in previous chapters.
Published by Gregg/McGraw-Hill, 1221 Ave. of Americas, New York, NY 10020. PC Boards: Soft cover. 128 pages. $\$ 6.50$. Projects: Soft cover. 144 pages. $\$ 7.95$.

## Antenna Data Reference Manual

by Joseph Carr, K4IPV Contained in this book is design information for those who want to construct antennas for amateur, shortwave listening, CB and FM broadcast applications. Each chapter deals with a different type of antenna (Horizontal Dipoles, Inverted Vees, Verticals, Quads, Yagis, etc.) and has computer program printouts giving the physical dimensions for a sequence of frequencies within each amateur and broadcast band. Also provided are line drawings showing the physical configurations of the antennas described, summaries of their electrical and radiating characteristics, and suggestions for their physical construction. Other topics discussed are transmission lines, tuning procedures, impedancematching systems, and antenna measurements and instrumentation.
Published by Tab Books, Blue Ridge Summit, PA 17214. 266 pages. $\$ 12.95$ hard cover, $\$ 7.95$ soft cover.

## Introduction to T-BUG

by Don \& Kurt Inman Written to acquaint the reader with the use of Radio Shack's T-BUG Monitoring and Debugging Aid, this book describes in detail the machine-language operations of the TRS-80 microcomputer. (It is not intended to serve as a complete machine-language textbook.) Each T-BUG command is thoroughly explained and examples are given to demonstrate how each can be used. Every step is discussed for each of the many sample programs contained in the book, and each program is accompanied by a sketch of the video display.
Published by Dilithium Press, 30 NW 23 Pl., Portland, OR 97210. Soft cover. 120 pages. $\$ 6.95$.

## DC Power Supplies

by Robert J. Traister Detailed information on dc power-supply theory and applications is contained in this book. Where it departs from most similar books is in its stress on experimentation with existing circuitry so the reader can develop new designs for particular needs. Divided into two parts, the first deals with theory, covering powersupply circuitry, components and ratings, dynamic and electronic regulation, protective circuits, voltage multiplication, metering, and safety circuits. Part II focuses on actual circuits: ac and dc supplies and low-, medium-, and highvoltage circuits.
Published by Reston Publishing Co., Inc., Reston, VA 22090. Hard cover. 234 pages. $\$ 16.95$

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Hy-Gain Amateur Equipment
Hy-Gain's new 24 -page catalog (No. AM 2504) covers 100 base and mobile antennas, towers, rotators, microphones, boom mic headsets and accessories for the amateur radio operator. Detailed specifications, including SWR curves on all base antennas, are given. Address: Kit Kitterer, Hy-Gain.. Div, of Telex Comm., Inc., 8601 NE Highway Six, Lincoln, NB 68505

## Updated Buzzword Guide

The 1980 edition of "Sherry's Guide to Data Communication Buzzwords" contains 24 pages of 192 definitions of words commonly used in communications and data processing. Address: Corporate Relations Dept., Racal-Milgo, Inc., 8600 N.W. 41 st St., Miami, FL 33166.

## VIZ Instrument Catalog

Over seventy VIZ electronic instruments are illustrated and described in its new 44-page catalog for 1980. Features including ranges and limits, power required, weight, etc. are given on DMMs, VOMs, power supplies, isolation transformers, signal generators, and frequency counters. Address: VIZ Mfg. Co., 335 E. Price St., Philadelphia, PA 19144.

## Radio Frequency Directory

The Federal Frequency Directory features over 100,000 discrete listings of frequencies, agencies, and locations of U.S. Government radio communications installations in the $2 \cdot 420-\mathrm{MHz}$ spectrum as released under the Freedom of Information Act. Entries include Justice, Treasury, NASA, FCC, FAA, Interior, Army, Navy, Air Force, and

Coast Guard listings arranged in order of frequency, agency, and geographical location. Available for $\$ 14.95$ postpaid from Grove Enterprises, Box 156B, Brasstown, NC 28902 and qualified dealers.

## TRS-80 Educator's Sourcebook

A 27-page booklet entitled "TRS-80 Microcomputer Sourcebook for Educators" has been issued by Radio Shack as a guide to the use of microcomputers as both a medium and an object of instruction in the classroom and as a tool for the school administrator. It provides guidelines for selecting a system based on potential applications, costs, service, reliability and courseware. Address: Radio Shack, Dept. NR-17, 1300 One Tandy Center, Fort Worth, TX 76102.

## Instrument Rental Catalog

The 1980 Electronic Instrument Rental Catalog from Continental Resources, Inc. describes over 1.000 pieces of test equipment for monthly rental. Specifications and rental rates are given for such items as oscilloscopes, recorders, logic analyzers, microprocessor test systems, power meters. function generators, frequency synthesizers, and computer peripherals. Address: Continental Resources, Inc., 175 Middlesex Turnpike, Bedford, MA 01730.

## Control Devices Catalog

"Switches, Pilot Lights and Control Devices" lists, in 174 pages, the stock of manufacturers such as Sigma, Potter \& Brumfield, and Struthers Dunn available from the distributor, Relay Specialties, Inc. Included in Catalog C-109 are sockets, photoelectric controls, terminal strips and blocks, circuit breakers, counters, buzzers, sensor, solenoids, etc. Address: Relay Specialties, Inc., 13-00 Plaza Rd., Fair Lawn, NJ 07410.

## RCA Microprocessors

A 60-page Product Guide (MPG-180C) from RCA describes all the elements needed to build a microprocessor system. Included are ICs, support systems, and accessories in the 1800 COSMAC family. It covers the CDP1802 microprocessor with data on a variety of RAMs, ROMs, and I/O devices, with characteristics and functional and terminal diagrams. A cross-reference gives RCA types for other manufacturers' equivalents. Address: RCA Solid State Div., Box 3200, Somerville, NJ 08876.

## Catalog of R-F Diodes and Transistors

Uprated and extended ranges are characteristics of the $125 \mathrm{r}-\mathrm{f}$ devices in Ferranti's new 24 -page catalog. Details on function, application, and essential parameters are given on variable capacitance tuner diodes, hyperabrupt variable capacitance tuner diodes, Schottky barrier diodes, and r-f transistors. Address: Ferranti Electric, Inc., Semiconductor Products, 87 Modular Ave., Commack, NY 11725.

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# PROJECT OFTHE MONTH 

BY FORREST M. MIMS

THIS month's project is a programmable digital timing circuit that has countless applications around the shop and home. You can learn how the circuit works by referring to the schematic diagram in Fig. 1. Astable multivibrator $I C I$ provides clock pulses for IC3, a 74192/74LS192 decimal counter. The clock frequency can be altered by changing the value of either $R I$ or $C 1$.

The circuit can be programmed to generate any one of ten timing intervals. Any desired starting point from 0000 (0) to 1001 (9) is selected by means of switches $S 1$ through $S 4$. When pushbutton $S 5$ is closed momentarily, the BCD number present at the parallel data inputs of the coun-
counter's input. The clock pulses cannot reach the counter when the BORRow output is at logic 1.
As IC3 begins its downward count, the current BCD number present at its outputs is decoded by IC4 and is displayed on a common-a node, sevensegment LED readout. Once the count has reached 0000 , the BORROW output goes to logic 0 upon receipt of the next clock pulse. Clock pulses are thereupon blocked and the counter outputs idle at 0000 . The counting cycle can be initiated again by momentarily depressing $S 5$. Of course, a new starting point can first be loaded into the counter by means of switches SI through $S 5$.

The circuit shown in Fig. 1 includes


Fig. 1. Schematic diagram for a programmable countdown timer.
ter is loaded into it, and the chip's BORROW output (pin 13) goes to logic level 1.

Both the BORROW output of the counter and the output of the clock are combined in AND fashion by NAND gates $I C 2 A$ and $I C 2 B$. The resulting logic signal is applied to the count-down input of the 74192. When the BORROW output is at logic 1 , clock pulses pass through the gates to the
two optional indicator LEDs. The diode designated LED2 flashes each time a clock pulse is applied to the input of the counter. The other diode, $L E D 1$, is dark during the counting sequence, but begins to glow one clock pulse after the count has reached 0000. You can omit these LEDs if you choose, or you can connect other circuits in their place which will then be actuated by the counter.

## Programmable Countdown Timer

## Model-Rocket Countdown Laun-

 cher. Having long been a modelrocket enthusiast, my favorite application for countdown timers is in automatic rocket-launching systems. Such systems are ideal for rocketeers who like to photograph their birds lifting off their launch pads. An audible beeper circuit triggered by the clock pulses applied to the counter input would be a helpful addition. It would allow the photographer to keep track of the countdown sequence while he aims his camera at the rocket as it sits on its launcherFigure 2 schematically shows how to add a rocket-motor ignition circuit to the basic countdown timer. A relay is used as the power-switching compo-
quickly deactivate the launch sequence should an unforeseen problem occur. You can use the ignition circuit's 6 -volt battery to power the count-down circuit as well, if you so desire. This will simplify the overall launch system by eliminating the need for two separate power supplies. To do this, connect the +5 -volt bus of Fig. 1 to the cathode of a 1 N 4001 diode Connect the a node of the diode to the positive terminal of the battery, and the ground bus of Fig. 1 to the negative battery terminal, using a suitable length of lamp cord.

Other Applications. Of course, lauching model rockets is only one application for this versatile circuit


Fig. 2. How to add a relay to the timer for igniting model rockets.
nent because the resistance of its closed contacts is much less than the "on" resistance of an SCR or other solid-state switching device. This ensures that the highest possible current will flow through the nichrome-wire rocket-motor igniter. The diode connected across the relay coil absorbs the high-voltage spikes which can be generated by the collapsing magnetic field when the relay coil is deprived of current.

If you use this circuit to launch model rockets, be sure to follow all of the standard safety precautions. Avoid the temptation to place the ig-nition-control system adjacent to the model-rocket launcher. Instead, connect the countdown circuit to the launcher with a $20^{\prime}$ length of lamp cord. Be sure to include a disarming switch so that you can safely and

Many others are possible. For example, you can alter the timer's clock circuit to produce pulses at the rate of say, 5 Hz . It can then be used as a darkroom timer. Slowing down the clock rate to one pulse per minute makes it possible to time a boiled egg, a phone call, or any other event lasting 10 minutes or less.

You can even extend the timing capacity of this circuit by cascading one or more additional counting stages. And you can reverse its operating mode by applying the gated clock pulse to the count-up input (pin 5) and connecting pin 2 of IC2A to the CARRY output of the counter (pin 12) instead of to pin 13, the BORROW output. Because the programmable countdown timer circuit is reasonably foolproof, it's an excellent first project for the novice experimenter


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## Personal Electronics News

YOU CAN RENT A ROBOT by the hour from Jerry Rebman Electronics if you live in the Houston, TX area. If you don't live in the area and can't afford to buy one of Jerry's radiocontrolled robots (average price is $\$ 3000$ ), you can buy Jerry's book for $\$ 50$ and get construction details for building your own robot. The book gives full details for fabricating "Garcan," a radio-controlled robot that can be built into a steel garbage can. For more information, write: Jerry Rebman Electronics, 3530 Timmons Lane \# 155, Houston, TX 77027.

VCR OR VIDEODISC? According to a recent survey conducted by Venture Development Corp., more than two-thirds of consumers queried prefer one or the other system. Consumers who don't own video-playback equipment prefer VCRs to videodiscs by more than three to one, while about one-fifth of potential buyers want both systems. Another $23 \%$ express no preference but are certain that they would purchase only one of the two. The study also reveals that VCR owners have a greater interest in having both systems (almost 29\%) than people who don't own a VCR. On the other hand, three out of five VCR owners express no interest in videodiscs.

A "TALKING" AUTOMOTIVE DIAGNOSTICS WARNING SYSTEM, employing speech synthesis technology, has been demonstrated in prototype form by National Semiconductor Corp. It takes inputs from a microcontroller and gives audible warning messages, such as "low fuel," "low brake fluid," etc. The system employs National's speech processor and memory device containing the words and phrases to be spoken. An actual recorded voice is A/D converted and its code is compressed by a factor of 100 and stored in a ROM for individual word or phrase playback through the speech processor chip. Speech quality is claimed to be comparable to that of a high-fidelity magnetic tape recording and is related to the number of bits of memory used to store the speech.


#### Abstract

DISK INSURANCE is now being offered by Micro Lab, Highland Park, $I L$, to microcomputer users who buy its Data Factory product lines. The insurance-policy package is sold with two locked versions of the master diskette. Should a master become damaged or blown during the policy period, the policy holder can simply return the inoperative disk for immediate free replacement. In addition, if an update in the program occurs, policy owners will be notified and older versions will be revised free of charge. Cost of the insurance policy (not disks) is $\$ 17.50$ per year.


PAY-BY-PHONE IS A REALITY with Chase Manhattan Bank's "Bank-by-Phone" computer and a "Touch-Tone" telephone with * and \# keys. Checking-account customers access the computer with a keyed-in-personal account number and security code. Then, using the phone"s "dial" buttons, the computer is instructed to pay the specific merchants you have in mind and how much. You can even check your account balance, cancel a call in midstream, and send an SOS to get help. No Touch-Tone phone? . . simply call, toll-free, and a Chase representative will relay bill-payment information to the computer.

UNCHECKED COMPUTER CRIMES may mean the loss of a billion dollars annually if The Federal Computer Systems Protection Act (S-240) is not passed says Philip R. Manuel, who drafted the legislation and is an investigative consultant in the area of white-collar crime. Computer crimes are now covered by "existing, but inadequate, laws dealing with crimes ranging from mail and wire fraud to obscene phone calls," observes Manuel. The new bill was drafted to put a clearer definition on computer crime and provide protection for computer systems of all types. It would also protect from fraud sophisticated electronic fund transfer systems. The bill was first introduced by Sen. Abraham Ribicoff (D-Conn.) in 1977, and was reintroduced in January 1979. It has prompted several states to pass similar legislation, but it is feared that the bill will die at the Federal level unless acted upon before Congress adjourns this Fall.

NATIONWIDE ELECTRONIC SERVICE FRANCHISING is the goal of a new company called
"Tronics 2000" in Bloomington, IN. Sensing that the independent serviceman will be faced with increasing complexity and diversity in the future, the company intends to sell franchises to provide technical assistance through training courses, updated business techniques, national and local advertising, and volume buying. The headquarters will contain graphic arts and training departments, and a supervisor for every five franchises.

# For less than $\$ 25$, hire a full time Night Sentry to protect your home. 

## Night Sentry puts electronic timed lighting control where it belongs: on the wall...

Most home security experts agree that one of the most effective deterrents to intruders and burglars is lighting that creates the "lived-in" look while you're away. Now the new Dynascan Night Sentry enables you to easily achieve that lived-in look. Using a micro-computer that does the work of more than 10,000 transistors, it provides automatic control of indoor or outdoor light fixtures, including porch, post, kitchen, bathroom, and bedroom lights.
The amazing micro-computer's "memory" permits automatic self-programming (just use the Night Sentry as a conventional light-switch—after 24 hours, your pattern of light use will be repeated daily thereafter.) You also can program it manually in a matter of seconds! Up to 48 ON-OFF settings per 24-hour period are possible. Installs in minutes with only a screwdriver.

Comparison Chart

| FEATURE | $\begin{aligned} & \text { NIGET } \\ & \text { SENTRY } \end{aligned}$ | OTHER TIMERS |
| :---: | :---: | :---: |
| 1 Automatic programming | YES | NO |
| 2. Manual programmung | YES | YES |
| 3. Built-1n microprocessor/memory | YES | NO |
| 4 Sohd-state reliabulity | YES | NO |
| 5 Silent operation (no motor) | YES | NO |
| 6. 48 "ON-OFF" selections | YES | SOME <br> MODELS |
| 7. Variable "ON-OFF" times | YES | SOME MODELS |
| 8 Easy pushbuton overnde | YES | NO |
| 9. Easy 2-wire installation | YES | NO |
| $\begin{aligned} & 10 \text { Fits any single or } \\ & \text { multuple switch box } \end{aligned}$ | YES | NO |
| 11. Usable with most stan dard or decorator switch plates | YES | NO |
| 12. Attractive "low-profile decorator styling | YES | NO |
| 13 "Soff-start" triples bulb life | YES | NO |
| $\begin{aligned} & \text { 14 Avalable in 3-way } \\ & \text { model } \end{aligned}$ | YES | NO |
| 15. Available in table model with dimmer | YES | NO |

Check these "never-before-available features: No clock motor or gears to become noisy; easy override of program to use as conventional ON-OFF switch (no fumbling behind or under furniture to 10 cate an override butron); built-in variability of ON-OFF times to confuse intruders or burglars "casing" your home; usable in single or gang switch boxes; uses existing standard or decorator switch plates; available in 3 -way version. Also available: table top model for controling lamps plugged into wall outlets.
Visit your local hardware, home center, or department store and see the new Night Sentry Suggested retail price under \$25.

## NIGHT SENTRY

 Timed Light ControlFrom the people who brought you COERA Radios and Cordiess Telephones

Write for cclor brochure.
DYNASCAN
CORPORATION
6460 W. Cortland St.. Chicago, IL 60635

# Introducing a totally new concept in stereophones. 

The new Koss HV/X high velocity stereophone represents a remarkable breakthrough in hear-thru stereophone design and performance. For the first time, Koss engineers have been able to create a lightweight, hear-thru stereophone that com bines the transparency of high velocity phones with the superior bass performance of closed-type phones. The result is a breathtaking musical experience.

## C(ONT()URED VARIABLE-DENSITY EARCUSHIONS

While most lightweight, hear-thru stereophones have earcushions that fit against the ear, the new Koss HV/X features a unique, contoured, vari-able-density cushion that fits around the ear. Not only does this unique earcushion design create a far more comfortable stereophone but it has also allowed Koss engi neers to create a dramatically better element

design as well.
These new variabledensity earcushions are made up of a very porous

for Koss engineers to design a lightweight element that reproduces a Sound of Koss you have to hear to believe. Incredibly, even though the overall weight of the element was reduced, Koss engineers were able to develop a magnet with enough magnetic density to drive an extra large diaphragm. With a response range of 15 to $35,000 \mathrm{~Hz}$, the new Koss HV/X will drive you into ecstacy and our competitors nuts.

## HEARING IS BELIEVING.

Slip into the new Koss HV/X or HV/XLC with volume/balance controls at your audio dealer soon. You'll like the best of both worlds: the open, airy, upfront sound of hear-thru stereophonesand the deep, rich bass performance of closed-type stereophones.

And while you're with your audio dealer, listen to our full line of Koss stereophones and CM loudspeakers. There's no sound quite like the Sound of Koss.

For more information on the $H V / X$, our full line of stereophones and loudspeakers or our new Koss K/4DS digital delay system, write c/o Virginia Lamm.

# (1) hearing is believing " 


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