

# Popular Electronics®

WORLD'S LARGEST SELLING ELECTRONICS MAGAZINE DECEMBER 1981/\$1

THE ELECTRONIC WORLD

## A User's Guide to Computer Languages

## Detect Car-Battery Drain Before It's Too Late

## Comparing New High-Tech Audio Cassettes



MINNEAPOLIS  
1088 22ND AV SE  
KENNETH J LYNES  
490684 LNE 10882099 04

### ests IBM's y Personal Computer

## Baked Apple.

Last Thanksgiving, a designer from Lynn/Ohio Corporation took one of the company's Apple Personal Computers home for the holidays.

While he was out eating turkey, it got baked.

His cat, perhaps miffed at being left alone, knocked over a lamp which started



a fire which, among other unpleasantries, melted his TV set all over his computer. He thought his goose was cooked.



But when he took the Apple to Cincinnati Computer Store, *mirabile dictu*, it still worked.

A new case and keyboard made it as good as new.

Nearly 1,000 Apple dealers have complete service centers that can quickly fix just about anything that might go wrong, no matter how bizarre.

So if you're looking for a personal computer that solves problems instead of creating them, look to your authorized Apple dealer.

You'll find everything well-done.

The personal computer.

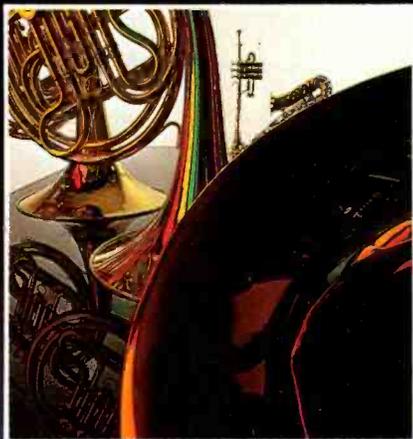


# In a world where sound reaches new levels every day, ADC delivers the ultimate high.

The ultimate high is total control. And an ADC Sound Shaper® Frequency Equalizer lets you control your sound and custom-tailor your music with the mastery of a pro.

And no better way demonstrates the benefits of an ADC Sound Shaper than taping. Even without a studio environment, you can recreate your personal recordings by changing the frequency response curve of the source material — making the sound more like the original and more agreeable to your ears.

Our complete ADC Sound Shaper IC line\* has an equalizer that is right for you and your system. The SS-110 ten-band full octave equalizer, a step up from our SS-1, features LED-lit slide controls and one-way tape dubbing. If you desire even more control, our twelve-band SS-II and top-of-the-line SS-III include two-way tape dubbing and sub-sonic filters. Our SS-III Paragraphic™ with 24 ancillary switches that enable you to control 36 bands per channel combines



the ease and control of a graphic equalizer with the precision and versatility of a parametric. All at a price you can afford.

All of our equalizers feature LED-lit slide controls allowing for visual

plotting of the equalization curve. And all ADC Sound Shapers embody the outstanding ADC technology that has made us the leaders in the industry.

To really complete your custom-tailored control-ability, our ADC Real Time Spectrum Analyzer is a must. Equipped with its own pink noise generator and calibrated microphone, the SA-1 provides a visual presentation of the changing spectrum through 132 LED displays. So you can actually see proof of the equalized sound you've achieved.

With an ADC Sound Shaper and an ADC Real Time Spectrum Analyzer, you can attain a new level of control. And ultimately, isn't that the musical high you've always wanted?

*Sound Shaper®*  
Frequency Equalizers  
and Spectrum Analyzer



**ADC**  
A BSR COMPANY

Sound thinking has moved us even further ahead.

Write for a free 24-page booklet, "Shaping Sound at Home: A Guide to Equalization" (A \$2.50 value.)

BSR (USA) Ltd., Blauvelt, N. Y. 10913, BSR (Canada) Ltd., Rexdale Ontario

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CIRCLE NO. 64 ON FREE INFORMATION CARD

# Popular Electronics®

WORLD'S LARGEST-SELLING ELECTRONICS MAGAZINE

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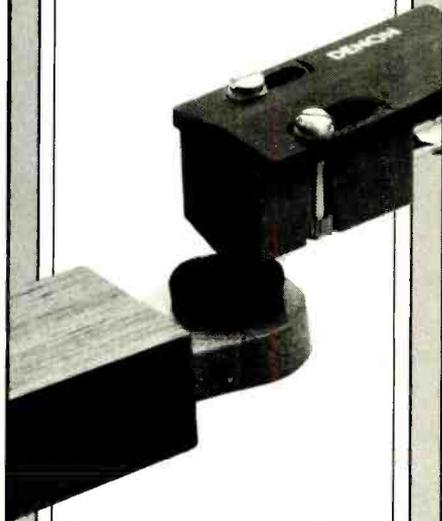
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Brush away stylus contamination with the SC-2™ Stylus Care System. Two drops of SC-2 fluid on the special nylon fiber brush effectively loosens and wipes away harmful coatings.

Protect your stereo system and maintain its sound with the SC-2 Stylus Care System.

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CIRCLE NO. 11 ON FREE INFORMATION CARD

# The \$149<sup>95</sup> personal computer.



## Introducing the Sinclair ZX81

If you're ever going to buy a personal computer, now is the time to do it.

The new Sinclair ZX81 is the most powerful, yet easy-to-use computer ever offered for anywhere near the price: only \$149.95\* completely assembled.

Don't let the price fool you. The ZX81 has just about everything you could ask for in a personal computer.

### A breakthrough in personal computers

The ZX81 is a major advance over the original Sinclair ZX80—the world's largest selling personal computer and the first for under \$200.

In fact, the ZX81's new 8K Extended BASIC offers features found only on computers costing two or three times as much.

Just look at what you get:

- Continuous display, including moving graphics
- Multi-dimensional string and numerical arrays

\*Plus shipping and handling. Price includes connectors for TV and cassette, AC adaptor, and FREE manual.

- Mathematical and scientific functions accurate to 8 decimal places
- Unique one-touch entry of key words like PRINT, RUN and LIST
- Automatic syntax error detection and easy editing
- Randomize function useful for both games and serious applications
- Built-in interface for ZX Printer
- 1K of memory expandable to 16K

The ZX81 is also very convenient to use. It hooks up to any television set to produce a clear 32-column by 24-line display. And you can use a regular cassette recorder to store and recall programs by name.

### If you already own a ZX80

The 8K Extended BASIC chip used in the ZX81 is available as a plug-in replacement for your ZX80 for only \$39.95, plus shipping and handling—complete with new keyboard overlay and the ZX81 manual.

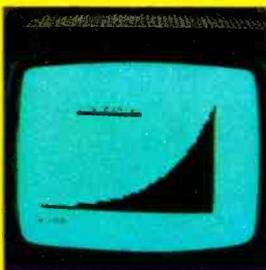
So in just a few minutes, with no special skills or tools required, you can upgrade your ZX80 to have all the powerful features of the ZX81. (You'll have everything except continuous display, but you can still use the PAUSE and SCROLL commands to get moving graphics.)

With the 8K BASIC chip, your ZX80 will also be equipped to use the ZX Printer and Sinclair software.

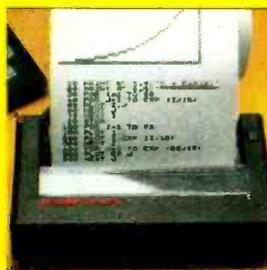
### Warranty and Service Program\*\*

The Sinclair ZX81 is covered by a 10-day money-back guarantee and a limited 90-day warranty that includes free parts and labor through our national service-by-mail facilities.

\*\*Does not apply to ZX81 kits.



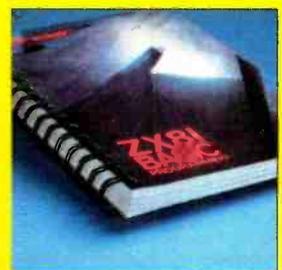
**NEW SOFTWARE:** Sinclair has published pre-recorded programs on cassettes for your ZX81, or ZX80 with 8K BASIC. We're constantly coming out with new programs, so we'll send you our latest software catalog with your computer.



**ZX PRINTER:** The Sinclair ZX Printer will work with your ZX81, or ZX80 with 8K BASIC. It will be available in the near future and will cost less than \$100.



**16K MEMORY MODULE:** Like any powerful, full fledged computer, the ZX81 is expandable. Sinclair's 16K memory module plugs right onto the back of your ZX81 (or ZX80, with or without 8K BASIC). Cost is \$99.95, plus shipping and handling.



**ZX81 MANUAL:** The ZX81 comes with a comprehensive 164-page programming guide and operating manual designed for both beginners and experienced computer users. A \$10.95 value, it's yours free with the ZX81.

# The \$99<sup>95</sup> personal computer.

## Introducing the ZX81 kit

If you really want to save money, and you enjoy building electronic kits, you can order the ZX81 in kit form for the incredible price of just \$99.95\* It's the same, full-featured computer, only you put it together yourself. We'll send complete, easy-to-follow instructions on how you can assemble your ZX81 in just a few hours. All you have to supply is the soldering iron.

### How to order

Sinclair Research is the world's largest manufacturer of personal computers.

The ZX81 represents the latest technology in microelectronics, and it picks up right where the ZX80 left off. Thousands are selling every week.

We urge you to place your order for the new ZX81 today. The sooner you order, the sooner you can start enjoying your own computer.

To order, simply call our toll free number, and use your MasterCard or VISA.

To order by mail, please use the coupon. And send your check or money order. We regret that we cannot accept purchase orders or C.O.D.'s.

**CALL 800-543-3000.** Ask for operator #509. In Ohio call 800-582-1364. In Canada call 513-729-4300. Ask for operator #509. Phones open 24 hours a day, 7 days a week. Have your MasterCard or VISA ready.

These numbers are for orders only. For information, you must write to Sinclair Research Ltd., One Sinclair Plaza, Nashua, NH 03061.

# sinclair

AD CODE	12PE	PRICE†	QTY.	AMOUNT
		ZX81	\$149.95	
		ZX81 Kit	99.95	
		8K BASIC chip (for ZX80)	39.95	
		16K Memory Module (for ZX81 or ZX80)	99.95	
		Shipping and Handling	4.95	\$4.95
		To ship outside USA add \$10.00		
		TOTAL		

MAIL TO: Sinclair Research Ltd., One Sinclair Plaza, Nashua, NH 03061.

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† U.S. Dollars



# EDITORIAL

## The IRS Cometh

Most readers determine their true income-tax bite on a calendar-year basis, so December 31 likely spells the end of the tax year for you. Therefore, you still have time to avail yourself of tax reductions related to your special interest in electronics for the year 1981.

Obviously, one cannot claim deductions for a purely avocational pursuit. But since so many readers use electronic-related equipment for business purposes, you may well have a legal opportunity to reduce your income tax. This is an advantage you might take even though you are an employee!

I remember a study made of purchasers of the MITS "Altair" computer in early 1975 in which more than 40% of respondents indicated that they purchased the computer for either business or combined business and personal use. Doubtless, this percentage has increased today.

"Business" means an activity for profit, of course. This might even be a requirement by an employer for the end user to do work at home that requires use of a computer. If this can be a proven fact, discuss the activity with your accountant. It could mean that the computer may be depreciated as business equipment. Furthermore, there's an ex-

tra depreciation that can be taken during the first year of a purchase, which has two clear advantages: Recouping more of your investment during the year you bought it and, if taken in '81, a more desirable year for tax deductions than 1982. Consider software tax deductions, too, as well as diskettes, printer paper, and so on.

Must you have a professional calculator, such as a Hewlett-Packard HP-41CV, which is not inexpensive, for your job? Well, this could be considered a tool of the trade, much as a plumber's or carpenter's tools. Do you pay dues to a professional organization such as the IEEE? Subscribe to publications to help you keep up with your profession, such as POPULAR ELECTRONICS? Buy texts on electronics? Don't overlook these possible deductions, either.

Additionally, do you earn extra income by servicing electronic equipment, producing computerized mailing lists for local business people, etc.? If you do, then you open up a new world of possible tax deductions, such as your telephone expenses, business entertainment, promotional material, automobile depreciation and expenses, test instruments, hand tools, home utility costs, etc. If the foregoing are also used for personal rea-

sons, then you would have to apportion a percentage of the expenses used for business purposes.

One doesn't even have to make a profit all the time to justify being in "business," so long as there is some profit for two or more years within a five-year period. Thus, you might be able to report some losses within a five-year period, decreasing your tax burden along the way.

A personally rewarding way to earn income as a result of your experience in electronics, of course, is to write articles for POPULAR ELECTRONICS. To get started, simply send a brief summary of your proposed article to my attention. If it's a construction project, include approximate cost to the builder and a legible schematic. There's no better way to prove that you're using your electronic gear (and typewriter, desk, et al.) for profit-making than to have your byline accompanying a published article, as well as a payment stub as proof of income.

Do check all this out with an accountant for the tax implications. There's a fair chance you're giving money to the IRS that rightly belongs to you.

*Art Salsberg*  
**Season's Greetings  
 and  
 Happy New Year**

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**CARMEN ROBLES**  
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**JEFF NEWMAN**  
*Editorial Assistant*

*Contributing Editors*

**Carl Warren, Stan Prentiss, Glenn Hauser,  
 Julian Hirsch, Forrest Mims, Walter Buchsbaum**

**MARIE MAESTRI**  
*Executive Assistant*

**Editorial and Executive Offices**  
 One Park Avenue  
 New York, New York 10016  
 212 725-3500

**Publisher**  
 Joe E. Mesics  
 212 725-3568

**New York Office**  
 Advertising Manager:  
 Richard Govatski (725-7460)  
**Sales:**  
 Tom Ballou (725-3578)  
 Ken Lipka (725-3580)

**Midwestern Office**  
 Suite 1400, 180 N. Michigan Ave.,  
 Chicago, IL 60601 (312 346-2600)  
 Sales: Ted Welch

**Western Representative**  
 Norman S. Schindler & Associates, Inc.  
 7050 Owensmouth Ave., #209  
 Canoga Park, CA 91303 (213 999-1414)  
 Sales: Norm Schindler, Jon Marshall

**Representation in Japan**  
 James Yagi  
 Oji Palace Aoyama  
 6-25, Minami Aoyama, 6 Chome, MinatoKu  
 Tokyo, Japan (407-1930/6821, 582-2851)

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# Synthesized Hand-Held Scanner!

Chances are the police, fire and weather emergencies you'll read about in tomorrow's paper are coming through on a scanner right now. All scanners sold by **Communications Electronics** bring the real live excitement of action news into your home or car. With your scanner, you can monitor the exciting two-way radio conversations of police and fire departments, intelligence agencies, mobile telephones, energy/oil exploration crews, drug enforcement agencies and more.

Some scanners can even monitor aircraft transmissions! You can actually hear the news before it's news. If you do not own a scanner for yourself, now's the time to buy your new scanner from **Communications Electronics**. Choose the scanner that's right for you, then call our toll-free number to place your order with your Master Card or Visa. A scanner is an excellent holiday gift.

We give you excellent service because **CE** distributes more scanners worldwide than anyone else. Our warehouse facilities are equipped to process thousands of scanner orders every week. We also export scanners to over 300 countries and military installations. Almost all items are in stock for quick shipment, so if you're a person who prefers fact to fantasy and who needs to know what's really happening around you, order your scanner today from **CE!**

## NEW! Bearcat® 350

**The Ultimate Synthesized Scanner!**  
Allow 30-60 days for delivery after receipt of order due to the high demand for this product.  
List price \$599.95/CE price \$419.00  
**7-Band, 50 Channel • Alpha-Numeric • No-crystal scanner • AM Aircraft and Public Service bands • Priority Channel • AC/DC Bands:** 30-50, 118-136 AM, 144-174, 421-512 MHz.  
The new Bearcat 350 introduces an incredible breakthrough in synthesized scanning: Alpha-Numeric Display. Push a button—and the Vacuum Fluorescent Display switches from "numeric" to word descriptions of what's being monitored. 50 channels in 5 banks. Plus, Auto & Manual Search, Search Direction, Limit & Count, Direct Channel Access, Selective Scan Delay, Dual Scan Speeds, Automatic Lockout, Automatic Squelch, Non-Volatile Memory. Reserve your Bearcat 350 today!

## Bearcat® 300

List price \$549.95/CE price \$339.00  
**7-Band, 50 Channel • Service Search • No-crystal scanner • AM Aircraft and Public Service bands • Priority Channel • AC/DC Bands:** 32-50, 118-136 AM, 144-174, 421-512 MHz.  
The Bearcat 300 is the most advanced automatic scanning radio that has ever been offered to the public. The Bearcat 300 uses a bright green fluorescent digital display, so it's ideal for mobile applications. The Bearcat 300 now has these added features: Service Search, Display Intensity Control, Hold Search and Resume Search keys, Separate Band Keys to permit lock-in/lock-out of any band for more efficient service search.



## NEW! Bearcat® 350

## Bearcat® 250

List price \$429.95/CE price \$269.00  
**6-Band, 50 Channel • Crystalline • Searches Stores • Recalls • Digital clock • AC/DC Priority Channel • Delay • Count Feature**  
Frequency range: 32-50, 146-174, 420-512 MHz.  
The Bearcat 250 performs any scanning function you could possibly want. With push button ease you can program up to 50 channels for automatic monitoring. Push another button and search for new frequencies. There are no crystals to limit what you want to hear. A special search feature of the Bearcat 250 actually stores 64 frequencies and recalls them, one at a time. Overseas customers should order the Bearcat 250FB at \$379.00 each. This model has 220 V AC/12 V DC power supply and 66-88 MHz low band coverage.

## NEW! Bearcat® 20/20

List price \$449.95/CE price \$279.00  
**7-Band, 40 Channel • Crystalline • Searches AM Aircraft and Public Service bands • AC/DC Priority Channel • Direct Channel Access • Delay**  
Frequency range: 32-50, 118-136 AM, 144-174, 420-512 MHz.  
The Bearcat 20/20 automatic scanning radio replaces the Bearcat 220 and monitors 40 frequencies from 7 bands, including aircraft. A two-position switch, located on the front panel, allows monitoring of 20 channels at a time.

## Bearcat® 210XL

List price \$349.95/CE price \$219.00  
**6-Band, 18 Channel • Crystalline • AC/DC**  
Frequency range: 32-50, 144-174, 421-512 MHz.  
The Bearcat 210XL scanning radio is the second generation scanner that replaces the popular Bearcat 210 and 211. It has almost twice the scanning capacity of the Bearcat 210 with 18 channels plus dual scanning speeds and a bright green fluorescent display. Automatic search finds new frequencies. Features scan delay, single antenna, patented track tuning and more!

## Bearcat® 160

List price \$299.95/CE price \$184.00  
**5-Band, 16 Channel • AC only • Priority Dual Scan Speeds • Direct Channel Access**  
Frequency range: 32-50, 144-174, 440-512 MHz.  
Would you believe...the Bearcat 160 is the least expensive Bearcat crystalline scanner.

This scanner presents a new dimension in scanning form and function. Look at the smooth keyboard. No buttons to punch. No knobs to turn. Instead, finger-tip pads provide control of all scanning operations, including On/Off, Volume and Squelch. Of course the Bearcat 160 incorporates other advanced Bearcat features such as Priority, Direct Channel Access, Dual Scan Speeds, Lockout, Scan Delay and more.

## NEW! Bearcat® 100

**The first no-crystal programmable handheld scanner.**  
Allow 60-120 days for delivery after receipt of order due to the high demand for this product.  
List price \$449.95/CE price \$299.00  
**8-Band, 16 Channel • Liquid Crystal Display Search • Limit • Hold • Lockout • AC/DC**  
Frequency range: 30-50, 138-174, 406-512 MHz.  
The world's first no-crystal handheld scanner has been compressed into a 3" x 7" x 1 1/4" case more scanning power than is found in many base or mobile scanners. The Bearcat 100 has a full 16 channels with frequency coverage that includes all public service bands (Low, High, UHF and "T" bands), the 2-Meter and 70 cm. Amateur bands, plus Military and Federal Government frequencies. It has chrome-plated keys for functions that are user controlled, such as lockout, manual and automatic scan. Even search is provided, both manual and automatic. Wow...what a scanner!

The Bearcat 100 produces audio power output of 300 milliwatts, is track-tuned and has selectivity of better than 50 dB down and sensitivity of 0.6 microvolts on VHF and 1.0 microvolts on UHF. Power consumption is kept extremely low by using a liquid crystal display and exclusive low power integrated circuits.

Included in our low CE price is a sturdy carrying case, earphone, battery charger/AC adapter, six AA ni-cad batteries and flexible antenna. For earliest delivery from **CE**, reserve your Bearcat 100 today.

## Bearcat® 5

List price \$134.95/CE price \$94.00  
**4-Band, 8 Crystal Channels • Lockout • AC only**  
Frequency range: 33-50, 146-174, 450-508 MHz.  
The Bearcat 5 is a value-packed crystal scanner built for the scanning professional — at a price the first-time buyer can afford. Individual lockout switches. Order one crystal certificate for each channel.

## Bearcat® Four-Six ThinScan™

List price \$189.95/CE price \$124.00  
Frequency range: 33-47, 152-164, 450-508 MHz.  
The incredible, Bearcat Four-Six Thin Scan™ is like having an information center in your pocket. This four band, 6 channel crystal controlled scanner has patented Track Tuning on UHF, Scan Delay and Channel Lockout. Measures 2 3/4 x 6 1/4 x 1 1/2". Includes rubber ducky antenna. Order crystal certificate for each channel. Made in Japan.

### TEST ANY SCANNER

Test any scanner purchased from **Communications Electronics** for 31 days before you decide to keep it. If for any reason you are not completely satisfied, return it in original condition with all parts in 31 days, for a prompt refund (less shipping/handling charges and rebate credits).

## Fanon Slimline 6-HLU

List price \$169.95/CE price \$109.00  
**Low cost 6-channel, 4-band scanner!**  
The Fanon Slimline 6-HLU gives you six channels of crystal controlled excitement. Unique Automatic Peak Tuning Circuit adjusts the receiver front end for maximum sensitivity across the entire UHF band. Individual channel lockout switches. Frequency range 30-50, 146-175 and 450-512 MHz. Size 2 3/4 x 6 1/4 x 1 1/2". Includes rubber ducky antenna. Order crystal certificates for each channel. Made in Japan.

## Fanon Slimline 6-HL

List price \$149.95/CE price \$99.00  
**6-Channel performance at 4-channel cost!**  
Frequency range: 30-50, 146-175 MHz.  
If you don't need the UHF band, get this model and save money. Same high performance and features as the model HLU without the UHF band. Order crystal certificates for each channel. Made in Japan.

### OTHER SCANNERS & ACCESSORIES

NEW! Regency† D810 Scanner	\$319.00
NEW! Regency† D300 Scanner	\$219.00
NEW! Regency† D100 Scanner	\$169.00
NEW! Regency† H604 Scanner	\$129.00
Regency† M400 Scanner	\$259.00
Regency† M100 Scanner	\$199.00
Regency† R1040 Scanner	\$149.00
SCMA-6 Fanon Mobile Adapter/Battery Charger	\$49.00
CHB-6 Fanon AC Adapter/Battery Charger	\$15.00
CAT-6 Fanon carrying case with belt clip	\$15.00
AUC-3 Fanon auto lighter adapter/Battery Charger	\$15.00
PSK-6 Base Power Supply/Bracket for SCMA-6	\$20.00
SP50 Bearcat AC Adapter	\$9.00
SP51 Bearcat Battery Charger	\$9.00
SP58 Bearcat 4-6 ThinScan™ carrying case	\$12.00
MA506 Regency carrying case for H604	\$15.00
FB-E Frequency Directory for Eastern U.S.A.	\$12.00
FB-W Frequency Directory for Western U.S.A.	\$12.00
FFD Federal Frequency Directory for U.S.A.	\$12.00
TSG "Top Secret" Registry of U.S. Government Freq.	\$10.00
B-4 1.2 V AAA Ni-Cad batteries (set of four)	\$9.00
A-135cc Crystal certificate	\$3.00
Add \$3.00 shipping for all accessories ordered at the same time.	

### INCREASED PERFORMANCE ANTENNAS

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# NEW PRODUCTS

Additional information on new products covered in this section is available from the manufacturers. Either circle the item's code number on the Free Information Card or write to the manufacturer at the address given.

## Mitsubishi Receiver



The top-of-the-line DA-R35 has linear switching circuitry, a built-in moving-coil head amp, quartz-synthesized tuning with fluorescent digital display, and seven AM and seven FM presets. The R35 offers 85 watts per channel and features two-speaker relay switching, 12 dB/octave high and low filters, separate record selector with two-tapedeck duplication, mode switch, ten-position loudness compensation, and an input selector with separate MM and MC switches. Specs (amp section): frequency response 20 to 20,000 Hz  $\pm 3$  dB; THD, 0.005%. The tuner section offers automatic scan, LED signal-strength and multipath indicators, and auto i-f and high-blend circuits with LED indicators and manual defeat. Specs include a THD of 0.10% on wide-band stereo, and a capture ratio of 1.5 dB. \$650.

CIRCLE NO. 91 ON FREE INFORMATION CARD

## Household Remote Control

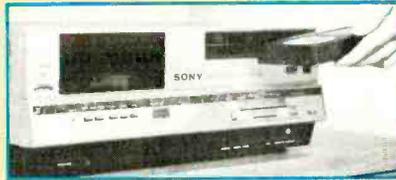
The System X-10 from BSR is designed for use in conjunction with a remote-control setup (also from BSR) for home appliances. If you are away, but able to telephone your home, it is now possible to turn any or all of your appliances on or off as long as they are plugged into the base sta-



tion. The system is comprised of a calculator-sized telephone transmitter and a base station connected to the owner's telephone. To operate it, the owner dials a three-number security code into the base station before leaving the house. When it receives a call, the base station waits 40 seconds and then answers. The owner places the transmitter to the telephone mouthpiece, enters the code and punches in the desired commands. It works the same way with answering machines; the owner ignores the outgoing message and proceeds as above. The transmitter, designated Model TR274, has to be used with the TR270 telephone responder/controller. As a set they cost \$100. A transmitter alone costs \$30, and any number of them can be used with the TR270.

CIRCLE NO. 92 ON FREE INFORMATION CARD

## Front-Operation Betamax

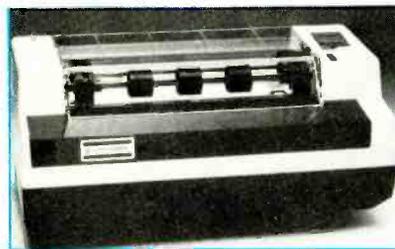


Sony's new Betamax video cassette recorder, the SL-5000, loads from the front, where controls are also located. The SL-

5000 has all the usual features associated with Betamax: BetaScan, pause control, freeze frame, and an electronic tuner/timer with a 14-pushbutton channel selector. The unit records in Beta II or Beta III, and plays back in the mode of either Beta I, II, or III. A 24-hour timer can be set to program at the same time of day, as long as there is tape on the feed side of the cassette. A four-digit counter keeps track of the tape contents. \$895.

CIRCLE NO. 94 ON FREE INFORMATION CARD

## VIC 20 Printer



Commodore's Computer Systems Div. has announced availability of a printer for its personal computer, the VIC 20. The printer, designated VIC 1515, can print any of the alphabetic, numeric, and graphic symbols common to the VIC 20. It uses a dot-matrix format and has a speed of 30 cps. Options allow the VIC 1515 to print extra-wide and reversed (negative) characters. \$395.

CIRCLE NO. 93 ON FREE INFORMATION CARD

## Earth Station Receiver

The ESR24 earth station receiver from R.L. Drake operates over a frequency range of



3.7 to 4.2 GHz and features digital channel display, preset and variable audio subcarrier selector, afc, and full metering. For installation versatility, the down converter module (included) may be mounted internally or at the antenna. Accessories (optional) include a remote control, a remote tuning meter, and splash-proof housing. \$1000.

CIRCLE NO. 95 ON FREE INFORMATION CARD

## Polyphonic Moog Synthesizer



The Realistic MG-1 Synthesizer (42-2000) by Moog is being introduced at Radio Shack stores. The unit is said to be able to recreate the sounds of musical instruments—from a piccolo to a tuba to an organ—as well as creating special effects such as churning helicopters, raging storms, etc. Its polyphonic quality permits it to play whole chords, rather than simply one note at a time. The MG-1 features a 2½-octave full-chromatic keyboard with three-octave tone sources that produce variable wave shapes (square, triangular, random). Also offered is a detune control for a dissonant or full-interval offsetting of pitch. A modulation section creates vibrato and tremolo, pulsating notes, random tone sequences, and glide effects. The four-control filter alters timbre by controlling cutoff frequency, peak emphasis, and contoured

# Why use their flexible discs:

Athana, BASF, Control Data, Dysan, IBM, Maxell, Nashua, Scotch, Shugart, Syncom, 3M, Verbatim or Wabash when you could be using

# MEMOREX

for as low as \$1.94 each?

Find the flexible disc you're now using on our cross reference list... then write down the equivalent Memorex part number you should be ordering.

Product Family	Product Description	Memorex Part Number (3201-)	CE quant. per disc (\$)	Athana	BASF	Dysan	IBM	Maxell	Nashua	Scotch 3M	Shugart	Syncom	Verbatim	Wabash	Control Data
Flexible Disc 1a Single Headed Drive Single Density Media	IBM Compatible 128 B/S 26 Sectoral	3060	1.99	473071	53426	800506	2305630	FD1-126	FD 1	740 0	B/A-100	15002	FD34-9000	F111111X	421002
	IBM Compatible 128 B/S 26 Sectoral w/ W.P.N.	3067	2.04	—	—	—	—	—	—	740-0	—	—	FD34-9000	—	—
	IBM Compatible 128 B/S 26 Sectoral w/ W.P.N. & Hub Ring	3064	2.39	—	—	—	—	—	—	—	—	—	FD34-9000	—	—
	IBM Compatible 128 B/S 26 Sectoral REVERSIBLE	1729	3.10	473072	54431	—	—	—	FD 2	740-0/2	—	15150	FF34-9000	F171111X	—
	IBM Strata-6 Compatible	3086	2.04	473077	54561	800509	1889959	—	—	740-0/096	—	15003	FD80-9000	F118111X	—
	IBM Compatible 128 B/S 15 Sectoral	3110	1.99	473074	—	800584	2305645	—	—	740-3000	—	15006	FD36-9000	F112111X	—
	IBM Compatible 128 B/S 8 Sectoral	3109	1.99	473074	—	800585	1889954	—	—	—	—	15004	FD80-9000	F113111X	—
	Shugart Compatible 32 Head Sector	3015	1.99	470091	53802	10111	—	FD1-32	FD 132	740-32	B/A-101	15025	FD32-9000	—	421327
	Wang Compatible 32 Head Sector w/Hub Ring	3087	2.48	—	54491	—	—	—	—	740-32RH	—	—	—	F37A411X	—
	CPI 8000 Compatible	3045	2.89	—	—	—	—	—	—	—	—	15228	—	—	—
Flexible Disc 1d Single Headed Drive Double Density Media	IBM Compatible 128 B/S 26 Sectoral	3060	2.89	474071	54568	3740-1D	—	FD1-126/M2100	FD-1D	741-0	—	—	FD34-8000	F131111X	423002
	IBM Compatible 128 B/S 26 Sectoral REVERSIBLE	3063	3.89	—	—	—	—	—	—	—	—	—	FD34-8000	—	—
	Shugart Compatible 32 Head Sector	3001	2.89	470091	54598	10111D	—	FD1-32D	—	741-32	B/A-103	15075	FD32-8000	F33A411X	423222
	Wang Compatible 32 Head Sector w/Hub Ring	3088	3.09	—	—	—	—	—	—	—	—	—	—	—	—
Flexible Disc 2a Double Headed Drive Single Density Media	Soft Sector 128 B/S 26 Sectoral	3113	3.09	—	54426	800814	1788870	—	—	—	B/A-150	15163	FD10-4028	F121111X	424812
	Soft Sector 128 B/S 15 Sectoral	3108	3.09	473477	54226	800815	2738700	FD2-256D	—	742-0	—	15154	FD10-4015	F122111X	—
Flexible Disc 2a Double Headed Drive Double Density Media	Soft Sector (Unformatted)	3102	3.09	473485	—	02150	—	FD2-30M	FD 2D	743-0	—	15103	0034-4001	—	425002
	Soft Sector 128 B/S 26 Sectoral	3115	3.09	—	—	—	—	—	—	—	B/A-150	—	—	—	—
	Soft Sector 128 B/S 26 Sectoral	3103	3.09	473471	54325	800817	1788872	FD2-256D	—	743-0/256	—	15101	0034-4028	F144111X	425002
	Soft Sector 128 B/S 15 Sectoral	3114	3.09	473472	54479	800818	1889944	—	—	743-0/512	—	15100	0034-4015	F145111X	425022
	Soft Sector 1024 B/S 8 Sectoral	3104	3.09	473473	54485	800819	1889945	—	—	743-0/1024	—	15102	0034-4008	F147111X	425022
	32 Head Sector	3105	3.09	470061	—	10170	—	FD2-32D	—	743-32	B/A-151	15125	0032-4000	F34A411X	425222
	Burroughs 8-80 Compatible 32 Head Sector	3116	3.49	—	—	—	—	—	—	—	—	—	—	F34A411X	—
	Soft Sector 1024 B/S 8 Sectoral w/ Hub Ring	3161	3.39	—	—	—	—	—	—	—	—	—	—	—	—
	Shugart Compatible 32 Head Sector	3161	3.39	—	—	—	—	—	—	—	—	—	—	—	—
	FD Vi (Vint) Jackell	3071003	2.89	470851	—	FDV	—	—	FD-185	811-0	—	15026	FD86-1000	F81A111X	—
Mini Flexible Disc 1a 5 1/4" Single-Headed Drive Single Density Media	Soft Sector (Unformatted)	3401	1.94	475001	54256	10471	—	MD1	MD 1	744-0	B/A-104	15300	MD25-01	M1A211X	441002
	10 Head Sector	3403	1.94	475010	54257	10717	—	—	MD 110	744 10	B/A-107	15325	MD25-10	M4A211X	441102
	16 Head Sector	3405	1.94	475016	54258	10571	—	—	MD 116	744 16	B/A-108	15326	MD25-16	M5A211X	441162
	Soft Sector (Unformatted) w/Hub Ring	3431	2.14	—	—	—	—	—	—	—	—	—	—	—	—
	10 Head Sector w/Hub Ring	3433	2.14	—	—	—	—	—	—	—	—	—	—	—	—
	16 Head Sector w/Hub Ring	3435	2.14	—	—	—	—	—	—	—	—	—	—	—	—
Mini Flexible Disc 1d 5 1/4" Single-Headed Drive Double Density Media	Soft Sector (Unformatted)	3417	2.14	—	54846	10470	—	—	—	—	—	—	—	—	—
	10 Head Sector	3418	2.14	—	54846	10710	—	—	—	—	—	—	—	—	—
	16 Head Sector	3419	2.14	—	54852	10510	—	—	—	—	—	—	—	—	—
	Soft Sector (Unformatted) w/Hub Ring	3481	2.34	—	—	—	—	—	—	—	—	—	—	—	—
	10 Head Sector w/Hub Ring	3483	2.34	—	—	—	—	—	—	—	—	—	—	—	—
	16 Head Sector w/Hub Ring	3485	2.34	—	—	—	—	—	—	—	—	—	—	—	—
Mini Flexible Disc 2a 5 1/4" Double-Headed Drive Double Density Media	Soft Sector (Unformatted)	3421	2.59	—	54824	10470	—	—	—	—	B/A-184	—	—	—	—
	10 Head Sector	3423	2.59	—	54827	10720	—	—	—	—	B/A-187	—	—	—	—
	16 Head Sector	3425	2.59	—	54830	10520	—	—	—	—	B/A-185	—	—	—	—
	Soft Sector (Unformatted) w/Hub Ring	3491	2.79	—	—	—	—	—	—	—	—	—	—	—	—
	10 Head Sector w/Hub Ring	3493	2.79	—	—	—	—	—	—	—	—	—	—	—	—
	16 Head Sector w/Hub Ring	3495	2.79	—	—	—	—	—	—	—	—	—	—	—	—

## Memorex Flexible Discs...The Ultimate in Memory Excellence

### Quality

Memorex means quality products that you can depend on. Quality control at Memorex means starting with the best materials available. Continual surveillance throughout the entire manufacturing process. The benefit of Memorex's years of experience in magnetic media production, resulting, for instance, in proprietary coating formulations. The most sophisticated testing procedures you'll find anywhere in the business.

### 100 Percent Error Free

Each and every Memorex Flexible Disc is certified to be 100 percent error free. Each track of each flexible disc is tested, individually, to Memorex's stringent standards of excellence. They test signal amplitude, resolution, low-pass modulation, overwrite, missing pulse error, and extra pulse error. They are torque-tested, and competitively tested on drives available from almost every major drive manufacturer in the industry including drives that Memorex manufactures. Rigid quality audits are built into every step of the manufacturing process and stringent testing result in a standard of excellence that assures you, our customer, of a quality product designed for increased data reliability and consistent top performance.

### Customer-Oriented Packaging

Memorex's commitment to excellence does not stop with a quality product. They are proud of their flexible discs and their packaging them with pride. Both their packaging and their labeling have been designed with your ease of identification and use in mind. The desk-top box containing ten discs is convenient for filing and storage. Both box labels and jacket labels provide full information on compatibility, density, sectoring, and record length. Envelopes with multi-language care and handling instructions and color-coded removable labels are included. A write-protect feature is available to provide data security.

### Full One Year Warranty — Your Assurance of Quality

Memorex Flexible Discs will be replaced by Memorex if they are found to be defective in materials or workmanship within one year of the date of purchase. Other than replacement, Memorex will not be responsible for any damages or losses (including consequential damages) caused by the use of Memorex Flexible Discs.

### Quantity Discounts Available

Memorex Flexible Discs are packed 10 discs to a carton and 10 cartons to a case. Please order only in increments of 100 units for quantity 100 pricing. We are also willing to accommodate your smaller orders. Quantities less than 100 units are available in increments of 10 units at a 10% surcharge. Quantity discounts are also available. Order 500 or more discs at the same time and deduct 1%; 1,000 or more saves you 2%; 2,000 or more saves you 3%; 5,000 or more saves you 4%; 10,000 or more saves you 5%; 25,000 or more saves you 6%; 50,000 or more saves you 7% and 100,000 or more discs earns you an 8% discount off our super low quantity 100 price. Almost all Memorex Flexible Discs are immediately available from CE. Our warehouse facilities are equipped to help us get you the quality product you need, when you need it. If you need further assistance to find the flexible disc that's right for you, call the Memorex compatibility hotline. Dial 800-53-9080 and ask for the flexible disc hotline extension 0997. In California dial 800-672-3525 extension 0997.

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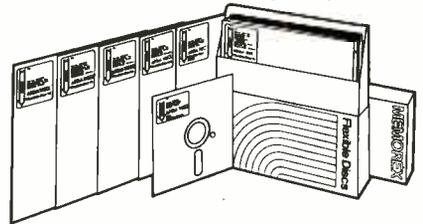
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# MFJ SHORTWAVE ACCESSORIES

**NEW Indoor Tuned Active Antenna. Rivals, can even exceed reception of outside long wire.**

**Rivals long wires**

**\$79<sup>95</sup>**



**MFJ-1020 NEW INDOOR ACTIVE ANTENNA** sits on your desk ready to listen to the world. Rivals, can often exceed, reception of outside long wire. Unique Tuned Active Antenna minimizes intermod, provides RF selectivity, reduces noise outside tuned band. Also use as preselector for external antenna. Covers 300 KHz to 30 MHz in five bands. Adjustable telescoping antenna. Controls: Tune, Band Selector, Gain, On-Off/Bypass. LED, FET, bipolar circuitry. Phono jack for external ant. 6x2x6 inches. 9-12 VDC or 9 V battery for portable use. 110 VAC with optional AC adapter, \$9.95.



**\$99<sup>95</sup>**

**MFJ-1040 RECEIVER PRESELECTION.** Improves weak signal reception, rejects out-of-band signals, reduces image response, 1.8 to 54 MHz. Up to 20 db gain. Low noise MOSFET. Gain control. Bandswitch. Can use 2 ant., 2 rcvrs. ON-OFF/Bypass. 20 db attenuator. LED. Coax, phono jacks. 8x2x6 in. Also for XCVRS to 350 watts input. Auto bypass. Delay control. PTT jack. **MFJ-1045, \$69.95.** Same as MFJ-1040, less attenuator, xcvr auto bypass, delay control, PTT. Use 1 ant., 1 rcvr. 5x2x6 in. 9V bat. Both requires 9-18 VDC or 110 VAC with optional AC adapter, \$9.95.

**\$89<sup>95</sup>**



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cutoff. The contour section adjusts tonal rise and fall times; and a sustain feature can be switched in and out. Volume control is available at both headphone and phono-type output jacks that permit direct connection to most sound systems. Dimensions are 20 1/2" L x 12" W x 5" D. \$500.

CIRCLE NO. 96 ON FREE INFORMATION CARD

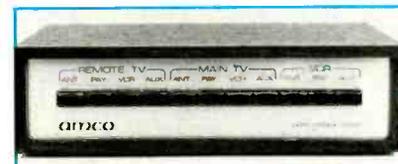
## Logic-Control/Metal-Cassette Deck



The Luxman Div. of Alpine Electronics of America introduces its K-117 two-headed cassette deck that offers full-logic solenoid controls, metal-tape capability, and Dolby B noise reduction. A front-panel bias control switch permits a user to alter frequency response of any tape to  $\pm 15\%$  of the normal bias point. A fluorescent peak-level indicator adds 3-dB calibration for metal tape, up to  $\pm 8$  dB. Other features include an MPX filter, and independent left and right input level controls. Specs: 0.04% wow and flutter, 70 dB S/N (Dolby metal), 20 Hz to 20 kHz response (metal), and 0.7% THD (LH tape) at the 1-kHz/0-dB level. Dimensions are 18" W x 5" H x 10 1/2" D. \$400.

CIRCLE NO. 97 ON FREE INFORMATION CARD

## Video Control Center



The Model VCC-1 Video Control Center from Amco Electronics is a passive 4-input/3-output rf switcher for use with home video systems. Any four sources such as antenna (vhf and uhf), cable, VCR, disk, computer, video games, etc. can be selected independently for viewing on two TVs; and any three can be selected for VCR recording. Internal shielding maintains isolation of at least 60 dB between ports (channels 2-13). Weight is two lb. Dimensions are 8" W x 2 1/2" H x 5 1/2" D. \$100.

CIRCLE NO. 98 ON FREE INFORMATION CARD

## Portable Digital Lab

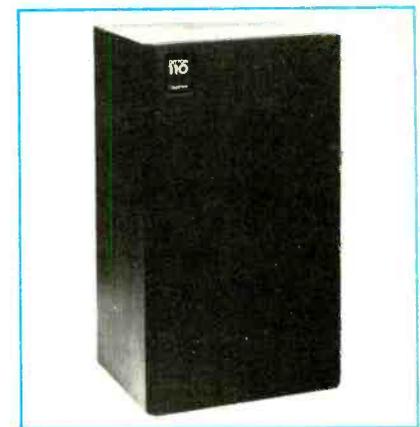
B&K-Precision has introduced a lab-logic analyzer, the LA-1000. Designed for use by field engineers in the maintenance of microcomputers, it features a 20-MHz single-channel logic analyzer (TTL, CMOS) that



can present data in both time and state domains. State data are formatted in hexadecimal on the four-digit LED display. Timing diagrams can be displayed on most conventional oscilloscopes. The memory permits storage of 16 "words," at 16 bits per "word," providing a total memory capacity of 256 bits. The signature analyzer is formatted in hexadecimal and offers continuous and hold modes. It is compatible with TTL, MOS, or CMOS, and has self-test capability. Autoranging circuitry eliminates the need for a separate DVOM; ac and dc voltage and resistance are displayed on a 3 1/2-digit LED. After a user selects a function and interfaces to a circuit being tested, a built-in microprocessor automatically selects the range providing maximum resolution. Accuracy is claimed to 0.1% dc, 0.5% ac; and to within 0.3% for resistance. The frequency counter also provides autoranging measurements from 1 Hz to 25 MHz. Resolution is given as 1 Hz. The unit weighs 14 lb. \$1,745.

CIRCLE NO. 99 ON FREE INFORMATION CARD

## Laser-Designed Speakers



Celestion Industries claims to have corrected the minor distortions that affect vibration modes of loudspeaker surfaces by using laser interferometry. The Ditton 110 speaker is an acoustic suspension system with an 8" PVC-surround cone woofer and a 1" dome tweeter, aligned vertically. Specifications: frequency response, 72-20,000 Hz; crossover frequency, 2.3 kHz; impedance, 5 ohms; amplifier requirements, 10 to 50 W/ch; dimensions, 16 3/4" H x 9 1/2" W x 8 3/4" D. \$350 a pair.

CIRCLE NO. 100 ON FREE INFORMATION CARD

# Listen to your planet on a short-wave receiver!

Communications Electronics, the world's largest distributor of radio scanners, is pleased to introduce Panasonic Command Series shortwave receivers. Panasonic lets you listen to what the world has to say. Unlike a scanner, a Command Series radio lets you listen to shortwave broadcasts from countries around the world, as well as the U.S.A. It's the space age shortwave performance you've been waiting for...at a down to earth price you can afford.

All Panasonic shortwave receivers sold by Communications Electronics bring the real live excitement of international radio to your home or office. With your Command Series receiver, you can monitor exciting radio transmissions such as the BBC, Radio Moscow, Ham Radio and our own Armed Forces Radio Network. Thousands of broadcasts in hundreds of different languages are beamed into North America every day. You can actually hear the news *before* it's news. If you do not own a shortwave receiver for yourself, now's the time to buy your new receiver from CE. Choose the receiver that's right for you, then call our toll-free number to place your order with your credit card.

We give you excellent service because CE distributes more scanners and shortwave receivers worldwide than *anyone* else. Our warehouse facilities are equipped to process thousands of orders every week. We also export receivers to over 300 countries and military installations. Almost all items are in stock for quick shipment, so if you're a person who needs to know what's *really* happening around you, order today from CE.

## Panasonic® RF-6300

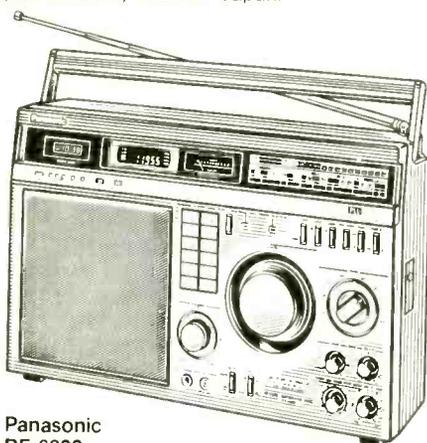
Allow 30-120 days for delivery after receipt of order due to the high demand for this product.

List price \$749.95/CE price \$499.00

Bands: LW 150-410 KHz., MW 520-1610 KHz.

SW1-5 1.6-30 MHz., FM 87.5-108 MHz.

The new Panasonic RF-6300 Command Series PLL synthesized 8-band portable communications receiver, lets you hear the world. The RF-6300 has features such as microcomputer pre-set tuning and PLL quartz synthesized digital tuner. Microcomputer stores up to 12 different frequencies for push-button recall. FM/MW/LW/SW1-5 reception. Manual tuning knob. Wide/Narrow bandwidth selector. Double superheterodyne system. Fast/Slow manual tuning. Built-in quartz digital alarm clock. 5 inch dynamic PM speaker. 3 antennas. Multi-voltage. Detachable AC cord. Operates on 6 "D" batteries (not included). Made in Japan.



Panasonic RF-6300



RF-4900

## Panasonic® RF-4900

List price \$549.95/CE price \$389.00

Bands: MW 525-1610 KHz., SW1-8 1.6-30 MHz.

FM 88-108 MHz.

The Panasonic RF-4900 shortwave receiver features a 5-digit fluorescent display for all 8 SW bands, as well as for AM/FM, AC or battery operation. Full coverage from 1.6 to 30 MHz on SW. Covers SSB and CW. Premix Double Superheterodyne. Fast/slow 2 speed tuning. AFC Switch on FM, narrow/wide selectivity switch for AM and SW. Antenna trimmer. Calibration control. FET RF circuit. Mode switch for AM-CW/SSB. BFO Pitch control. ANL switch for AM. RF gain control. Tuning-Battery meter with meter function switch. Separate bass and treble tone control. Dial light switch. Digital display on/off switch. Separate power switch. Rack type handle. Made in Japan.

## Panasonic® RF-3100

Allow 30-120 days for delivery after receipt of order due to the high demand for this product.

List price \$369.95/CE price \$269.00

Bands: MW 525-1610 KHz., SW1-29 1.6-30 MHz.

FM 88-108 MHz.

The Panasonic RF-3100 portable 31-Band portable radio has PLL Quartz-Synthesizer tuning that "locks" onto SW stations. Operates on AC or battery. SW frequencies from 1.6 to 30 MHz. are in 29 bands. All-band 5-digit frequency readout. Horizontal design with front mounted controls for shoulder strap operation. Double superheterodyne for clean SW reception. BFO pitch and RF gain controls. Separate bass and treble controls. Wide/Narrow bandwidth selector. Meter for tuning and battery strength. LED operation indicator. Meter light switch. 3 1/2" PM dynamic speaker. Comes with detachable shoulder belt. Battery power (8 "D" batteries not included). Made in Japan.

## Panasonic® RF-2900

List price \$349.95/CE price \$249.00

Bands: MW 525-1610 KHz., SW1-3 3.2-30 MHz.

FM 88-108 MHz.

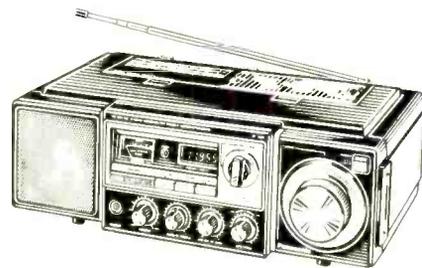
The Panasonic RF-2900 is a portable five-band shortwave radio with digital five digit fluorescent frequency display. Full coverage from 3.2 to 30 MHz. on SW. Covers SSB and CW. Double superheterodyne receiver. Fast/slow two speed tuning. AFC switch on FM, narrow/wide selectivity switch for AM and SW. FET RF circuit. BFO switch and pitch control. RF gain control. Tuning battery meter. Separate bass/treble tone control. SW calibration control. Dial light switch. Digital display on/off switch. Separate power switch. Detachable dial hood included. Rack type handle. Includes whip antenna and ferrite core antenna, speaker, earphone, recording output jacks, AC line and detachable adjustable shoulder belt. Made in Japan.



Command Series RF-2900

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# Panasonic Command Series



Panasonic RF-3100

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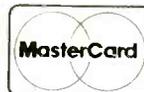
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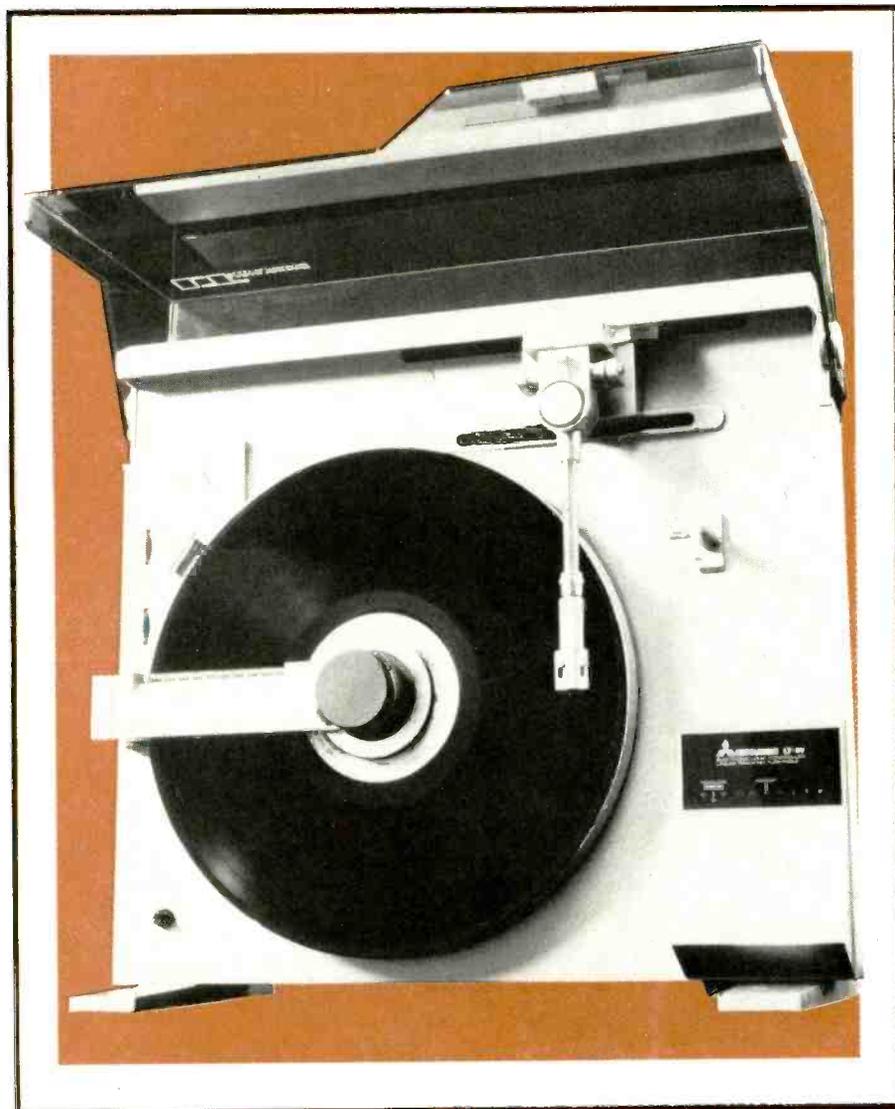
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# Audio Product of the Month

CHOSEN BY THE EDITORS OF POPULAR ELECTRONICS

## *Mitsubishi LT-5V Record Player*



**T**HE Mitsubishi LT-5V record player offers a novel mechanical format. Vertically positioned, its linear-tracking servo-driven tonearm is suspended from its pivots with the cartridge hanging down. The heavy cast turntable is belt-driven by a dc servo motor and controlled by an LSI logic system through light-touch pushbuttons. Because of its vertical alignment, the platter is rigidly fastened to the drive shaft and the record is clamped to the rubber mat by a swing-away arm that locks to the center spindle when the record player is operating.

The depth of the LT-5V is very small (only 8") so it can be installed on ordinary bookshelves. However, since its height is 17", a large vertical clearance is needed. Each of the four rubber feet is separately adjustable for leveling. Turntable width is 18½", and it weighs 27½ pounds. \$450.

**General Description.** Other than its unusual orientation, the drive system of the Mitsubishi LT-5V is conventional. The servo-controlled dc motor drives a heavy inner flywheel through a belt. The external platter and rubber mat (which together weigh about 4 pounds) mount on an extension of the drive shaft. The center spindle screws into the end of the drive shaft to hold the platter in place.

An illuminated stroboscope pattern on the internal flywheel is visible through a window on the vertical "motorboard." It is marked for 33⅓ and 45 rpm at 50 or 60 Hz. Near the viewing window is a clear plastic prism that causes a light beam to shine across the mat. The mat and platter are perforated at four places

# Pocket Concert Hall

## PRICE SLASHED

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Cybernet has a problem. They built over 100,000 of these fabulous Pocket Concert Halls. They built them to be better than the Sony Walkman, but by the time they got them here, the price had already come down on the Walkman (it's now as low as \$139 in some areas) and lots of cheap Hong Kong-made recorders had hit the market.

The Cybernet was built to sell for \$200 and wholesale for \$119. It has a very sophisticated dual flywheel tape drive system and superb electronics. The best way to test a portable is to play music and shake it. If the music wows badly, or even stops, you have a cheap unit.

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First there was silence. Then from nowhere, there explodes an awesome kaleidoscope of sound. Violins from the left, trumpets from the right, while the sounds of a vocalist seem to come from somewhere above.

You've just experienced a breathtaking experience in sound. It's better than sitting in the middle of a full symphony orchestra.

As you take off the almost weightless 1½ ounce headphones and tentatively look around for the orchestra, the realization of the experience sinks in.

Now you can enjoy more beautiful sound from your cassettes while you are jogging or washing the car than when you are sitting in front of your stereo.

### SOMERSAULTS AND CARTWHEELS

If you were on a diet yesterday, you probably lost more weight than the trifling 16 oz. of the Pocket Concert Hall.

The Concert Hall goes where you go with a protective leatherette case that easily hangs on your belt or rides comfortably in your pocket. It also comes with an adjustable shoulder strap.

Whether you're into gymnastics, long walks, or just mowing the lawn, you'll enjoy the unbelievably rich stereo sound of the Pocket Concert Hall. And, to prove our point, we offer you a 30 day risk free trial.

### ENGINEERED FOR STRESS

Most fine stereo equipment is designed to be lovingly placed on a shelf and never moved. Obviously, this isn't a practical way to listen when you're walking your dog or riding a bike.

The Pocket Concert Hall incorporates a special dual flywheel tape movement system with a unique anti-rolling mechanism resembling a fine clock. It keeps the music smooth and stable, even when you're doing cartwheels.

The Pocket Concert Hall is specifically designed for rough treatment. Other machines are really redesigned dictation recorders. The Pocket Concert Hall is designed to provide superb stereo music while you're on the move.

You can expect years of enjoyment from this finely crafted revolutionary new sound machine.



### MUSIC AT YOUR COMMAND

You won't be 'roughing it' when you leave home. You can play all your cassettes since there is an equalization switch for Metal/Chrome or standard.

The sound is crisp and clean with a frequency response of 30-14,000hz. The entire system is operated by 4 AA batteries (included).

There are convenient lock-in fast forward and rewind buttons, plus cue and review. If you want to listen to a song again, just press the review button without ever leaving play.



You can listen to the outside world by pushing the talk switch without taking off the feather-light stereophones. Dual volume controls, auto-stop and a battery condition/operation indicator light round out a full compliment of convenience features to enhance your musical enjoyment.

### MULTINATIONAL GUARANTEE

Cybernet is an extremely large Japanese Company with a large presence here in the U.S. They manufacture component systems valued in the thousands of dollars and sophisticated wireless transmission systems.

Cybernet backs the Pocket Concert Hall not with the usual limited 90 day parts and labor guarantee, but with a limited full one year guarantee.

DAK is America's largest direct selling manufacturer of magnetic products. We add our own 30 day 100% satisfaction guarantee to protect your purchase.

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on each of four radii, allowing the light to pass through to photo sensors located below the motor board. This prevents operation of the unit without a record in place and protects the stylus from contact with the mat. The arm merely travels to the center of the platter with the cartridge raised, and returns to rest, shutting off the motor. If a 12-inch disc is on the turntable, the arm descends at the lead-in groove (since all the light "windows" are blocked by the record). A 7-inch record will block only the innermost holes, switching the turntable speed to 45 rpm and indexing the arm to a 7-inch diameter.

Special combinations of size and speed (such as 12-inch 45 rpm, 7-inch 33 $\frac{1}{3}$  rpm, or any 10-inch discs) must be played manually, using procedures described in the instruction manual. This also applies to translucent or colored records, or records of nonstandard size.

The tonearm is driven on a track rod by a small servo motor operating through a worm gear and a fine stainless-steel wire. The arm is free to pivot slightly (parallel to the record surface) and optical sensors on the arm carriage detect any deviation from the vertical position—in which the pickup is tangent to the record groove. The arm servo motor drives the carriage to reduce the tracking error toward zero. The servo operates both toward and away from the center of the record, and can accommodate slightly eccentric records, as well.

The arm itself is a short, straight tube equipped with a universal four-pin bayonet-mount headshell (cartridges with integral plugs can also be used). The cartridge is installed in its shell with the aid of a plastic gauge to set its longitudinal position and its distance from the shell mounting surface. First the arm is balanced with the cartridge hanging down vertically by turning a weight threaded into the structure at the rear of the arm. After balancing, the tracking force scale on the weight is set to zero, and the entire weight is then turned to provide the desired tracking for the stylus. The scale is calibrated from 0 to 2.5 grams at 0.1-gram intervals.

One advantage of the vertical record player is that the cartridge does not have to be counterbalanced, since its mass acts along the axis of the arm tube and pivot. Therefore, cartridges can be interchanged merely by plugging them in, with no need for re-balancing.

The tonearm has a relatively low mass, being just long enough to locate the stylus on a record radius, with no unnecessary overhang or bends to add mass to the arm. This provides very good tracking on warped records. The absence of an antiskating bias gives added

stability in the presence of vibration or jarring.

The vertical format of the LT-5V also serves to isolate the unit from base-conducted vibration. Vertical motion has virtually no effect on the cartridge output, since it acts along the axis of the rigid arm tube. Horizontal motion can have the same effect as on any other record player.

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## OPERATING FEATURES

### Controls (Flat pushbuttons)

**START:** Initiates operating cycle and slews arm inward when held in.

**STOP:** Initiates end of operation and slews arm outward when held in.

**SPEED:** Changes speed between 33 $\frac{1}{3}$  and 45 rpm on alternate operations (turntable always starts at 33 $\frac{1}{3}$  rpm)

**REPEAT:** Causes record to be repeated indefinitely (second touch cancels).

**LIFT/CUE:** Raises and lowers pickup on alternate operations.

**Speed Vernier Wheels:** Marked "33" and "45"; produce  $\pm 3\%$  shift in the nominal speeds.

**Indicator Lights:** On panel above operating buttons. Arrows show up or down arm position, direction of slew inward or outward. Dot shows status of REPEAT function. Arm deviation from tangency is shown by arrows flanking TRACKING ERROR line.

**Disc Clamper:** Swinging arm that locks to center spindle and clamps disc to turntable. Carries index scale for arm cueing. Matching scale above arm carriage.

**Arm Lock/Power Switch:** Shuts off power when arm is clamped to post. Turntable is switched on by releasing arm lock.

**Tracking Force:** Dial on adjustable weight mounted on arm pivot housing, used to balance arm vertically before setting tracking force.

**Cover:** A top-hinged clear plastic cover, that covers the arm and about half the turntable when lowered.

---

The record clamp is claimed to act as a stabilizer, exerting a damping action on the disc by pressing its center to the turntable platter. Mitsubishi has also used the clamp bar to help solve a problem peculiar to the vertical format—how to cue the pickup to a specific diameter of a record. Under normal circumstances the stylus cannot be seen, calling for some guesswork if cueing is to be done by eye. However, the clamp bar carries a scale, extending across the recorded surface of a record, and calibrated linearly from 0 to 10 with spac-

ings of 0.5 units. By sighting across the top of the scale to the disc, one can estimate the numerical reading corresponding to any band or section of the record.

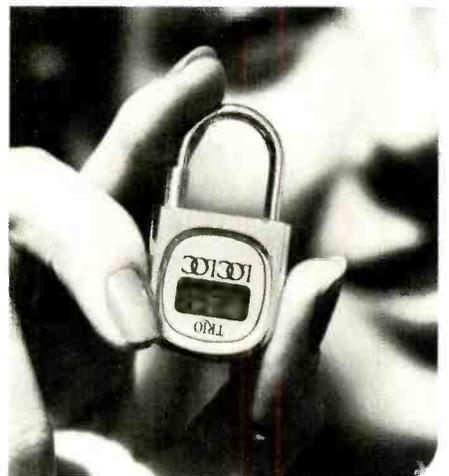
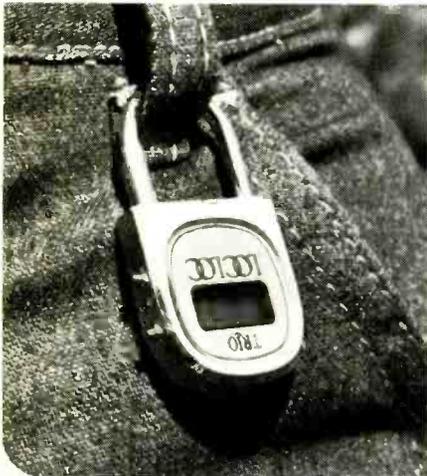
Along the upper part of the record player, next to the rear (top) of the tonearm, is an identical scale. When read against a line marked on the rear of the arm carriage, this scale shows the position of the cartridge along the playing radius. Although the two scales are on opposite sides of the disc, their readings are mirror images and therefore equivalent. By transferring the reading from the clamp scale to the fixed scale, the pickup can be lowered to the record with reasonable assurance of contacting the selected passage.

The operating controls of the Mitsubishi LT-5V are flat pushbuttons that require little pressure and almost no movement. When the unit is activated by unlocking the arm clamp (thereby lighting a "power-on" indicator), a touch on START turns on the motor and causes the arm to scan in to the lead-in groove and descend to playing height. At the end of the record, it lifts, returns to the rest, and shuts off the motor. This can also be done at any time by touching STOP. These buttons serve as slewing controls, since holding in either START or STOP causes the arm to move continuously inward or outward, stopping when the button is released. A touch on LIFT/CUE is required to lower the pickup to the record after it has been slewed.

Small green arrows above the control panel show the operating mode of the record player. The "up" and "down" arrows show the status of the LIFT/CUE control, and "left" and "right" arrows next to them show the slew direction of the arm. A green dot lights when REPEAT is engaged. Finally, the TRACKING ERROR "0" bar lights up when the arm is tangent to the groove (its normal condition). Arrows on either side are lit when the tracking error exceeds the rated maximum of 0.1 degree. This can happen when the pickup enters the eccentric groove at the end of a record, or while the arm is being slewed.

**Laboratory Measurements.** We installed several different cartridges and found the process straightforward and rapid. The measurements were made using an ADC Integra XLM III, which has an integral plug-in "shell".

Because of the unusual orientation of the record player, it was not possible to measure the actual tracking force or the effective arm mass. However, all the cartridges performed properly when the tracking force dial was set properly, and we have no reason to doubt its accuracy.



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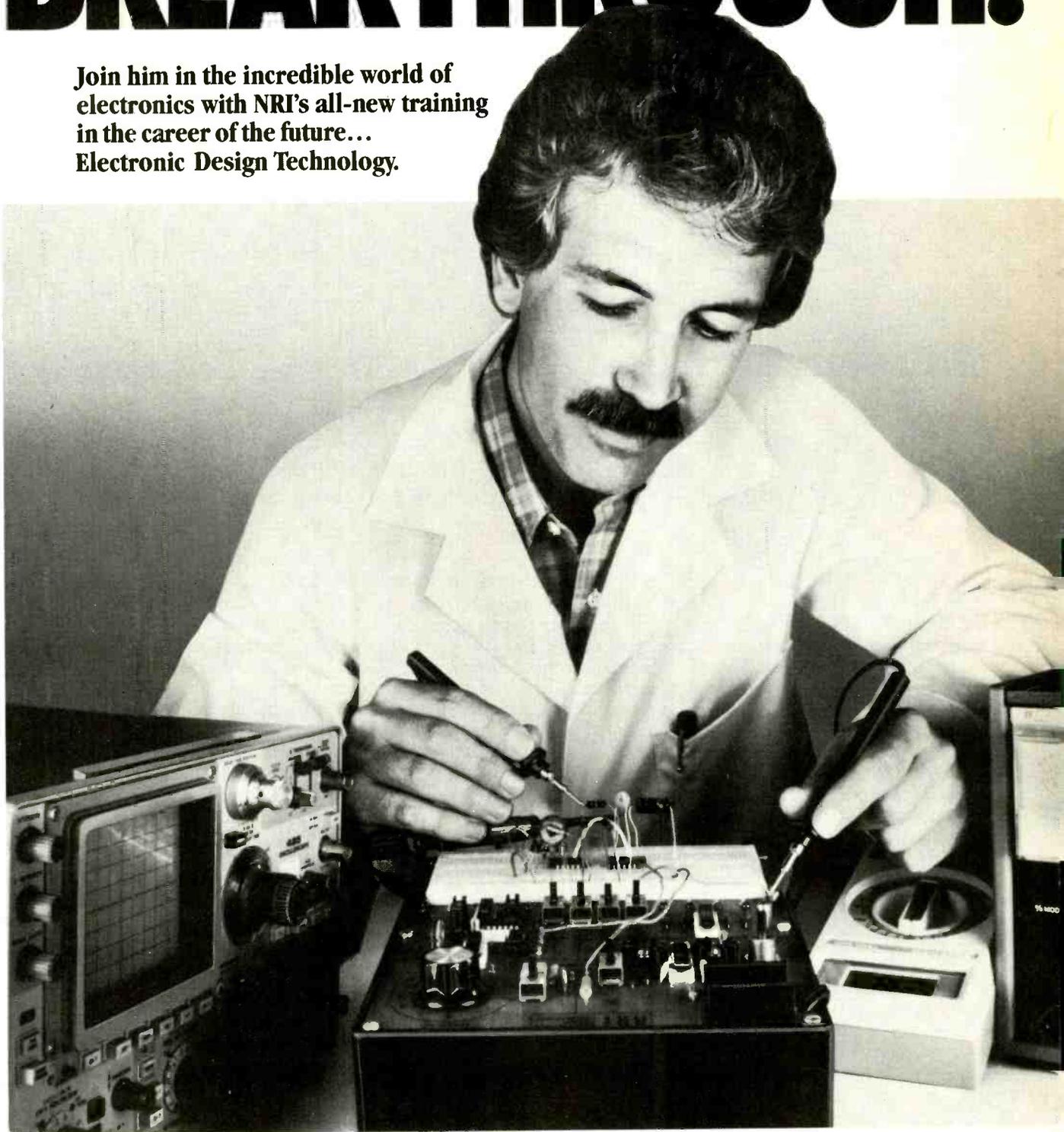
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**MITSUBISHI** (continued)

The capacitance to ground of the arm and signal cable wiring was 215 pF per channel. The low frequency resonance of the arm and cartridge system was 12 Hz, with an amplitude of 7 to 8 dB.

Turntable rumble was very low, measuring -38 dB unweighted and -63 dB with ARLI weighting. The major rumble component was at 10 Hz, with a smaller one at 30 Hz. The flutter was 0.13% weighted peak (DIN) and 0.09% weighted rms (JIS). It mostly occurred at rates of 4 and 10 Hz, and its spectrum dropped off rapidly above 15 Hz.

The speed vernier controls had maximum ranges of +3.9 to -6.5% (33 1/3 rpm) and +5.6 to -6.9% (45 rpm), both well beyond the rated 3% range. However, the strobe markings were very difficult to see—lit by a dim red light and recessed so far into the body of the player that they could be viewed only from a narrow angle directly above the window.

The operating controls worked smoothly and silently. Because of the very small force needed to activate the

buttons, there was never any tendency to jar the record player when using the controls. The START cycle required about 8 seconds, and the STOP cycle some 13 seconds. The arm slewing functions were easy to use, but they were very slow—requiring some 18 seconds to cover the surface of a 12" record. The arm lift and cueing mechanism worked very well—operating in less than 2 seconds with only a slight repeat of the record (2 or 3 seconds) each time the arm was raised and lowered in place.

The protective system that prevents the pickup from descending to an empty but rotating platter worked well. The instructions warn against trying to move the arm manually (which could damage its drive system since the arm is constrained at all times by the stainless steel drive wire). However, if the arm is not moved beyond its region of free movement, no harm will result. With the record player operating, we found manual cueing faster and more convenient than the slow internal slew system. If the pickup is lifted by its finger lift and slowly moved in either direction, the servo causes the arm to follow and there is

no need to approach the mechanical limits of the arm's movement. Lateral pick-up displacement required to light the TRACK ERROR indicators was about ±0.8 degrees. Whether the servo actually responds to a smaller error could not be determined.

Since one of the claims made for the superiority of the vertical turntable mounting is its resistance to vibration, we were especially interested to see how it would fare when placed on vibrators used to measure base isolation. It performed very well, and we consider the manufacturer's claims for this feature to be justified, i.e., a single transmission mode in the 20-to-30-Hz range was measured through the rubber mounting feet. Isolation from vertical vibration was excellent.

**User Comment.** It would be easy to dismiss the novel design of the Mitsubishi LT-5V as a "gimmick" or novelty to catch the attention of a prospective buyer. While there may be some validity in that assumption, it is a fact that the LT-5V does a superb job of record playing, and would be hard to surpass in any of its measured characteristics.

The advantages claimed for it in improved base isolation are quite real. Although we could not measure its isolation at infrasonic frequencies, we found that vigorous pounding on the supporting shelf usually had no effect on the contact of the pickup with the groove, nor on the sound we heard from the speakers. We conclude that this is a very well engineered record playing system.

To us, one of the chief attractions of the LT-5V is that it fits easily on a narrow shelf. The drawback is that it requires at least 17" of vertical clearance. In any case, the consumer now has a choice not previously available.

In using the unit, we found only a few minor inconveniences resulting from its format. Because the spindle has a notched tip that locks the pivoted clamp holding the record in place, you have to wiggle the record a bit to remove it from the turntable. Also, the arm indexing scales, while accurate, are inconvenient to use unless they are at eye level. Otherwise, parallax becomes a problem. Finally, the stroboscope markings are much too difficult to see, and should be lit more brightly if they are to remain in their present location.

The appearance, construction, finish, and smoothness of operation of the Mitsubishi LT-5V are suggestive of a more expensive record player. If its special qualities meet one's needs, it might well be the system of choice for many users.—Julian D. Hirsch

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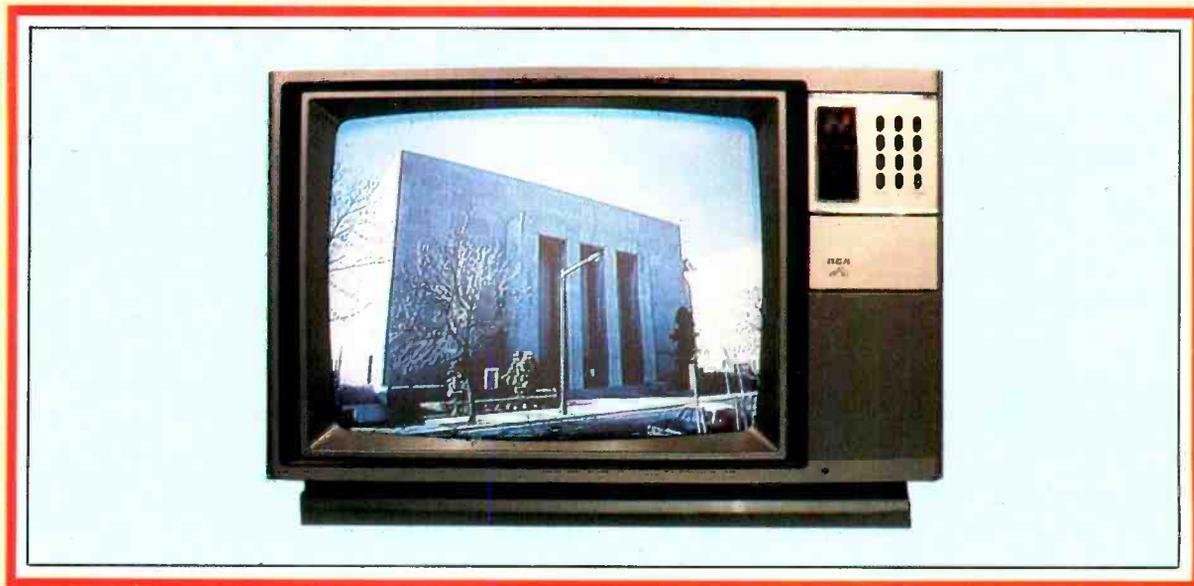
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CIRCLE NO. 38 ON FREE INFORMATION CARD

# Popular Electronics Tests



## RCA CTC-111A 19" Color TV Receiver

**T**HE CTC-111A 19" color chassis for 1981/82 offers 127-channel tuning, CCD comb-filtering, and pseudo-stereo sound. It's quite different from either last year's top-of-line CTC-101 or the more recently developed XL-100 CTC-108.

The table model examined here measures 17"H x 17"D x 26"W. It has a black-striped silver front, and walnut-colored plastic top and sides. Large, red LED channel numbers are located above a push/pull on-off and volume control switch that's situated immediately to the left of a 12-button direct-channel address and a programmable up/down slow and rapid scan arrangement. Directly below and behind a 5" x 2.5" plastic door are controls for channel-select, sharpness, brightness, contrast, color, tint, and treble/bass. Suggested retail price is \$580.

**General Description.** *127-Channel Tuning.* When the on/off switch is pulled, audio comes on immediately and the picture appears in about four seconds, a vast improvement over previous 11-second warmups for older-type cathode ray tubes. Channels are selected by up/down pushbuttons, activating a frequency-synthesis manual-scan tuning system that covers all vhf, uhf, midband, superband, and hyperband CATV chan-

nels. Selected u/v frequencies may either be added or deleted in the receiver's electronic tuner by simply pressing an add or erase button. And when a channel is entered in memory, the select/lock switch fixes it in position. Memory is retained even if the receiver is disconnected from the ac line. On the set's back are switches for cable-TV-mode tuning and "ColorTrak" defeat.

Electronically, the MST007 composite tuner is divided into three sections: uhf, vhf, and superband/hyperband. Shielding separates the uhf section from the other two. Not only does the tuner furnish the usual 82 u/v channels, but also cable channels A-1 through A-5, A through I, J through W, and W+1 through W+17, for a total of 127. The cable/normal switch does not affect vhf channels 2 through 13, but must be set to *normal* position for uhf. In addition, a sync detector has been added from a transistor on the chassis to defeat aft during improper sync cable search.

Channel scan can be expanded automatically in discrete steps from -2 MHz below video carrier to +2.25 MHz above. After full search, the sync detector cuts off and the system returns to normal operation. Then, even carriers without apparent sync may be received. But such special sync operates *only* in the CATV position.

**Other Features.** The CTC-111A's SAW (surface wave) filter input, its transistorized preamplifier, the i-f, aft, agc/video detector IC, and the 28-pin LSI chroma and luminance processor are incorporated into the same monolithic chips used in the CTC-108 (POPULAR ELECTRONICS, June 1980). The charge-coupled-device comb filter was used first in the RCA CTC-101 chassis. (See PE, October 1981 review of Toshiba's CB965 TV set for details on how the comb filter works.) Finally, there is an automatic peaking detector, a vastly improved sound detector and output, and a new horizontal/vertical oscillator and regulator control IC—all new for this series.

**Auto Peaker.** Vertical detail resolution from the comb filter is extracted through a nonlinear processor. This is summed with the delayed luminance in a U701 peaker IC. Further correction is supplied from an automatic peak-frequency detection circuit, the idea being to offer uniform horizontal resolution from one TV station to the next, and to compensate for any poor SWR of the receiving antenna. This occurs without the user having to adjust preferred sharpness for individual channels.

Consequently, a peaking circuit has been designed that consists of three transistors, and related components.

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The heart of the system is the Heath/Zenith 89 Computer, a complete, stand-alone unit with professional keyboard, smart video terminal and 5¼-inch disk drive. It's easy to use for people having little or no experience – yet it can also run extended languages like BASIC, COBOL, FORTRAN and Pascal.

The 89 comes with 48K bytes RAM, expandable to 64K. It has two Z80 microprocessors, one for computer functions, one for terminal functions. And three serial I/O ports for interface with printers and modem.

The video display features a 12-inch diagonal, high-resolution CRT that's easy on the eyes. It displays up to 2,000 characters at a time, 24 lines (plus 25th status line) by 80 characters, with full cursor control. Also 33 block graphic characters for charts and graphs.

The heavy-duty keyboard follows standard typewriter format for easy operator training. All terminal functions are programmable from keyboard or I/O ports.

The 5¼-inch floppy diskette stores 100K bytes of information and interfaces on line with the Heath/Zenith 67 Hard Disk System.

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The 67 Disk System features one hard disk and one 8-inch, soft-sectored floppy for total on-line storage of 10.782 megabytes (formatted). That's a huge data base.

The floppy is double-sided, double-density and can also operate in single-sided or single-density modes, compatible with standard IBM 3740 format.

The 67 features write-protect switches for both drives to prevent accidental erasure of information. The average access time of the hard disk drive is 70 milliseconds.

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The Heath/Zenith 25 Printer is a heavy-duty, high-speed, dot matrix printer that gives you sharp, clear printouts. It prints over 150 characters per second with whisper-quiet smoothness.

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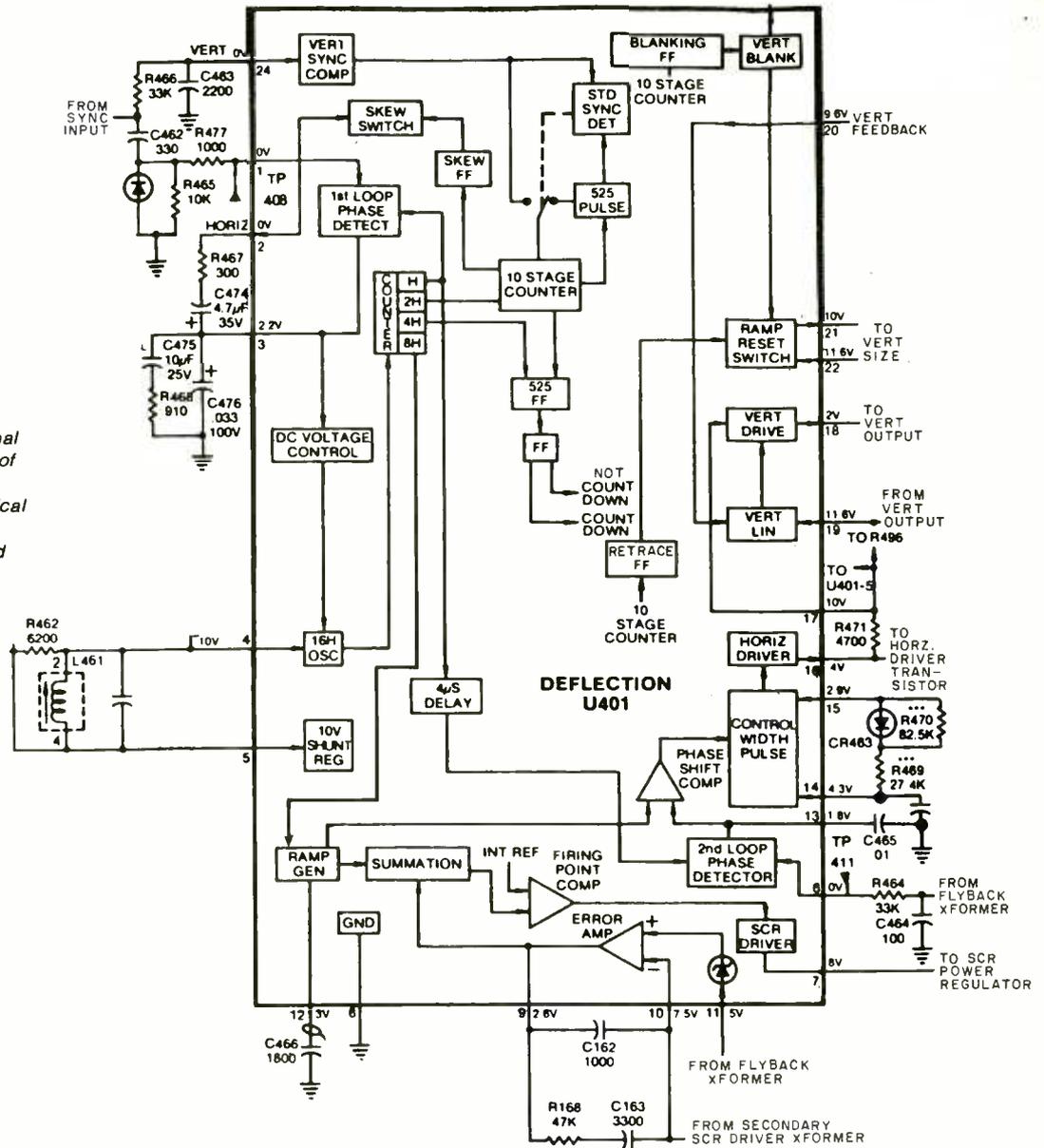


Fig. 1. Functional block diagram of the 3-part horizontal/vertical phase-lock, count-down and regulator IC.



Internal view of the CTC-111A chassis.

A Horizontal/Vertical Oscillator and Regulator Control, a U401 IC, was first used successfully in RCA's 9-inch CTC-115 color chassis that was introduced earlier this year. It's a rather complex 24-pin IC, with three feedback loops. The first two of these are used for sync control and the third operates an SCR voltage regulator.

Circuit operation begins with a  $16 \times 15,734$ -Hz horizontal-frequency LC461 tank circuit that oscillates at 251.744 kHz as sync input enters the monolithic chip in Fig. 1 between R466 and C462. A standard sync detector receives normal sync and compares it with internal frequencies (derived from double the normal horizontal frequency and divided by 525). Seven fields of 262.5 lines each constitute an acceptably good signal, while a defective one overlaps and free-runs temporarily, pending extended 568-line countdown and lockup. When

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**RCA MODEL FFR495W (CTC-111A)  
19" COLOR TV TABLE MODEL  
LABORATORY DATA**

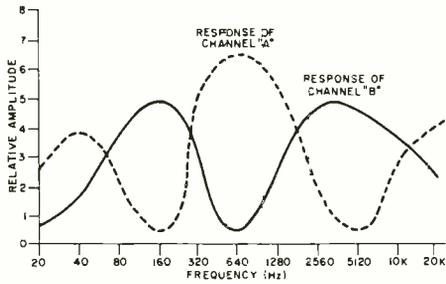


Fig. 2. Frequency response of the synthesis stereo circuit.

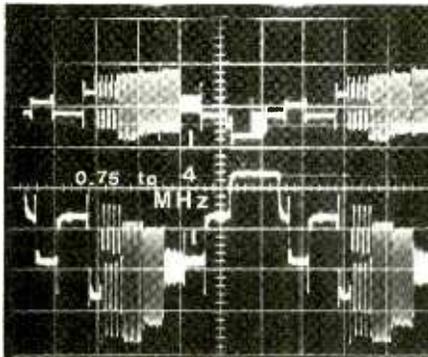


Fig. 3. Multiburst test at the video detector (top) and CRT (bottom).

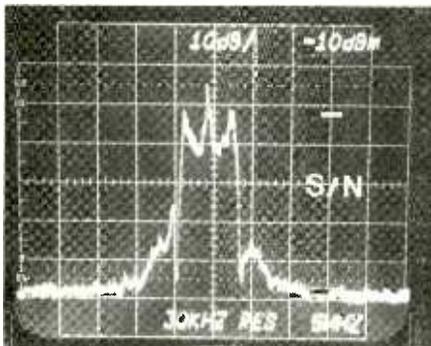


Fig. 4. Spectrum analyzer display of luminance at the CRT.

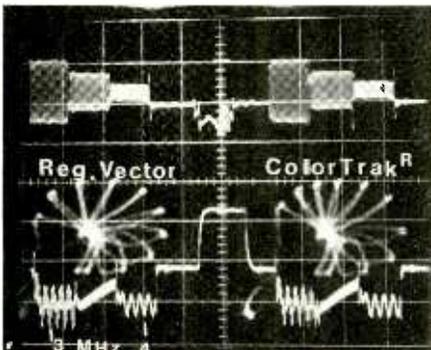


Fig. 5. Swept chroma test at video detector and CRT. Vector test is at center.

Parameter	Measurement
Tuner/receiver sensitivity (min. signal for snow-free picture):	vhf (Ch. 6): -11 dBmV uhf (Ch. 30): -3 dBmV
Voltage regulation w/signal input (line varied from 105 to 130 V):	Low voltage: 125-V supply—99.6% 16-V supply—95%
Luminance bandpass at video detector:	High voltage: 29-V supply—96.8%
Luminance bandpass at CRT:	4 MHz
S/N at CRT:	4 MHz (approx.)
Convergence:	46 dB
Horizontal overscan:	99%
Pincushion/barreling:	8%
Agc response (before sync clipping or white/black level shifts):	None
CRT color temperature measurement (with normal picture on screen):	66 dB
Dc restoration:	6500°K
CB interference (at 60 ft):	88%
Power requirements (signal applied)	Slight on Ch. 2 125 W (max.)

NOTE: Instruments used in these measurements are: Tektronix 7L12 and 7L5 spectrum analyzers; Tektronix/Teletquipment D66, D67 oscilloscopes; Sadelco FS-3D-VU 1/a meter; Winegard DX-300 amplifier; Data Precision 245, 258, 1750 multimeters and 585 frequency counter; B&K-Precision 1248, 1250 color bar generators and 3020 function generator; Sencore VA48 (modified), CG189 color bar and signal generators, PR57 variable power supply; Tektronix C-5A camera.

channel or other changes cause sync interrupt, the vertical sync pulse is first detected between the 513 and 568 count. The vertical ramp will then reset at a 525 count. Following seven consecutive fields of coincidence between the 525 count and detected vertical sync, the countdown is locked up in standard mode. To get out of the countdown mode, there must be two consecutive fields that are *not* apparent in the 525 count. Following the 525-line flip-flop, the count/no-countdown flip-flop makes the decision and, if the result is no, it resets the counter.

Frequency synthesis within the U401 begins with the high-frequency LC461 tank oscillator via the 16H oscillator block and goes from there to the counter and chain of dividers.

*Dual Dimension Sound* was first introduced in RCA's CTC-101 series in 1979, and has seen major improvements for the 1981/1982 line. This pseudo-stereo effect is created by synthesizing audio sound separation into two channels so that one speaker handles midfrequency audio between 60 and 200 Hz and 1.2 and 10 kHz, as shown in Fig. 2. This is done by selective filter separation, with two electrical paths each going to a single-output amplifier.

Both L and R jacks seem to have adequate power to drive stereo hi-fi amplifiers with a quasi-stereo effect.

The limiter/amplifier, FM detector, and dc volume control and tone controls are contained in a TDA2791 16-pin IC package. The power amplifier, a TDA2611A, is capable of 5 watts output (here about 3.5 W) and comes in a 9-pin in-line package, with a dc operating voltage of 24 V, and a THD of 0.3%.

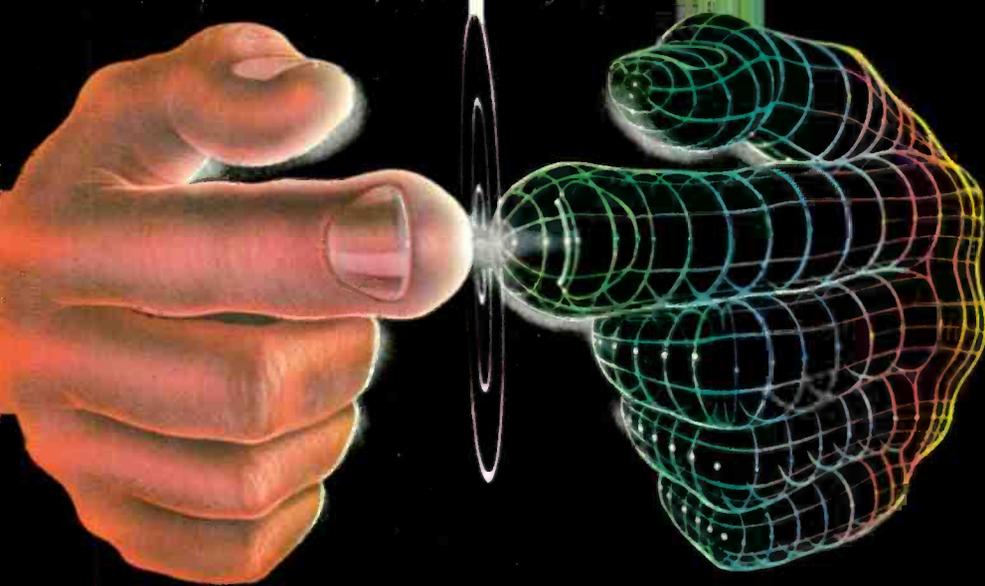
**Laboratory Measurements.** RCA's charge-coupled comb filter supplies about 4 MHz (Fig. 3) to the CRT without crosstalk between chroma and monochrome. A high voltage of 29 kV and a warm white color CRT temperature measured at 6500°K (5800°K is average) produced a well-illuminated picture with fine contrast. Video S/N is 46 dB (Fig. 4); and vhf tuner/system signal pickup is -11 dBmV. An agc swing of 66 dB gives a fast response to aircraft flutter. Convergence is accurate; and overscan is a precise 8%.

The ColorTrak mixing circuit in the 42-pin LSI squeezes only oranges and reds together (losing the usual 30° separation (Fig. 5) between the first four petals of the 10-spoke vector). Blues and green, however, are not disturbed.

**Comments.** This new TV chassis is evidently a fine one, fortified by its 127-channel electronic tuner, pseudo-stereo sound capability, good cable-TV response, better-than-average dc restoration, a CRT that brings out the best in color, and a handful of very satisfactory specifications in other areas.

Nothing is perfect, though. Serviceability on the chassis might be improved, for example, and we would like to have seen a "cold" chassis for safety's sake when servicing. Too, front-end filtering against CB intrusion on Ch. 2 could be better, and audio is not completely muted when the volume switch is turned down. But these aside, the RCA CTC-111A receiver is a fine chassis, featuring a sharp, bright picture, as well as better-than-average sound for a table-top model.—*Stan Prentiss*

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# Popular Electronics Tests

## The IBM Personal Computer

**T**HE long-awaited entry of IBM into the microcomputer field—rumored since 1976—has occurred. Happily, we were able to spend considerable “hands-on” time with it and can share the results with our readers without delay. This 16-bit microcomputer (based on an Intel 8088) is designed for business, school, and home use. It will be available from ComputerLand dealers and business-equipment outlets of Sears, Roebuck Co., as well as through IBM’s sales centers and a sales unit in its Data Processing Division.

The basic expandable system consists of a detached keyboard (cable-connected to the System Package) and the System Package that contains 16K of RAM, 40K of ROM (carrying the operating system and BASIC), a TV receiv-

er adapter, and a cassette port. The cassette recorder is not supplied. This basic package has a suggested retail price of \$1565.

The full system includes 64K of RAM, a 12” monochrome video monitor (mounted within the System Package), and an Epson MX-80 printer (carrying the IBM logo). The complete system has a suggested retail price of \$4500 (the single-disk version is \$3005). Options include a full-color monitor and adapter, joysticks, and a light pen. The independent enclosures are pearl-white to match any environment.

**General Description.** The microprocessor-controlled keyboard has 83 keys including 10 for numeric entry and cur-

sor-control and 10 for special functions (user definable). Each key features autorepeat. The sloped keyboard has both tactile and “click” touch feedback, concave tops, and 1/4” key travel. Furthermore, the keyboard can be positioned flat on the operating surface or via a pair of detent supports, at a shallow but comfortable angle. Some keytop legends are unusual. For example, there is no key marked RETURN but there is a large-size key marked with the international “return” symbol (a left-facing arrow). A four-character keyboard buffer is provided, and many keys have four selectable functions—normal, shifted, control, and alternate.

Of the 256 characters available, 16 are for game and special-character graphics that include the four playing-



card suits, music symbols, etc.; 16 are used as a word-processing editing set; 96 are conventional ASCII upper/lower case, numerics, and symbols; 32 form an international character set for foreign languages; 16 form an extended Spanish and special character set; 48 are line graphics; and the remaining 32 are devoted to a mix of Greek and scientific symbols.

Physically, the keyboard is 20" W × 2" H × 8" D, and it weighs about 6 pounds. It is cable-connected to the System Package via a six-foot coiled telephone-like cable. A pencil "ledge" is provided above the keyboard, and four synthetic cork, nonskid bumpers are located near each bottom corner.

The Systems Package contains the 8088 and its associated logic on a "motherboard" having five bus-expansion slots. The system switching power supply, a fan, the cassette I/O port, a 2 1/4" speaker, and up to two 5 1/4" disk drives are located in this enclosure.

The disk drives use double-density, single-sided diskettes in a Tandon 5 1/4" drive. There are 512 bytes/sector, 8 sectors/track, 40 tracks/diskette, with 160K-byte capability and 320K bytes maximum. A LED "in-use" indicator signals disk activity. Power requirement is 63.5 watts.

The 8088 CPU combines a 16-bit internal architecture with an 8-bit bus interface. There are 16-bit wide registers, data paths, ALU, and a set of powerful 16-bit instructions identical with the 8086 CPU. The 20-bit memory address range and the 16-bit I/O port address range allows for 1-megabyte of memory and 64K I/O ports. In this

computer, the 8088 operates with a 4.77-MHz clock. The RAM memory uses a ninth bit for parity checking, and can be expanded to 256K bytes. The 40K ROM contains the IBM operating system and BASIC.

On power-up, 2K of ROM provides a series of 14 diagnostic tests including the processor, an 8K ROM checksum, DMA, 16K RAM, ROM checksum, interrupt controller, timers, video RAM, cursor, video sync, remaining RAM, keyboard, cassette port, and disk system. Any problems are reported via the speaker and video display. The test takes about 15 seconds.

Physically, the System Package is 20" W × 16" D × 5.5" H and weighs 21 pounds without the disk drives, 25 pounds with one drive, and 28 pounds with two drives installed.

The monochrome monitor uses an 11 1/2"-wide screen having a P39 (green) phosphor. An anti-glare screen surface and contrast and brightness controls are supplied. The display is 25 lines by 80 columns using a 7 × 9 character within a 9 × 14 character box. Underline, blinking, high-intensity, reverse video, and nondisplay for security are provided. Characters can be displayed in three background shades. Graphics resolution is 720 by 350 pixels, and a 4K screen buffer is supplied.

The CRT sweep speeds are unusual in that the horizontal sweep speed is 18 kHz, while the vertical sweep is 50 Hz. The bandwidth of 18 MHz makes for an excellent, crisp display. An optional add-on card is provided for using conventional NTSC monitors.

When using BASIC, each of the 10

user-definable keys can be made to display its function along the 25th line. This feature can be defeated.

Three types of software are provided. Cassette BASIC is provided in 32K ROM and uses the cassette for data storage with no disk facility. Disk BASIC comes on the DOS diskette and requires 32K of RAM. It provides access to both cassette and disk, has an RTC for date and time, and supports RS-232 as well as two additional drives. Advanced BASIC also comes on the DOS diskette and requires 48K of RAM. It features automatic event trapping and the use of function keys, joysticks, light pen, etc. It provides advanced graphics and supports graphic commands CIRCLE, GET, PUT, PAINT, and DRAW. It also provides music support via the PLAY statement.

Other software includes VisiCalc, EasyWriter word-processing package, Accounts Receivable/Payable and General Ledger from Peachtree Software, the Microsoft Adventure game package, a Communications package that uses the Asynchronous Communications Adapter, plus a PASCAL compiler. Both CP/M-86 and UCSD p-System are being adapted.

IBM will also solicit software from outside sources. To do this, IBM has created a new department called Personal Computer Software Publishing Department (Dept. 765, Armonk, NY 10504). IBM employees and external authors, from professional programmers to hobbyists, can submit programs for consideration.

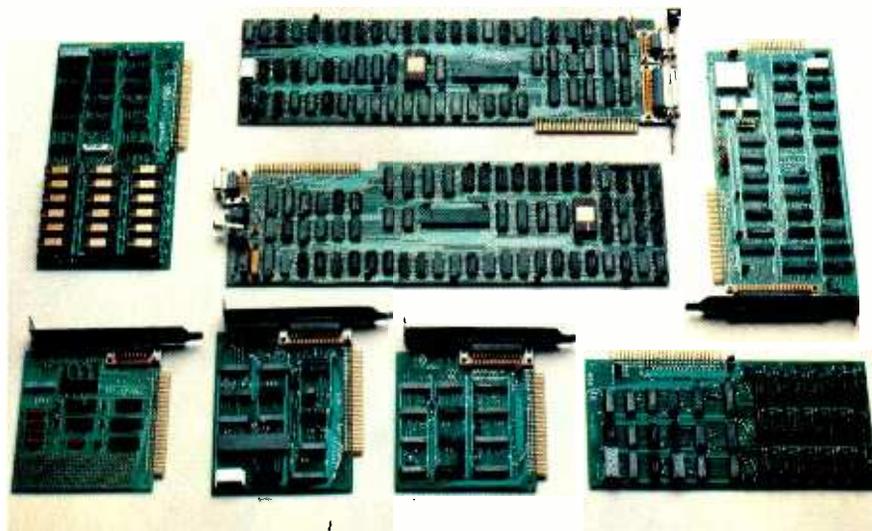
The IBM 80 CPS Matrix Printer is actually the popular Epson MX-80 featuring bidirectional printing, a one-line buffer, horizontal and vertical tabs, a choice of 40/66/80/132 characters per line, 12 character styles, and variable vertical spacing. The character set includes 96 standard ASCII, 64 graphic symbols, and 9 special symbols all using a 9 by 9 matrix. A "ripple print" test is provided.

The Asynchronous Communications Adapter supports RS-232C and is fully programmable with 5, 6, 7, or 8 bit characters, even/odd/no parity, and 1 1/2/2 stop bits. Baud rate is from 50 to 9600.

The 450-page loose-leaf manual is excellent and is exceptional in its clarity of explanation. It is very detailed and profusely illustrated.

**Evaluation.** Visually, the IBM Personal Computer is attractive. The three basic units (Keyboard, Systems Package, and the video monitor) complement each other with their pearl-white, smooth, round-cornered enclosures. The keyboard attaches via a coiled cable and it can be angled to the workspace in either of two positions—both very comfortable, and based on research developed in Sweden (of course, if you're a Mediterranean type . . .).

In the basic system, on power up,



*IBM Personal Computer plug-in modules. Lower row, left to right are: game adapter, asynchronous communications card, printer adapter card, and 32K memory board. Upper left is the 64K memory board and upper right is the disk drive adapter. Top center is the monochrome display/prINTER adapter, and lower center, the color graphics board.*

BASIC is automatically booted. In the disk system, BASIC resides on the same diskette as the DOS and has to be called as usual. The IBM DOS "looks" a lot like CP/M, but it isn't.

We used the system with both monochrome and a Hitachi color monitor. The video display in both cases was clean and crisp and easily usable to all four edges. Owing to the unusual horizontal/vertical sweep speeds, a conventional video monitor cannot be used unless an adapter is plugged in. In the chroma mode, the colors are exceptionally distinct and the alphanumerics and graphics are crisp. The disk drives are unusually quiet, with each drive having a LED to signal disk activity.

The Microsoft BASIC is very fast, with a lot of the speed due to the 4.77-MHz clock. In the graphics mode, BASIC statements allow the creation of graphic figures that can be stored as an array, and "called," in a manner similar to machine-language routines. The speed of animation is about as fast as machine-language routines. We were impressed by the smooth animated transitions of a "car" while operating a slot-car-racing game on screen. It puts comparable micros to shame.

The BASIC contains some 147 statements, functions, and variables. Among the novel instructions are CIRCLE, where

the user can define the parameters of the circle which can then be colored by a PAINT instruction. The circle can be opened up and generally manipulated. Since both alphanumerics and graphics can exist simultaneously, circle segments can be labelled. This is great for business applications when you want to see how the "pie" is divided. Several other graphic commands allow the creation of a limitless number of designs. The computer can also create music up to 32 tones in one voice (with graphics if desired). The SOUND command allows determining the tone frequency and duration. There are also commands for the joystick (STICK), and light pen (PEN). A light-pen adapter is available for the latter mode.

We ran the IBM demo programs, which are very impressive, but didn't have the time to write any lengthy programs of our own. Simple benchmark tests showed this machine to run as fast as (or faster than) the speediest micro hardware/software combinations we have tested to date.

We did write several short programs that used color, and they were great.

The ten "function keys" on the left side of the keyboard may initially add some confusion for the operator, as might some keys with international symbols. However, one adapts very quickly

to this, especially since codes at the bottom of the screen enable one to really reduce typing.

We found the green-phosphor screen easy on the eyes, fortified by highlighting functions such as underlining, blinking reverse images, and high intensity.

The only negative comment we can make about this system is the fact that, at the present, the user is locked into software that works with the IBM DOS. We were told that CP/M-86 is just down the pike and may be available by the time this review is in print. [Editor's Note: As we went to press, more information became available. See "Computer Sources" in this issue.] With this CP/M, the five expansion slots, and the services of some outside software authors, this new computer will be a force to be reckoned with. Furthermore, there is no doubt that a "cottage industry" will spring up about the IBM bus just as occurred with some other small computers. Also, it would be nice to have an 8" disk option, as 330K dual-disk capacity doesn't seem to be quite enough if one expands memory to its full 256K.

We found the IBM Personal Computer to be an excellent example of what small computers are about. This is a lot of machine for the money.

—Les Solomon

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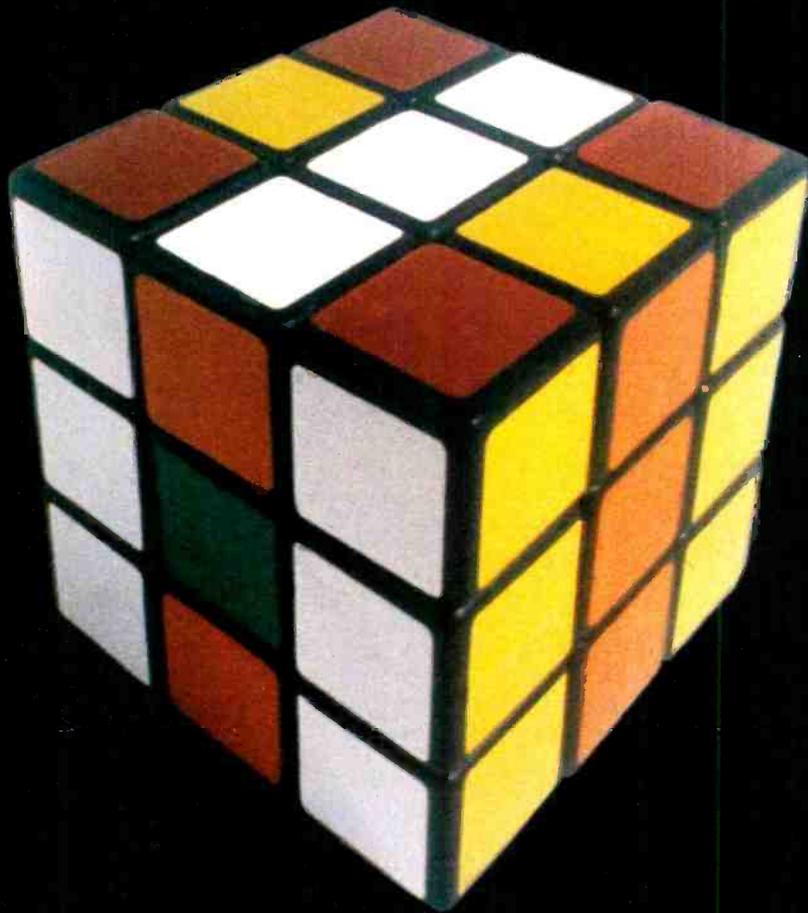
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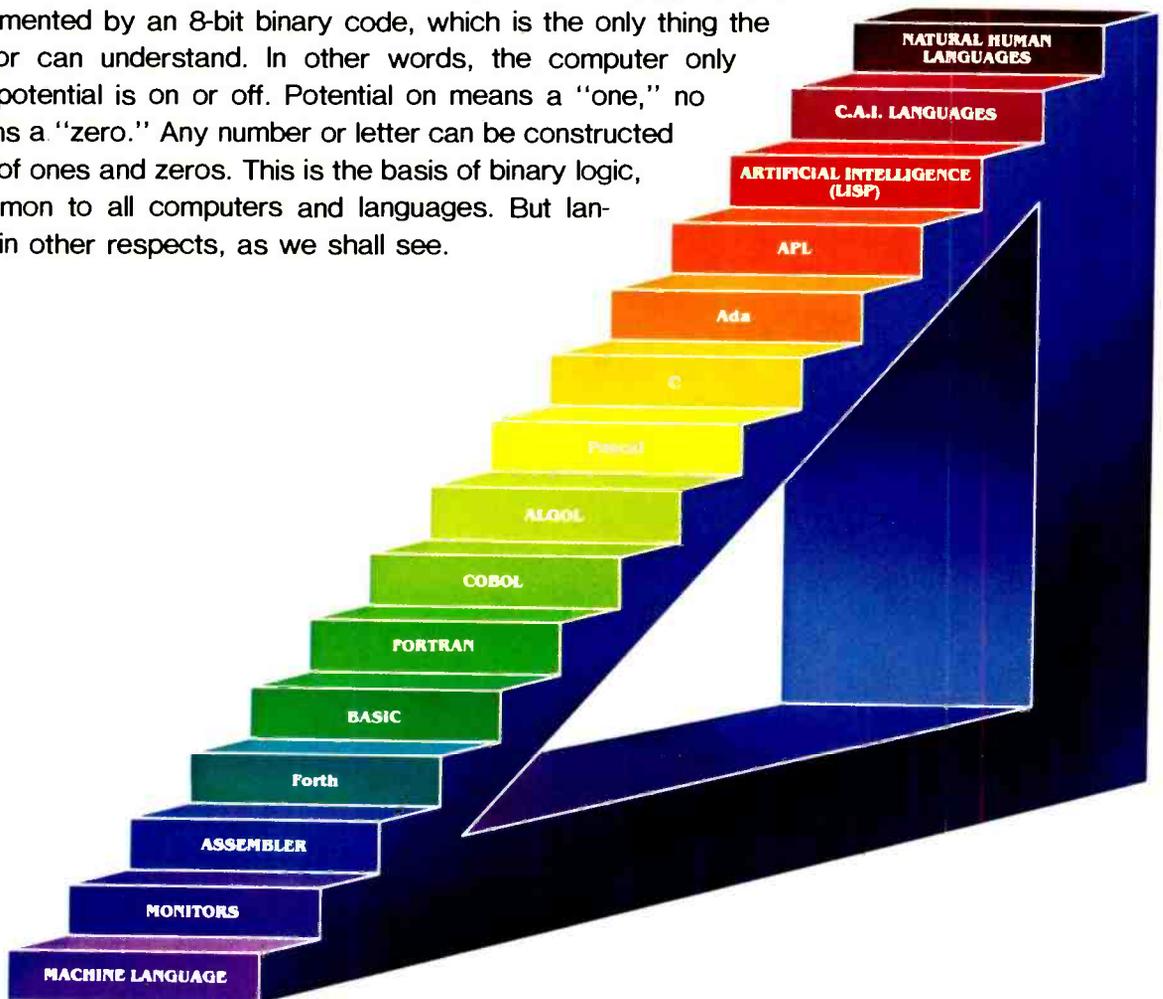
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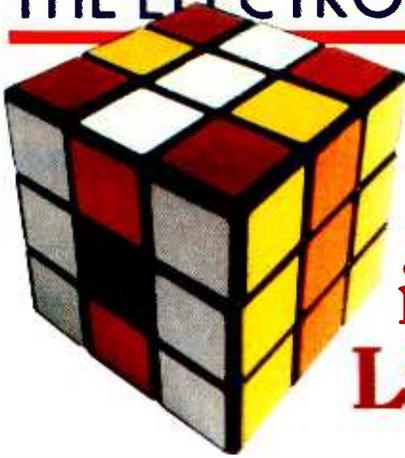
**I**T WOULD BE nice if people and computers shared a common language. However, since human languages are so complex, it has been necessary to invent computer languages. Most people who buy personal or small-business computers have little choice of computer languages. Someone has already made the decision for them so that, when they turn on the machine, an announcement such as "RADIO SHACK LEVEL II" or "APPLESOFT" is seen on the monitor.

In this article, we will discuss computer languages as thoroughly as possible to provide an understanding of what is available and to enable the computer user to choose the language most appropriate for his needs. The diagram below shows the various levels of computer languages from machine language at the bottom to some types at the top that actually resemble human language. We will examine each step and weigh its cost in computer resources and flexibility.

At the heart of your small computer is the microprocessor chip. It *is* the computer. Everything else, except the memory, supports the chip. It may be an 8080, Z80, 8085, 6502, 6800, etc. What these chips have in common is more important than their differences. They are all 8-bit chips with an 8-bit bi-directional data bus and 16-bit address bus. They all use instructions of one, two, or three bytes that perform similar operations. All of the microprocessors consist of an arithmetical logic unit, control circuits, and various registers. The 8-bit bus means that the word size is 8 bits (one byte) and the data bus is used to send 8 bits of data to or from external memory or input/output devices. The 16-bit address bus means that the microprocessor can directly address 65,536 ( $2^{16}$ ) unique memory locations.

All of the computer instructions are unique to a particular microprocessor and are implemented by an 8-bit binary code, which is the only thing the microprocessor can understand. In other words, the computer only knows if the potential is on or off. Potential on means a "one," no potential means a "zero." Any number or letter can be constructed from a series of ones and zeros. This is the basis of binary logic, and it is common to all computers and languages. But languages differ in other respects, as we shall see.





## Direct Programming in Machine Language

**T**HE METHOD of directly programming the microcomputer in binary form (or its equivalent) is called machine language. Everything else must be translated into machine language before it can be used by the microprocessor.

Programming in machine language consists of supplying the microprocessor with machine instructions, memory locations, and data in certain forms and sequences. The microprocessor cannot distinguish between instructions and data except through the form of the program. The instructions are unique to each microprocessor and are built into the chip, with the "power" of the microprocessor defined by the number and complexity of the instructions it can perform.

Early minicomputers and microcomputers used a set of front-panel switches to represent the binary digits. If a switch was set in one direction it was a "one." In the other direction, it was a "zero." When the row of switches was set to represent the desired binary number

for the starting memory location, another switch was operated to cause the computer to go to the specified memory location.

The data switches were then set to represent the first instruction. To enter the instruction into the computer, a switch marked ENTER had to be operated. After each ENTER, the system automatically stepped to the next memory location. The data switches were set to the next binary number and so forth, until the program was entered one binary number at a time. There was usually a set of LEDs associated with the switches to indicate that the program had been entered correctly. When the correct binary number was stored in each successive memory location, another switch was pressed and the computer ran the program. It was a good thing there was not much memory available at the time since the process of manual entry forced the programmer to keep the programs very short. Even today, however, simple single-board computers can be programmed this way.



## The Monitor Operating Program

**M**ODERN computers permit direct access to the microprocessor through an operating program called a Monitor. The program is usually stored in Read Only Memory (ROM) and starts to run as soon as the computer power is turned on. Since it is more difficult to work with strings of binary numbers, it has become common practice to shorten the machine codes by using octal notation (base 8) or hexadecimal notation (base 16). Figure 1 shows a short program in binary, octal, and hexadecimal notations.

The Monitor program, written in machine language and burned into the ROM has commands that allow the user to do many things. In minimal computers using cassette-tape data storage, the monitor may act as the

operating system. Some of the programming functions in the Monitor permit a user to examine the contents of any memory location, step through the memory showing the contents of one location at a time, and load the memory location with instructions or data in the form of two hex digits (or three octal digits). Each of the hex digits represents four bits (one nybble) of the eight-bit code (one byte) contained in the memory location. The user can also go to any memory location, display its contents, and change it if desired. The Monitor program can also move blocks of code from one memory location to another. Moreover, it can usually operate the cassette machine to store programs.

It is thus possible to enter a machine-language program from the keyboard. To do this, starting at the memory location desired as the program origin, a user enters the hex equivalent of the binary instructions and data comprising the program. As each two hex numbers are entered, the Monitor steps to the next memory location in sequence. If there are any "jump" instructions, the programmer must figure them out and tell the Monitor what memory location to go to and return. After entering the program and checking it, by going to the starting location and again stepping through the program while observing the indicator lights or CRT screen, it can be run by returning to the starting memory location and giving the GO command. If the program runs correctly, it may be saved on a cassette tape so that the next time it may be loaded from tape instead of from the keyboard. This procedure is an improvement over the front panel switches, but not a big one.

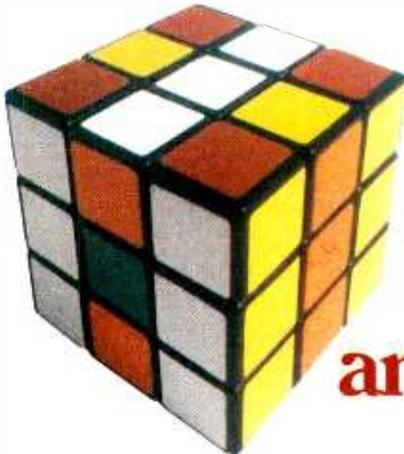


Fig. 1. A short program in three numbering systems: binary, octal and hexadecimal.

Some computers do not come with Monitor programs so it may be necessary to purchase them either as a tape or in the form of a plug-in ROM.

In cases where only BASIC is provided (the TRS-80 MOD 1, for example), you might want to enter a machine-language program to do something that you can't do in BASIC. The PEEK statement is provided to examine the contents of any memory location and the POKE statement is provided to change its contents, should you wish to. To enter the machine language program you have to POKE each memory location with the correct instruction, or data byte. When the program is loaded, it can be run by using the RUN statement in BASIC; or your version of BASIC may permit a CALL to a machine language program which may be in a separate library of machine language routines.

It should be noted that in Microsoft BASIC (including Radio Shack and Applesoft), the contents of memory for PEEK and POKE statements are expressed in decimal rather than in hex or octal. It is therefore necessary to convert the code in the machine-language program into decimal format before using PEEK and POKE. It is also necessary to be familiar with the memory map of the computer you are using so that POKES are not made into reserved memory areas.



## Assembly Language and Assemblers

**T**HE DEVELOPMENT of a machine-language program requires the programmer to write the source listing so that he can figure out the memory locations, how many of them are required for each instruction, and how many memory locations are required for the data. To do

this using binary numbers makes the program almost impossible to read, so symbols were invented to stand for the instructions. These symbols are called mnemonics (memory aids), and each microprocessor has a unique set of them. Usually several manufacturers will

Fig. 2. Program source for Fig. 1 written on an editor.



Fig. 3. Same program as in Fig. 1 in assembly language.



make the same chip, but all of them use the mnemonics of the originator. These instruction sets are published by the semiconductor manufacturers and are made available to users.

Four identical programs are shown in Figs. 2 through 5. They represent the program source as written on an editor, as well as the assembled, BASIC, and Pascal versions.

The process of using mnemonics and a particular syntax to write a program is called Assembly Language. The form of an Assembly Language line for one instruction is shown below

```
LABEL OPERAND ARGUMENTS , COMMENTS
SEARCH:
    LXI      B,0          ,INITIALIZE
                        COUNT TO
                        ZERO
```

In the example, taken from Fig. 1, the Operand is from the Intel 8080 instruction set, where LXI means Load Immediate. The arguments B,0 indicate that the register pair BC are to be loaded with zero. Sometimes, when the value of an argument is not known, a symbolic expression is used to represent it. The label field is optional and represents the location of the memory address of the instruction (which may be unknown), and which will change from application to application. In addition, each Assembler will have a set of pseudo-ops which do not produce machine code. The pseudo-op replaces the instruction mnemonic in the operand field. For example, the pseudo-op ORG (origin) tells the assembler that the program starts at a given location, e.g., ORG 100H (where H stands for hexadecimal).

The pseudo-op END ends the program, while EQU equates the symbolic name in the program to the argument given (i.e., EQU KBD 1 equates the name KBD to the value 1). At assembly time, the name KBD is placed in a Symbol Table and the value 1 is associated with it.

The program that translates the assembly language source code into the machine language object code is called the Assembler. Sometimes a simple Assembler is provided as part of a Monitor program, but most often the Assembler is a stand-alone program sold for your computer, or provided with the Operating System. CP/M and UCSD Pascal Operating Systems include Assemblers, and they also take care of the input/output connection and disk storage management required by the assembled programs.

A typical Assembler is a two-pass program. The first pass figures the length of the instruction and updates the

location pointer in the microprocessor. It will also construct a Symbol Table, putting each symbolic name in alphabetic order. By the end of the first pass, all the symbols should have been given a value; if not, it is called an unresolved reference, and an error message will be printed.

On the second pass, the Assembler again reads the source statements and translates them into object code in machine language, filling in the memory reference addresses with values from the operand field of the source, or with symbolic values from the Symbol Table. If there is an error, an error message is printed. If not, the object code version is printed (See Fig. 3) and the result is saved on the disk.

The object code presents the machine code in a form that can be read by a Loader program and run on the computer.

Some Assembler programs have the capability of creating and using a collection of routines called macros, and are therefore called Macro Assemblers. These are defined as one or more valid statements that may be called up by using a single symbol within the assembly-language program.

The macro has to have been previously defined by the user, within the body of the program. The macro call is the statement that names the macro as the statement operator and gives it the necessary arguments. Such a call within a program causes a macro to be included at the point of the call. It will cause one or more machine instructions to be assembled and the binary code to be generated.

Not every Assembler program is a Macro Assembler and they are not usually included within an operating system. For example, in CP/M the Assembler is not a Macro Assembler (MAC is the Macro Assembler sold by Digital Research, the authors of CP/M).

If you want to use one, you have to buy it as an option. However, once a programmer becomes an accomplished assembly-language user, the extra expense of a Macro Assembler is worthwhile.

A Macro Assembler allows the use of a subroutine (macro) many times in a Linker program by simply calling it by an assigned name. However, most programmers have collections of subroutines that they would like to use over again within many programs.

This program permits the user to have a library of macros stored on a disk. The Linker ties them into a program at assembly time. Of course, the Linker you use must be compatible with the Assembler program in your machine.

One thing that keeps programmers from using assem-

Fig. 4. The sample program is written here in BASIC.



Fig. 5. Same program as before is shorter in Pascal.



bly language to a greater extent is that finding and correcting errors—debugging—is like trying to map a can of worms. Don't worry. There are Debugging programs that make life easy. Using these, you can trace the execution of a program, and see the instructions executed. A program can be traced one step, or more, at a time, and can be modified to correct errors. Breakpoints

can be set to stop on an error, and the contents of registers and memory can be displayed. The CP/M debugger is called DDT (Dynamic Debugging Technique), and it's a good one, but it is not the only one in use. There are others with equal capacity and features. Some are sold separately and others are part of operating-system packages. Check the package before buying.



## Disk-Operating Systems (DOS)

**D**ISK-OPERATING systems are a topic that must be included in any discussion of computer languages because they are really the control language that makes everything else work.

Before the use of floppy disks for microcomputers, the Monitor program could handle the simple I/O and language support, but with the use of disks everything changed.

The operating system controls the allocation of the system resources. It operates the disk system, keeping track of the storage and retrieval of programs and data. It also creates, opens and closes files. The DOS contains all of the system utilities used to format diskettes, to copy files and entire disks, and to make a back-up copy of the disk system. In addition it provides the input/output connection for all of the languages running on the computer.

A DOS is indeed a complete language, full of commands that must be used in correct syntax to direct the operation of the system. Many of the commands require a complete set of extensions and modifiers. Since the DOS is not one program, but a software system, learning it requires study and experience before a user or programmer can become skilled in its use. CP/M, for example, comes with six manuals to explain its operation. The same can be said of UNIX and almost any complete disk-operating system.

When you buy a computer with floppy disks, it must include a disk-operating system. To get the most out of the operating system you must make a commitment to it that will cost a lot of money, and take a lot of time. The choice of an operating system, more than anything else, may determine the success or failure of your computing program.

The language and the application programs that you choose all depend upon the operating system that runs on the computer. Since the disk-operating system also controls the input/output methods, it indirectly determines the type of peripherals you can use.

Often we read about some great new language we would like to use, or an application program that is just what we need. Upon reading a little more, we find that it runs on a different operating system, uses a different disk format, or needs more memory than the system allows. Sorry, but you are just not going to run that soft-

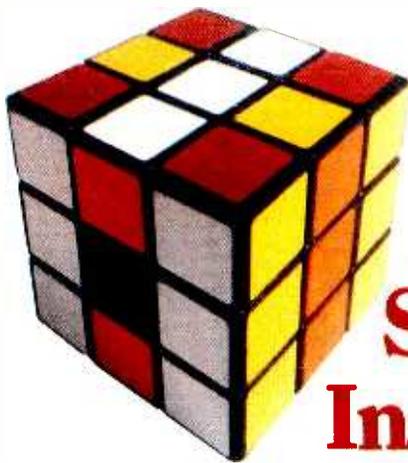
ware. With some computers, it is possible to change your operating system. Of course, you may lose your investment in languages and application software, but it could still be worth it. If you have an 8080, Z80, 8085, or 8088 system running in an S-100 bus, you have several choices. You can use CP/M, or one of its offspring such as CDOS, SDOS, IMDOS, OS, MPP/M, and TP/M. The same chip family can also use OASIS, MV FAMOS, UCSD-P System, CIS COBOL, and many others.

The TRS-80 Mod I, II, and III have TRSDOS in several versions, to say nothing of NEWDOS, VDOS and LDOS. And they can also use CP/M. Zenith/Heath has its own system called HDOS, and you can also choose CP/M or UCSD P-system. If you use a 6800 or 6809 on the SS-50 bus there is FLEX, UNIFLEX, OS-9, UCSD P-system, and a few others. The 6502-based APPLE II offers a choice of APPLE DOS and the UCSD P-system, but you can also plug in a Z80 and run CP/M. APPLE III uses a new system called SOS. The OSI computers have several versions of OS-65 and the UCSD P-system. Challenger III models, which have several microprocessors, can also run CP/M on the Z80. Commodore PET/CBM machines can use only the version of PET DOS they were made to use, and nothing else.

If you begin to get the idea that CP/M is almost a de facto standard for microcomputers, you are correct. That is why IBM and Xerox made sure their new micros could use it. CP/M offers the widest choice of languages and application software; and it will remain that way for some time because software authors write for the market where there are the most customers. The UCSD Pascal system (now called the UCSD P-system) is available as an alternate operating system on most computer systems—underscoring Pascal's rising popularity.

With the advent of 16-bit chips and multi-user systems, new DOS's are being introduced. We do not know what will be the "CP/M" of the future, but there are already indications that today's CP/M software will be able to run under the new improvements.

The UNIX operating system from Bell Labs has many advantages—among them, excellent language and application support. (Cromemco's CROMIX is based on it.) It is predicted that UNIX will become the major operating system for minicomputers and microcomputers. However it runs only on 16-bit machines with memory in excess of 64K, and is intended primarily for multi-user systems. There are some "UNIX lookalikes" that may run on 8-bit machines, but they have not become widely used. In the future, UNIX-based systems may replace CP/M, although MP/M II and CPM-86 are also contenders for future dominance, and they would not make all the present software obsolete.



## **BASIC (Beginners All-Purpose Symbolic Instruction Code)**

**B**ASIC is the most popular of all computer languages. It is also the most versatile. Not really one language, BASIC is a tribe of languages having a common root. They range from Tiny BASIC, which fits into a single-chip computer, to a multi-dimensional disk BASIC for a large main-frame computer.

BASIC was invented in 1963 at Dartmouth College by Professors Kemeny and Kurtz to enable noncomputer science students to use the school computer, which was one of the first interactive time-sharing systems.

BASIC was based on an earlier language, FORTRAN (discussed next), and it can do many of the same things. It has been so effective that it was extended by Digital Equipment Corp. (among others) and soon became a standard language for minicomputers.

The first microcomputers had only enough memory for small machine-language programs. With the introduction of the Altair microcomputer and the S-100 bus by MITS, more memory was added and Altair BASIC from Microsoft became the micro standard. Today, most of the microcomputers on the market use some form of Microsoft BASIC. It has been configured for 8080, Z80, 6502, and 6800, as well as for some of the 16-bit chips of tomorrow. Versions exist in ROM, and there are some on cassettes, floppy disks, and hard disks. Other notable versions are DEC BASIC-Plus, Commercial BASIC (Basic Four), E-BASIC and CBASIC-2 (both intermediate code types). There are also North Star BASIC, Benton Harbor BASIC, Alpha BASIC, TSC BASIC, TI BASIC, and many more.

There are both interpretive and compiled versions of BASIC and some that use a combination of both methods. An interpretive language translates the program source from the high-level language syntax into machine-readable code, on a line-by-line basis. When the RUN command is given, the interpreter translates the first line into machine code and the computer executes it. Then the second line is translated and executed, then the rest of the program, one line at a time. If an error is found, the process stops and an error message is printed. If the BREAK key (or "control C" is pressed) the program stops and the line number where the halt occurred is reported.

The interpretive process must be repeated each time the program is run. However, if changes or corrections are needed, only a few words or lines need be modified. The interpreter is quite large and it must always be present in the memory.

A compiled language has several parts associated with the writing and running of a program. First, there is the original document consisting of statements written in

the language format. This is called the "source" and it is composed using an editor program. After it is checked and stored on a disk, the source is compiled (or translated) into a machine-code version called the "object" code. The object code is also stored on the disk. When the program is to be run, the object code is loaded from the disk into the computer memory. Then the RUN command is given. This is much faster than the interpreted method because only the object code has to be loaded into the computer. However, if any change is needed, it is made on the source code and the program must be recompiled. The old object code is destroyed.

There is a third method of translation called intermediate that is a combination of compiler and interpreter. Both E-BASIC and CBASIC2 (among the most popular BASIC dialects) use this method. In intermediate code languages, the source is written and compiled as if it were a compiled language. However, the compiler produces an intermediate-code version rather than a machine-language version. Both the source and the intermediate code are saved on the disk. When the program is run, another program is also loaded at the same time as the intermediate code. This is called a Run-time Package and it is an interpreter that translates the intermediate code into machine code on a line-by-line basis and executes it. You may ask "why go to all that trouble?" The answer is that it makes it very easy to transport the language from one computer to another.

When we examine the reasons for the universal popularity of BASIC we find that it is mainly because BASIC is so friendly. Other computer languages are complicated. They use unfamiliar words, symbols and syntax, but BASIC speaks English. It is a very simple English, using only a few hundred words instead of the thousands of words in human language, but you can understand it from the start.

BASIC does have some defects caused by its inherent lack of structure. It is often said that, in BASIC, programmers have too much freedom to jump around. If a complex BASIC program is not well documented with comments, after a while it is even hard for the author to understand what has been done. To overcome this, Structured BASIC was developed. However, purists claim that the best cure for the defects of BASIC is not to use it.

As in FORTRAN, the common mathematical rules are generally followed, except that multiplication uses "\*" as a sign instead of "x." Trigonometry, arrays, matrices and other advanced operations can be done in many versions. With the extensions added over the years, BASIC has become an almost universal language.

The letters of the alphabet are used in equations. If you run out of A through Z, some versions let you use two letters, or a letter and a number.

Although BASIC is simple, it must be "spoken" with precision. It will not tolerate sloppiness. There are a few ground rules that must be followed.

A BASIC program (Fig. 4) consists of statements on numbered lines which are executed one at a time. The program can be made to jump around successive statements, or to other sections of the program, and then return to execute the next line in the program. Control of the program operation is executed via a few easily learned commands, such as PRINT, RUN, GOTO, GOSUB, RETURN, READ, and INPUT. A beginner who has never operated a computer can be writing programs after one or two hours of instruction.

One of the most useful features is BASIC's ability to access machine-language routines through call instruc-

tions and PEEK and POKE commands. Some versions have the ability to chain BASIC programs together into a complete software system.

The original operating mode of BASIC was as an interactive interpreter. In the interactive mode of operation, the user types a line and then presses the RETURN key or its equivalent. This returns control to the computer which acts upon what the user has typed. The BASIC then returns control to the user, who types the next line.

Many of the more complex versions of BASIC have been written as compilers. Previously, many commercial publishers of business software were afraid to publish application programs in interpretive BASIC because the source had to be supplied to run the program. The introduction of compiled BASIC and intermediate versions removed this condition and has been an important factor in the growth of microcomputer software development for the entire field.



## FORTRAN (FORmula TRANslator)

**T**HIS was one of the first high-level languages to achieve standardization and wide acceptance. It was mainly designed for scientific and mathematical use with large computers, and is always a compiled language. Many business calculations are performed in the FORTRAN language.

FORTRAN is a statement-oriented language, using alphanumeric, mathematical symbols, and logical ex-

pressions. Only lines with labels that are referenced elsewhere in the program are numbered in ascending order. FORTRAN executes statements in order and is much more rigid in format than BASIC. The sequence of the program statements is: 1. Specification, 2. Statement Function Definition, 3. Executable Statements. In addition, it is possible to call machine-language subroutines, or FORTRAN subroutines from a previously compiled library.

Early microcomputers did not have the memory capacity to run FORTRAN programs and, therefore, made do with BASIC. With the development of larger memories and the ability to run compiled languages, it became possible to run FORTRAN on smaller computers. There are now many FORTRAN compilers used with microcomputers. But because of the wide availability of compiled BASIC and its ability to do anything that FORTRAN can do, weaknesses aside, the latter has not replaced BASIC in popularity.



## COBOL (COmmon Business Oriented Language)

**D**ESIGNED to be used in a business environment, within a short time after COBOL's introduction it seemed that every computer installation had evolved its own version. To straighten out the confusion and under the lead-

ership of the U.S. Navy, a new standard language of business was created, known as ANS (American National Standard) COBOL. Today, many versions of COBOL have extensions beyond the standard, but this is clearly indicated in the instruction manuals.

COBOL is a statement-oriented, compiled language, very rigid in format, designed to match the flow of data in normal business transactions. This data is collected, punched into cards, and processed under COBOL programs in a batch mode on mainframe computers.

Interactive COBOL was only recently developed, matching the development of key-to-disk data input (replacing punch cards). This type of COBOL can run on minicomputers.

COBOL has only been available on micros for a short time and it has been used for many business applications from large computer sources. Little original programming has yet been done with microcomputer COBOL, so it is early to say if it will prove to be as popular on micros as it is on larger machines. Where software packages written in COBOL have been sold, often only the object code has been supplied, while the language has been transparent to the user.

COBOL programs are separated into divisions. The first is the Identification Division which always includes the author's name, and the date when the program was compiled. In addition, this division lists the Installation,

Date Written, Security level and Remarks, if desired.

The second is the Environmental Division which specifies the hardware needed by the program, and how it relates to the files. The Configuration Section and Special Names are also part of this division. The Input/Output Section is needed if files are used. For each file there is a SELECT entry and an ASSIGN clause.

The Data Division includes the File Section and the Working Storage section. Finally, the Procedure Division completes the program. All of this must be done within rigid specifications. This creates a self-documenting program that only requires an additional explanation of why certain procedures were selected for a problem.

The trouble with using COBOL for interactive microcomputer programs is that all these requirements were originally developed for a punch-card operation in a batch mode, where the programming and operating functions are separated. In an interactive operating mode, these requirements take up space and use a lot of the computer memory. This is one of the reasons there has been no rush to adopt the COBOL now available for microcomputers.



## APL (A Programming Language)

**A**PL was created at IBM by Kenneth Iverson. It remained an internal language until it was released in the 1960s as an interactive, time-sharing language running on large mainframe computers. APL was originally a scientific language noted for its ability to create and manipulate multidimensional matrices.

A number of the IBM people who had been involved in the development of APL left the company and started time-sharing services devoted to the language. These services extended APL, added file structures, and made it into a business-oriented language. APL is especially valued by insurance companies and airlines for their complex routing and scheduling problems.

APL has been implemented on mainframes and large minis, but only on two microcomputers. Recently, however, two versions were released to run under CP/M. There are several problems with APL as a popular language. First, it has a character set which is different from any other. Some of the characters require two key-strokes with a backspace in the middle and this is not compatible with most terminals. It does not use the ASCII code (the industry standard) but instead uses Z-code,

which is completely different. Second, all operations in APL are evaluated from right to left. In our culture we are used to evaluating things from left to right and this can be confusing. Third, APL uses very complex operators that permit programmers to express complicated ideas on a single line. This has led to a very compact code that is hard to read unless it has frequent comments.

APL is always an interpretive language, using a lot of memory space and running slower than compiled languages. It gives each user a large block of memory called the Workspace as soon as he signs-on the system. In addition, it does not save individual programs (called functions), but instead saves the entire Workspace. This also uses up memory space.

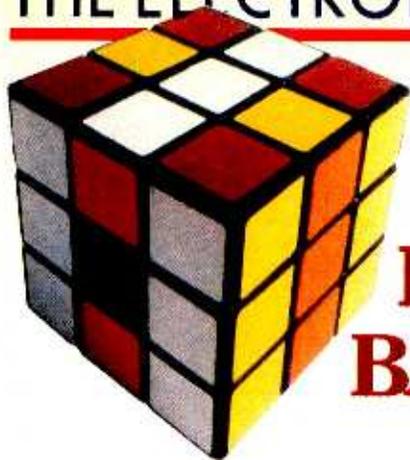
APL has been restricted to large computers until recently. Thus, it does not have the wide application of COBOL, FORTRAN, or BASIC. In addition, with the development of Pascal, C, and Ada, which can also use multidimensional arrays, one of the main advantages of APL has been usurped. It should be mentioned that the new IBM small computers no longer support APL and that cassette software in APL for the Model 5100 is also no longer supported.

## STRUCTURED LANGUAGES

Structured language is based on a hierarchy of operations starting with the general and proceeding to the specific. There has been a movement in computer science toward languages that fully utilize data structures and require a user to declare all the specifications for a program change. Pascal, PL/1, C, and Ada are languages of

this type; and they are replacing the older languages for both business and scientific computing. Structured languages, especially Pascal, are becoming the major instructional languages in computer science departments. Accordingly, they are expected to encroach on FORTRAN, COBOL, and possibly even BASIC in time as the most

important languages. Pascal is already supported on all major minicomputers, microcomputers, and a great many mainframes. PL/1, which was originally a language for very large computers, has been adapted for smaller machines. And the U.S. Defense Dept. will make Ada mandatory for all its offices and contractors by 1985. ■■■■➔



## Pascal- BASIC's Successor?

**A**BOUT the same time that FORTRAN was being developed in the United States, another language was designed to implement solutions to complex algorithms. It was called ALGOL (Algorithmic Language) and it became popular in Europe. It is an excellent language, but somewhat difficult to learn. In 1971, Niklaus Wirth of Zurich, Switzerland, invented Pascal as a tool for teaching ALGOL and to demonstrate the principals of structured language. In 1975, the standard Pascal was defined in *Pascal User Manual and Report* by Kathleen Jensen and Niklaus Wirth. Pascal is an easy language to learn and it is suitable for defining the data structures needed for problem solutions. The language was named for Blaise Pascal, the French mathematician who invented one of the first mechanical computing devices. So the name is not an acronym and, therefore, all letters are not capitalized.

Pascal compilers were written for mainframe computers and the language gained popularity in the computer community. At the University of California, San Diego, Dr. Kenneth L. Bowles started to implement Pascal on mini and microcomputers. The result has been the UCSD Pascal System. This is not only an implementation of Pascal, but an entire operating system that includes several Editors, a File Handling System, an Assembler, a Compiler, and a Debugger. UCSD Pascal now runs on Apple, North Star, Texas Instrument, Radio Shack TRS-80 Mod II, OSI, DEC, Western Digital Microengine, and many other personal computers. Other versions of Pascal are used on minicomputers, large mainframes, and microcomputers. Almost every manufacturer of computers supports some kind of Pascal in addition to whatever other language he uses.

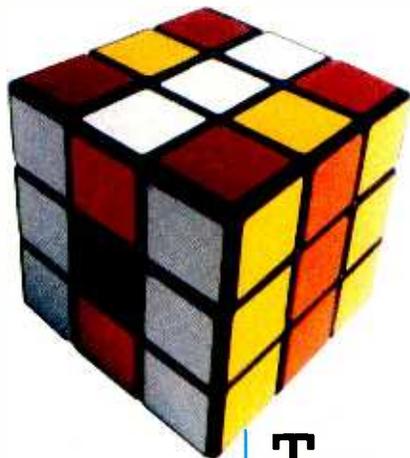
Like BASIC, Pascal "speaks" English, and uses the conventional mathematical symbols. It can do trigonometry and advanced mathematical operations, and deal with character data and strings. Pascal operates on standard data types such as integer, real, and Boolean, but gives the programmer the freedom to define new data types. The programmer can also define new functions and procedures.

Pascal is a compiled language, but it does not usually compile into machine code. Instead, it compiles into an intermediate pseudo-code called p-code. The p-code is then saved on the disk file system. At run-time, the p-code file is interpreted into the machine code of the computer.

As with all other intermediate-code languages, this method makes the language portable. To move Pascal to a new computer, all you have to do is write a new interpreter from p-code to the machine language, a far simpler task than adapting a complete language. In one computer, the Western Digital Microengine, p-code is the machine language of the microprocessor, allowing it to run without the interpretive step.

There are other versions of Pascal that do compile into machine code, called "native-code compilers." They run very fast, but are not transportable from machine to machine. Pascal-Z, produced by Ithaca Inter-systems Inc. for the Z-80, is of this type.

Pascal is fast becoming the most popular language for application software, a fact that is not always apparent because only the object code and a run-time package are delivered with the application system. (This provides a measure of protection for the software publisher). Figure 5 shows the same problem used in preceding references, done in Pascal.



## The C Language

**T**HE language called C is a computer language designed at Bell Laboratories to operate upon the powerful OS called UNIX. It is a structured language with some resemblance to Pascal. However, Pascal uses both functions and procedures, while the C language

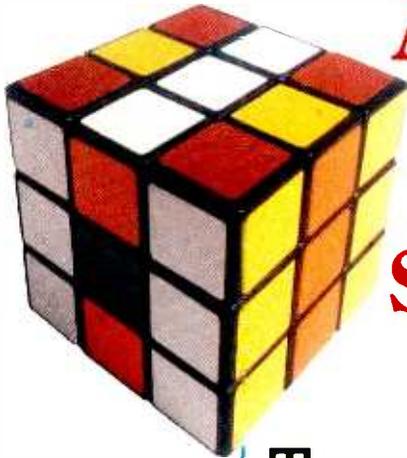
achieves modularity only through the use of functions. It builds the entire program structure through the use of functions even to the point of having no Print or Read statements. It does input and output through use of functions. It does have "if-then-else," "while loops," global and local variables and data types, pointers and arrays. Like Pascal, it can replace single statements with compound statements to promote program flow. C is a compiled language in which programs are composed using an Editor and then are compiled into machine code versions to be run on the computer. It has no I/O structure

of its own; instead, it uses the I/O of whatever operating system it is implemented on.

A C program is a set of functions. The ability of the programmer to create his own functions according to his needs make C an unusually flexible language. There are no line numbers in C. The program starts with the name of the function, then a square bracket to start function definition. This consists of compound statements between two square brackets. Statements are nested to any depth required and are treated just like simple statements. There are libraries of standard functions and those functions previously defined by the user. All of these can be called for use in the program. There can be both Global and Local variables and there can be arguments for the functions. There are also Expressions which are used to calculate and store data. C can call

machine-language routines when needed as well as any of the personal or standard function libraries.

While C originated on the UNIX OS, it has been transported to run on other operating systems such as CP/M and Unix-like systems. Many application software packages for micros have now been written in the C language and sold only in object code form. As the UNIX OS and its look-alikes become more popular, the C language will become more widely used. With Pascal, the original idea of a language that was transportable between machines seems to be lost as more and more incompatible versions come into use. With C this has not happened. The entire language was written by one person and there is one book specifying it: *The C Programming Language* by Brian W. Kernighan and Dennis M. Ritchie (Prentice-Hall, 1978).



## Ada— The Rising Star

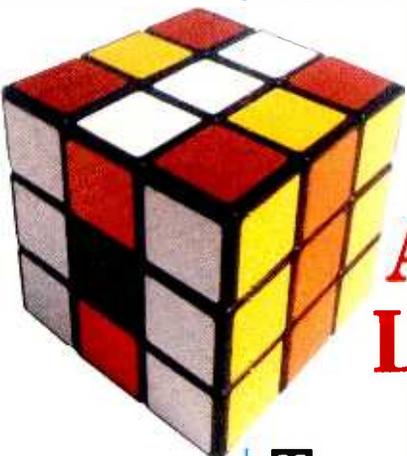
**T**HE COBOL language became a standard business language because it was required by government agencies in the 1960's. Now, the U.S. Department of Defense has decided that a new language is needed to coordinate the application needs of the Army, Navy, and Air Force. The process of development for this language was started in 1975 when suggestions were solicited from the three military services, industry, and academia. At the same time, an intensive study of existing languages was made to determine if any of them met the requirements for a universal language, and if not, to develop specifications for one. It was recommended that either Pascal, ALGOL 68, or PL/1 be used as the starting point for the new design. Pascal was the one put forward, and the new language was developed from it. Called the "Green" language, it was renamed Ada in honor of the first computer programmer, Lady Ada

Augusta Byron, Countess Lovelace, daughter of the poet Lord Byron. Since then, Ada has been undergoing extensive tests and compilers have been written for several mainframe computers.

Dr. Kenneth Bowles, the leader of the project that developed UCSD Pascal, has left the University of California and is working on implementing Ada on some of the advanced microcomputers using Western Digital microprocessors and the Motorola 68000 chip. Other versions will follow. This kind of support, and the fact that Ada will be mandatory for the Defense Dept. and all its contractors by 1985, portends a very important role for the new language.

Ada looks like Pascal. It has a declarative part and a statement part. It is a strongly typed language because all identifiers must be declared and their attributes specified. The two most important control structures are the conditional statements (which select alternative actions) and loop statements (which specify repetition of an action). Ada uses many types of functions and subprograms called Procedures. In addition, Ada also has two kinds of modules called Packages and Tasks.

Packages are used to define logically related collections of resources for use in computation. Tasks are separate jobs that are done at the same time in either a time-sharing environment, or a distributed processing system. The collective term for this is 'Multitasking' and ADA has been designed to set up and run such jobs as part of a program. Similar things can be done by Pascal, C, or PL/1, but Ada is the first language created to manage the complex computing activities that use multiple processors with almost unlimited memory resources.



## Forth— An Independent Language

**T**HIS is another language created by one man, Charles H. Moore, and it was first used to control the telescope at Kitts Peak Observatory. It has since developed

a following of programmers who have carried its banner at all computer meetings and shows, more like a cult than a computer language user group.

Forth is not an easy language to learn as it is quite dissimilar from anything we are used to. In addition, it does its calculations in Reverse Polish Notation (RPN). It is sometimes called the unfinished language because the programmer has almost unlimited freedom to create new Words. *Everything* in Forth is a Word (which is

another term for a function). It is not much good as a number cruncher, but it can link to subroutines in other languages for the heavy math. It greatly reduces the cost and work of subroutines. The programmer keeps on defining new words by using old ones, and before you notice, the job is done. You do not have to do much original work to write a new program because when the system comes up, so does all the work you have ever done before, just as if it had always been part of the language! Now you see why programmers take the trouble to learn this strange language.

When you look at a Forth program, it is confusing because everything seems to run together, but after a while it begins to make sense.

In Forth, most operations communicate by using the Stack (that section of memory where you store numbers in last-in-first-out order). While all languages use the stack, its operation is usually incorporated into the language itself. Not so in Forth, where the programmer controls the stack directly.

There are some normal things about Forth. It is a

structured language with no GOTOS or labels for statements. It is an interpretive language that is later compiled into machine-readable code, and therefore needs very little space in the computer memory. A full Forth can fit into a 16K machine and have room for 8K of programs. In addition, it is low cost. The Forth Interest Group has made versions available for almost every microprocessor. Even the commercial versions are cheap and offer a lot of features for the money.

One other characteristic of Forth is that it is a Threaded Language. This means that programs are constructed from a few subroutines which are connected together by a series of subroutine calls to perform a larger task. This entity is then called by a larger routine and connected to others to form a still larger entity. Threadedness is not restricted to Forth, but while other languages can use it, all Forth versions do use it.

When you go to a computer show and you see the cult members who have found the One True Language and wear the funny buttons, don't laugh. Perhaps they have found it and the rest of us are just lazy.



## LISP (LIST Processing Language)

**T**HE language of artificial intelligence research, LISP was based upon John McCarthy's work on nonnumeric computer languages published in 1960. The language LISP was implemented at MIT and is described in the LISP 1.5 Programmers Manual. It has since been configured on many mainframe computers, minicomputers and microcomputers.

LISP is a nonmathematical language composed of words, like any language. In LISP there are two kinds of words: atoms and lists. Atoms are the basic entities of LISP. Basically, any combination of the characters of the alphabet, A,B,C . . . X,Y,Z with any of the ten digits 0, 1 . . . 9 is an atom, as long as it starts with a letter. A list is the second type of word in LISP, and it is built up from atoms and other lists. A list consists of a left par-

entesis followed by any number of atoms and lists, terminated with a right parenthesis. The language has functions, variables, and arithmetic operators, but it looks strange to BASIC programmers because all the arithmetic operations are in Reverse Polish Notation (RPN). A LISP sentence looks like a list, but it carries meaning and it is actually an elementary program. All LISP functions have a single value and a program consisting of functions applied to arguments. The LISP language has many built-in functions, and the programmer can create functions at will.

The printout of a LISP program looks unusual, but if the LISP includes a "Prettyprint program," which formats a program by indenting subsections, the listing will look much more conventional.

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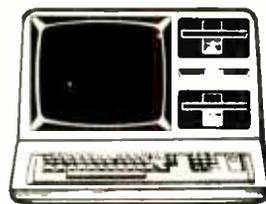
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## PILOT (Programmed Inquiry, Learning Or Teaching)

**P**ILOT was the first computer language dedicated to computer-aided instruction, and was developed at the University of California, San Francisco. It has been implemented on many computers ranging from very large mainframes to the simplest micros. This interactive language enables a person without prior computer experience to develop and test dialogue programs for teaching, since its structure and syntax are easy to explain to a student.

Using PILOT, the teacher can present the student with a reading passage, give him time to study it and then ask him multiple-choice questions based upon the passage. The program can include computer responses keyed to the answer the student has given. It can also scan his response and give him advice or comment based upon that response. It can introduce a mathematical problem and offer the solution on a step-by-step basis or give the student an opportunity to discover as many of the steps as he can, with hints from the computer.

PILOT instructions are divided into six categories:

1. Core Instructions. These basic functions are single-letter instructions and are standard for all of the ver-

sions of PILOT. Thus the programs are portable from machine to machine. The instructions are:

T: TYPE (includes Y: and N:)  
A: ACCEPT  
M: MATCH  
J: JUMP  
U: USE  
E: END  
C: COMPUTE  
R: REMARK

There are also multiword instructions called "keywords" that have been added to PILOT for special applications and are not included in all versions.

2. Cursor and video instructions to determine where the text will appear on the screen.

3. Instructions that set various kinds of parameters related to the computer such as output ports, display speed, or memory locations.

4. File system instructions relating to storing and retrieving programs and data.



## LOGO- A Learning Language

**T**HE LOGO language represents a completely different path to learning than does PILOT. Its inventor, Seymour Papert, believes that CAI techniques, like PILOT, only transfer the old methods of teaching to the computer without using the unique capabilities of this new tool to combine text, form, color, and sound into a new learning system. For the last ten years at MIT, he and his colleagues have been working to perfect the techniques used in LOGO. The result is a language in which a five-year-old can quickly learn to write a program. Yet it is sophisticated enough for higher instruction.

In the child's version, LOGO uses basic modes called sprites and turtles. The sprites are forms that the child

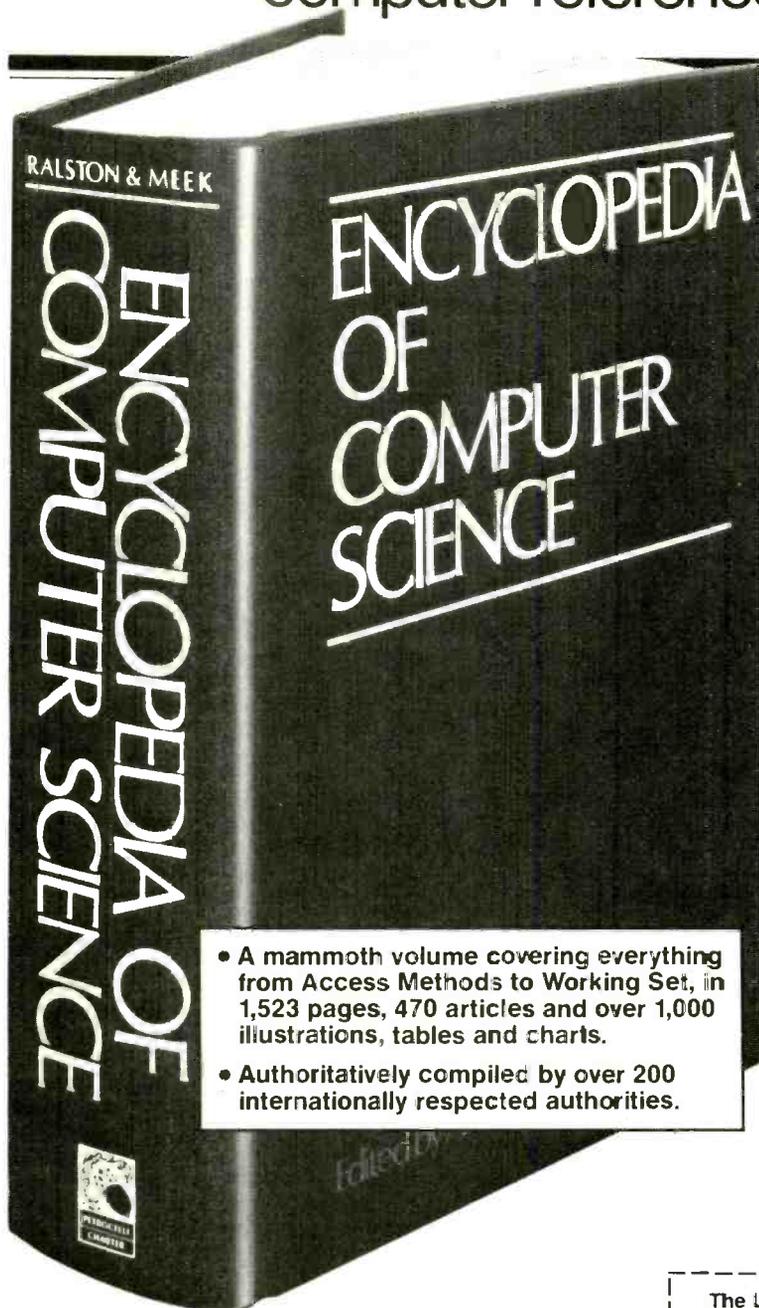
creates that move around the screen at any speed the child selects. The turtle is a figure that the child can interact with, moving it over the screen, coloring it, and making it draw or erase lines.

The teacher can also program more complex functions (programs) that children can interact with through simple keyboard responses. Children learn color, direction, letters, words, and sounds through this medium and usually find it fun. It also teaches them planning, and the use of the computer which will be one of their major educational tools throughout their school years.

At this time, LOGO is available for the T.I. 990/4 computer, and there is a version for the more popular Apple II that MIT has not yet released.

(Continued on page 56)

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## Other Computer Languages

**PL/1.** This language was one of the first structured languages and was designed by IBM to run its mainframe computers. The language suffered from its great scope and complexity. Later subsets of PL/1, such as PL/M, were designed to run on minicomputers, and they were often used as cross-compilers to develop microcomputer software on larger machines. Until very recently, no micro had enough memory and capacity to run PL/1. However, with the development of the 16-bit and 32-bit microprocessors, this language has a future for use on the large micros. Digital Research, the developer of CP/M, has a version called PL/1-80 to run under advanced versions of CP/M and MP/M.

**CAI. (Computer Aided Instruction) Languages.** This term designates a family of languages used with computers as a teaching tool. In the next decade, CAI will become more important as we learn to use the computer to enhance our educational system. Some of the languages designed to aid in education have already been discussed.

**Report Generation Languages.** RPG II is one of the most widely used languages for mainframes and large minicomputers. It is used to create report formats for the output of all kinds of application software. In the future, with multiuser and multitasking computers being designed around 16-bit and 32-bit microprocessors, RPG languages may be used by all computers.

**Data Description Languages.** These languages are used to create, input, select, sort, and format information stored in a general application data base. They are generally not called "languages" by the software publishers, who only refer to the complete system by name. However, the CODASYL (Conference on Data Description Languages) which was formed to set standards for data base systems uses this term to refer to the entire Data Base Management System (DBMS).

Some of the commercial DBMS systems using very complex data description languages are TOTAL, RAMIS, ADBS, and IMS. All of these run on large mainframe computers. With the development of large floppy-disk and hard-disk storage systems, data-base systems became possible using mini and microcomputers. Some of the larger DBMS were scaled down to run on minis, but most of the micro systems were written for microcomputers specifically. They all use operators and functions that are complex enough to be a complete language.

**Program Generation Languages.** This is a new family of software systems that constitute a set of languages. Their object is to automatically write programs in another language. They are a kind of "paint-by-num-

bers" software. They present the user with a set of fill-in-the-blank screens to enable the user to specify just what he wants to do. The answers to these questions constitute a pseudo program from which the system "writes" a program in BASIC, or whatever language the system is designed to use. In reality, the "system" is an English-to-BASIC translator language. PEARL and "The Last One" are typical of this type of system, but are by no means the only ones or the last ones.

**Conclusion.** Once you have read about computer languages and begun to understand their differences, you may find that you still can't decide which one is the best for you. There are just too many choices. For example, you may need to control a robot that has a single-board computer and only 4K of memory. You could use machine language or assembly language, or you could write in Forth and compile to machine code. However, you could also use Control BASIC or Tiny Pascal. The choice is yours, and there is no single answer.

If you are interested in business applications, home controls, or scientific research, the options are still wider. Perhaps this is why Charles More invented Forth to control his telescope; machine and assembly language took too long to use while other languages were too rigid and did not allow him adequate flexibility. Today, you do not have to invent a language to tailor a program to your needs.

For most people, the choice has been made for them. Usually, a computer comes with a language, most often a simple version of BASIC in ROM. Once you learn this BASIC, you will probably find that you can do all kinds of wonderful things with it. You will likely want to do more by adding memory and a floppy disk or two. This, in turn, opens up the world of disk software for word processing, business applications, etc. This is also when you find out that you have to buy the exact package to run with your BASIC and your DOS because there were many different types.

Do you want to try different languages on your computer? Well, it's simple. All you have to do is buy a version of a language that runs under your DOS and does not require more memory than you have. Then read the manual that comes with the package, put the diskette in your drive, and you are running PASCAL, FORTRAN, or COBOL. You also will need a good textbook—one designed for microcomputer versions of the language. The manual you get with the language package teaches you how to run the language and what special things are in that version. It does not teach you the language. Study your text, use what you learn on your computer, and it will bring back the fun you had when you first bought the machine as well as giving you opportunities to use more efficient languages for particular purposes.

For those who haven't yet bought a computer, but are thinking of it, everything we have said applies to you also. If you want to have a choice of languages later, be sure you choose a machine that has a variety of languages available.

Most often, language packages are available from the computer manufacturer. However, they are also sold by software companies that specialize in one or more languages. Another good source is the computer clubs since they may serve as a distribution channel for languages developed by universities and the government. Language user groups also distribute languages at low cost. The FIG (Forth Interest Group), for example, sells its software and books—and at reasonable prices. ♦



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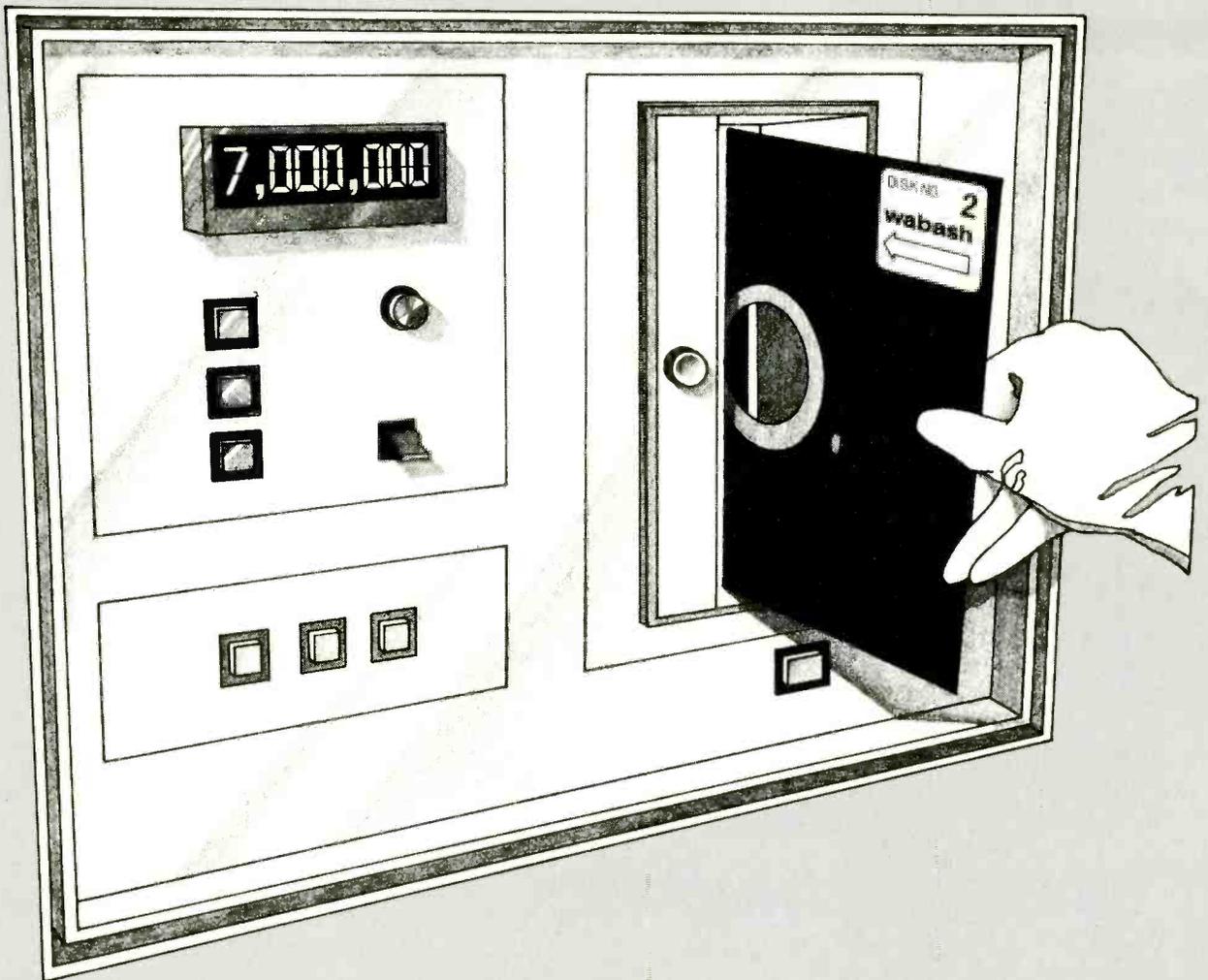
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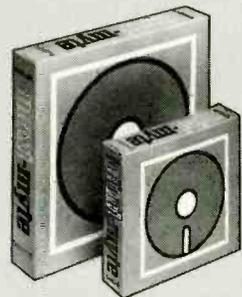
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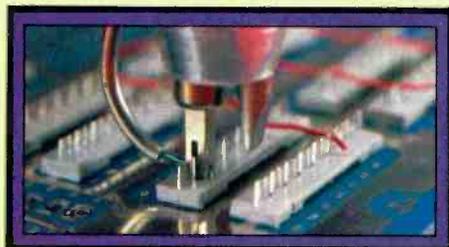
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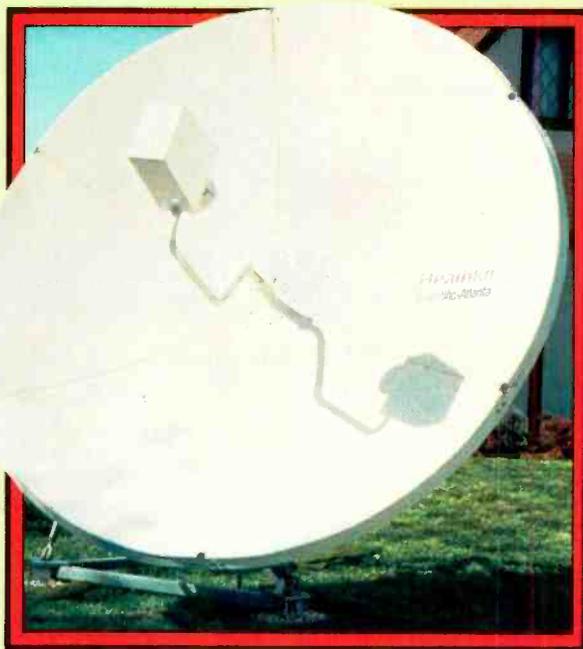
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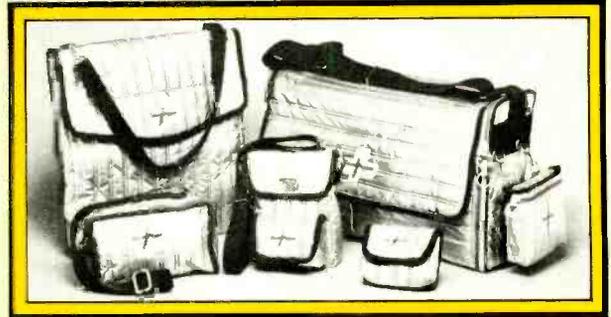
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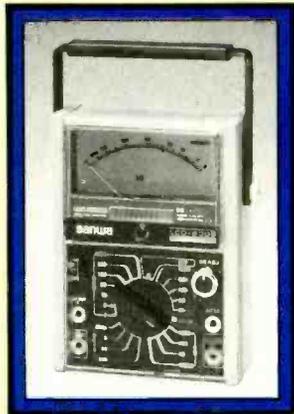
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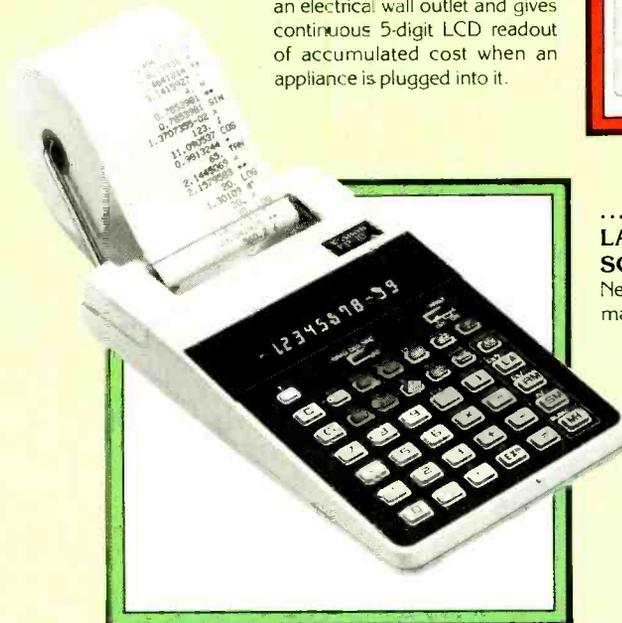
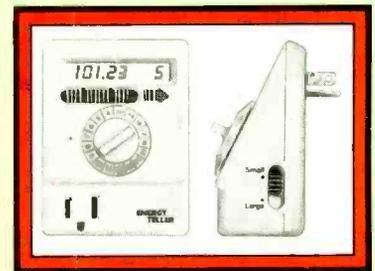


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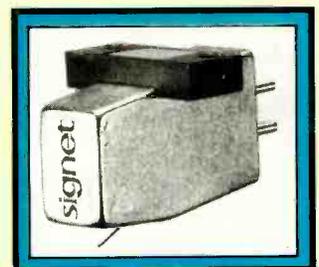
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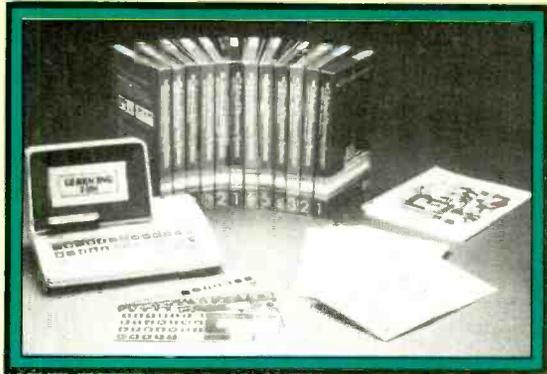
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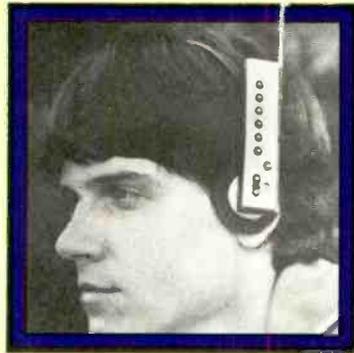
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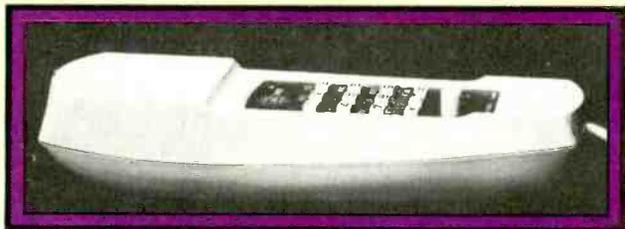
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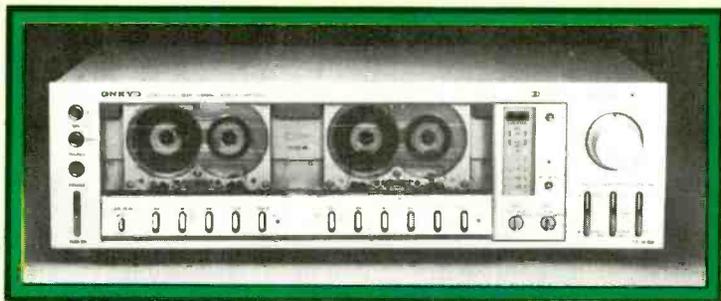
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# COMPARING HIGH-TECH AUDIO CASSETTE TAPES

What makes one cassette tape better than another? Are there any real differences between the technologically best products? To provide an answer to these questions, samples of some of today's "top tapes" were subjected to a number of established tests designed to bring out their strengths and weaknesses.

LET'S begin our comparison with an overview of the tape market. There are four major cassette tape *types* currently available. The most widespread is Type I, ferric oxide ( $\text{Fe}_2\text{O}_3$ ), which is produced in all grades of quality, from 3-for-a-dollar unbranded specials to top-notch formulations, some of which are lightly laced with cobalt to improve their magnetic performance. As a type, ferric-oxide tapes use the least record

bias, and are designed to be played back with 120-microsecond equalization (see Fig. 1).

Type II tapes are known generically as chromium dioxide ( $\text{CrO}_2$ ), though most such tapes today are cobalt-enhanced ferric-oxide formulations, usually designated as "chrome equivalent." Chrome-type tapes use roughly 50 percent more bias than ferrics, and are intended for playback with 70-microsec-

ond equalization. The difference between 70-microsecond (chrome) and 120-microsecond (ferric) playback equalization affects the amount of tape hiss you hear. All else being equal (though it rarely is!), you'll get less hiss from 70- than from 120-microsecond playback equalization (see Fig. 1).

Ferrichrome ( $\text{FeCr}$ ) tapes are designated Type III, and use two separately coated layers of magnetic material: a relatively heavy layer of ferric oxide, topped by a thin layer of chromium dioxide. Highly popular in the car stereo market, ferrichromes have not won wide acceptance among home-based audiophiles because of wide variations from brand to brand. Thus, hi-fi cognescenti rarely use these tapes for home systems; and for that reason they were not tested in this study.

The newest entrants on the cassette scene, Type IV, are the metal-particle tapes. Although these can be *played back* on any tape deck with a 70-microsecond ("chrome") equalization position, their extremely high bias requirement—approximately twice that of

CrO<sub>2</sub>—demands that they be recorded on a deck whose heads and bias oscillator are designed to handle the high current associated with such high bias.

Theoretically, the “best” tape is that which produces the least inherent noise while providing the greatest undistorted signal storage capacity across the range of desired wavelengths. “Wavelengths” correspond to “frequency response,” once you factor in tape speed. One second’s worth of a 1000-Hz tone at a tape speed of 15 inches-per-second has a wavelength eight times as long as the same one-second tone at the cassette speed of 1 7/8 ips, and a tape formulation optimized for the former speed will not be equally suitable for the latter.

“Undistorted” is admittedly a weak word, but long-standing tradition, based on successful experience, favors defining it in terms of 3% third-harmonic distortion at a suitable middle frequency for the tape speed involved. For the tests in this report, the 400-Hz frequency used by Dolby-level calibration tapes was chosen.

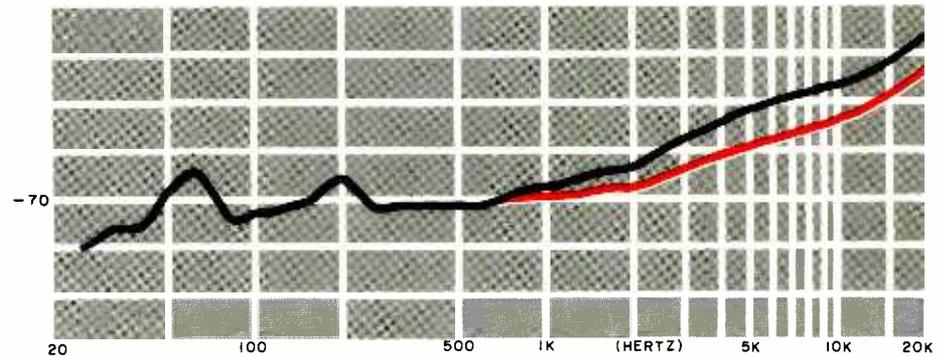
Practically, of course, the “best” tape is that which most closely approximates the theoretical ideal on your machine. This is an important caveat. Tapes do test (and perform) somewhat differently on different tape decks and under different bias (and record equalization) conditions. I used the top-quality three-head Nakamichi 582, which has a wide record-head gap (3.5 microns) and a narrow-gap playback head (0.9 microns). A typical two-head cassette recorder, on the other hand, might have a record-playback head with a gap of 1.2 to 1.3 microns. The head gap affects both the high- and low-frequency extremes. Too wide a gap limits treble playback response; too narrow a gap may limit penetration of the record signal to the full depth of the magnetic coating.

In practical terms, a deck with a wide record-head gap will favor tapes with a relatively thick magnetic coating when measuring signal-to-noise ratio; the advantage of the thick coating may not be realized on a two-head deck. Similarly, a recorder with a narrow-gap playback head will tend to favor tapes with highly polished surfaces, which facilitate flat response to 20,000 Hz and beyond, though this advantage might not show up at all on a typical two-head deck, whose response may only extend to about 16.5 kHz.

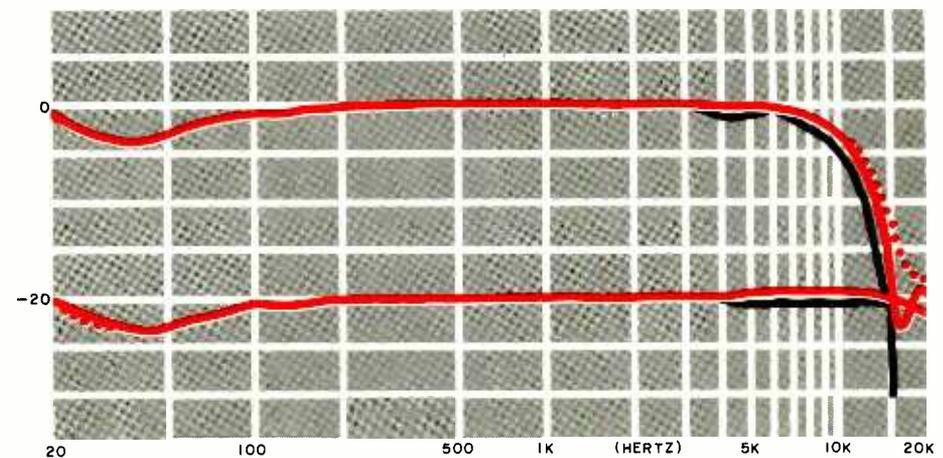
Record bias and equalization are additional considerations when tapes are compared. Most decks provide a 3- or 4-position switch that sets bias and equalization (EQ) for optimum performance from a specific tape within

each basic type. Brand-to-brand differences among tapes of the same type do exist, however, which is the rationale for the “bias optimization” controls included in many decks. Using the adjustments available on my tape deck, I could have optimized the deck for each tape in turn, but this wouldn’t show up tape differences under the fixed conditions most

deck owners have to use. I chose instead, therefore, to set up my test deck with the tapes most frequently specified as having been used in factory alignment by the numerous deck manufacturers whose recorders I have tested in recent years. These are: Maxell UD-XLI (ferric); TDK SA (CrO<sub>2</sub>-type); and TDK MA (metal).



**Fig. 1. Comparison of noise-level between 70-microsecond (colored curve) and 120-microsecond (black curve) playback equalization. The 70-dB level is with reference to 200 nanowebers/meter (Dolby level). The 70-microsecond equalization is standard for chromium dioxide tapes and 120-microsecond for ferric. If all other factors are equal, there should be less hiss from the 70-microsecond playback equalization.**



**Fig. 2. Response curves for the three reference tapes: Type I, Maxell UD-XLI (ferric), solid black curve; Type II, TDK SA (chrome), solid color curve; Type IV, TDK MA (metal) dotted color curve. (These conventions with regard to the curves and Types apply to all of the response curves on the opposite page.) The tape deck used in the tests was adjusted for the three reference tapes and left that way for the rest of the tests.**

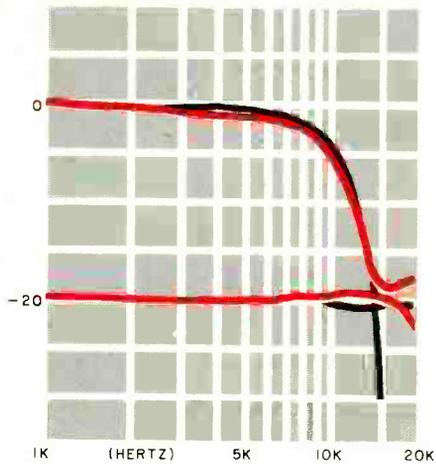


Fig. 3. BASF Professional I (solid black); BASF Professional II (solid color).

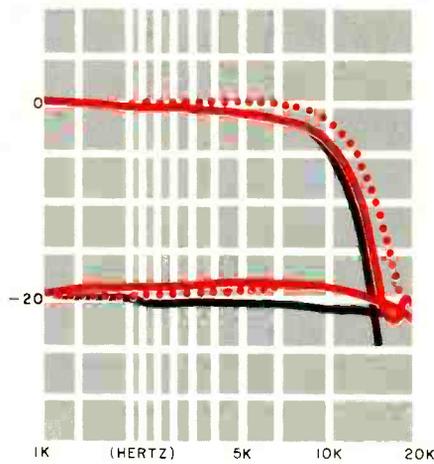


Fig. 4. Fuji FX-I (solid black); Fuji FX-II (solid color); Fuji Metal (dotted color).

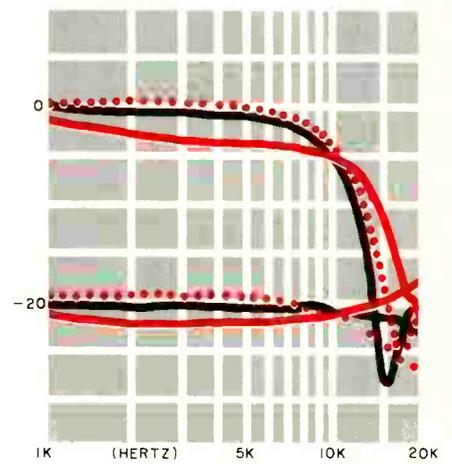


Fig. 5. Loran Normal Bias (solid black); Loran Chrome (solid color); JVC ME-P (dotted color).

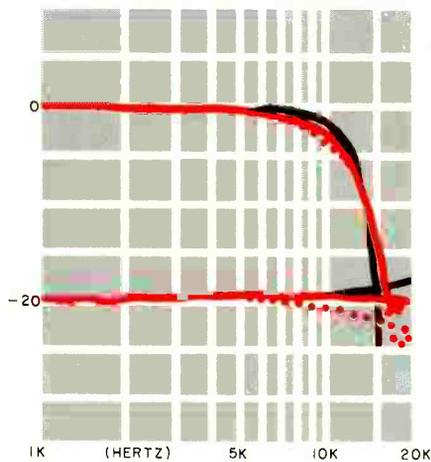


Fig. 6. Maxell XL-IS (solid black); Maxell XL-IIS (solid color); Maxell MX (dotted color).

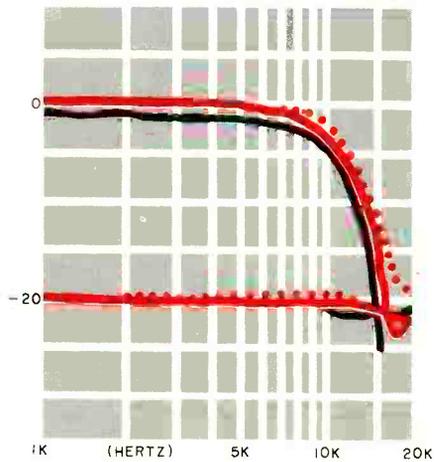


Fig. 7. Memorex MRX-I (solid black); Memorex HBII (solid color); Memorex Metal IV (dotted color).

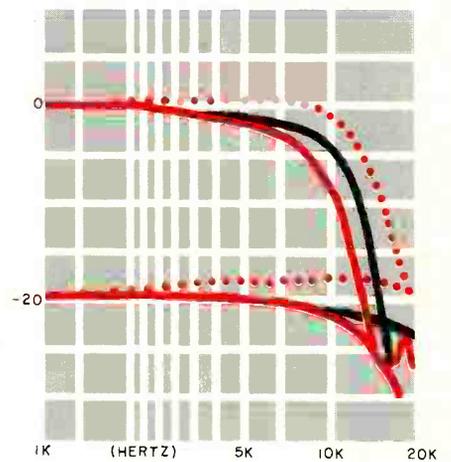


Fig. 8. PD Tri-Oxide Ferro (solid black); PD 500 Crolyn (solid color); PD 1100 Metal (dotted color).

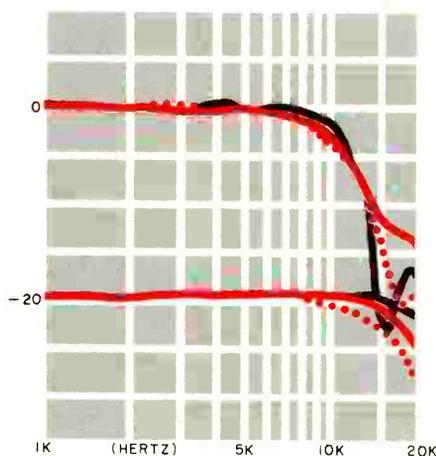


Fig. 9. Scotch Master I (solid black); Scotch Master II (solid color); Scotch Metafine (dotted color).

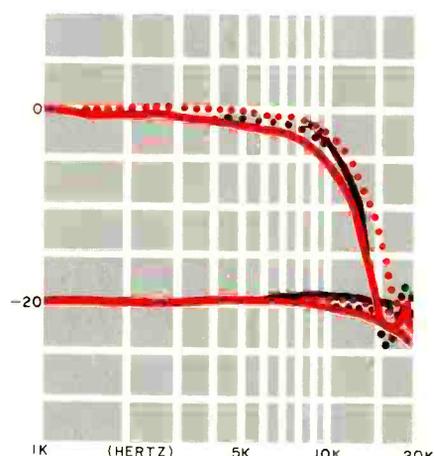


Fig. 10. Sony HFX (solid black); Sony EHF (solid color); Sony Dev. Hi-Bias (dotted black); Sony Metallic (dotted color).

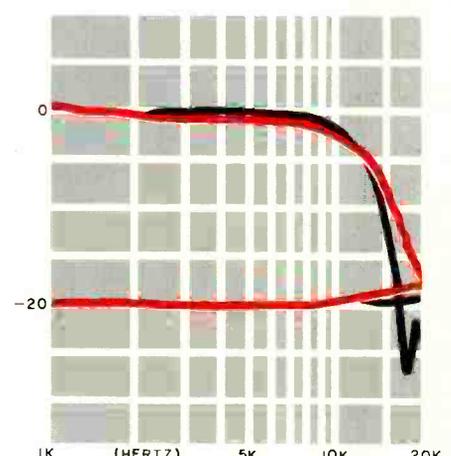


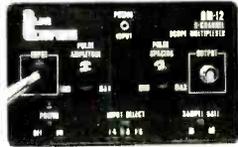
Fig. 11. TDK AD (solid black); TDK SA-X (solid color).

# ALBIA Electronics

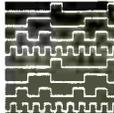
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- Trigger slope Sweep time Sweep-time magnifier Max. sweep rate
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- Weight
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**Testing the Tapes.** Using these tapes as reference points for each type, the first test was of tape sensitivity at 400 Hz, the Dolby calibration frequency. Each tape was fed a fixed-level tone, and the resulting output was measured. The results are in the first column of the Table. A variation of  $\pm 1$  dB is hardly consequential in and of itself. As  $\pm 2$  dB is approached, however, it is possible that some frequency-response errors may occur when using the Dolby noise-reduction system if your deck is not set up with this particular tape.

After checking sensitivity, I raised or lowered (as necessary) the 400-Hz level so that the tape played back at a standard 200 nanoWeber/meter (Dolby) level, and measured the third-harmonic distortion, using a General Radio 1900-A wave analyzer. These results are recorded in the second column. The record signal input was then raised to the point where the tape playback produced 3% third-harmonic distortion, the normal reference level for signal-to-

noise measurements. The higher the number in the third column (400 Hz Maximum Output Level) of the Table, the higher is the record level you can use before the onset of serious (audibly noticeable) distortion.

At the 400-Hz 3% maximum, the input signal was short-circuited so that biased tape noise level could be measured under no-signal conditions. The noise level was passed through an IEC "A" weighting network (whose weighting, by frequency, corresponds closely to the sensitivity of the ear to low-level sounds such as tape hiss), and the difference between the MOL and the biased tape noise is reported in the fifth column as the A-weighted signal-to-noise level.

To check the high-frequency capacity of the tape, I used a 10-kHz tone, adjusting the signal-input level to obtain maximum possible output. This is shown in the fourth column of the Table, with reference to the 400-Hz Dolby level of 200 nWb/m. As can be seen, no tape could produce that much output at 10

kHz, so the numbers are all negative. The closer the negative number is to zero, the more 10-kHz storage capacity the tape has, given the bias/machine conditions imposed. A lower bias level would raise the maximum 10-kHz output capacity of all the tapes, but it would also lower the 400-Hz MOL (column 3) and raise distortion at 200 nWb/m (column 2). It would also influence the high-frequency response of all of the tapes. On the other hand, a higher bias level would depress the 10-kHz MOL still further, though it might slightly increase the 400-Hz MOL.

As a final test, each of the tapes was measured for frequency response across the 20-to-20,000-Hz range, both at a 0-dB record level (200 nWb/m) and at the conventional -20-dB level normally used for checking frequency response. These results are presented graphically. Where a tape shows a rising high-frequency response at -20 dB, it is probable that it is slightly under-biased; where there is a treble fall-off at this level, it is almost certainly overbiased, since all of the tapes in this survey are capable of flat response throughout the audio range. The degree of high-frequency roll-off at the 0-dB level is an index of short-wavelength storage capacity.

**Conclusions.** The differences between tape types and among brands are slight enough to be virtually inaudible. Usually, a 3-dB difference between sounds of like frequency is necessary before a listener can determine which sound is less distorted or "noisy." Looking at each column of the Table, it is clear that the performance of any single tape falls within 3 dB of almost all the others. (The percent of Dolby-level distortion in the second column also reflects differences which would be inaudible to even the listener with above-average sensitivity.) Still, differences can be measured, if not always heard.

As a group, the metal tapes provide a very slight advantage over the CrO<sub>2</sub> types and a more significant advantage over the ferric oxides in terms of signal-to-noise ratio. In terms of 10-kHz storage capacity, metal tapes, overall, are better than ferrics and chromes—hardly surprising since this is metal tape's claim to fame. Keep in mind, though, that the metal tapes cost about 70% more than the other two types. (Ferrics and chromes cost about the same.)

Which should you buy? That's a tough question. If you don't have a high-end cassette deck, it probably doesn't make any difference. If you do, you can check the figures in the Table given here and make a determination based on numbers alone. Or you can trust your ears. Good luck! ◇

### TAPE TEST RESULTS

Tape Type I	400-Hz Sens.(dB)	Dolby-level Distortion (%)	400-Hz MOL (dB)	10 kHz MOL (dB)	S/N A-wtd (dB)
<b>Maxell UD-XLI</b>	0.0	0.38	+6.5	-5.2	58.0
<b>BASF Professional I</b>	-1.0	0.44	+5.5	-5.8	56.8
<b>Fuji FX-I*</b>	-1.3	0.31	+4.8	-6.2	58.5
<b>Loran Normal Bias</b>	0.0	0.34	+7.0	-6.0	59.5
<b>Maxell XL-IS</b>	0.0	0.45	+7.0	-3.0	58.2
<b>Memorex MRX-I</b>	-0.8	0.38	+6.2	-5.5	59.8
<b>PD Tri-Oxide Ferro*</b>	0.0	0.27	+7.0	-6.2	58.5
<b>Scotch Master I</b>	0.0	0.38	+7.2	-4.8	58.2
<b>Sony HFX</b>	-0.8	0.51	+4.8	-5.8	56.1
<b>TDK AD</b>	-1.0	0.50	+4.8	-2.8	58.9
<b>Type II</b>					
<b>TDK SA</b>	0.0	0.64	+5.0	-5.5	59.1
<b>BASF Professional II</b>	-1.4	1.05	+4.1	-6.5	61.2
<b>Fuji FX-II*</b>	0.0	0.86	+4.8	-5.0	60.1
<b>Loran Chrome</b>	-0.3	1.20	+4.1	-6.3	60.9
<b>Maxell XL-IIS</b>	-0.4	0.97	+4.2	-4.5	58.8
<b>Memorex HBII</b>	-0.2	0.80	+4.8	-4.8	59.5
<b>PD 500 Crolyn*</b>	-1.2	1.00	+4.5	-9.8	61.1
<b>Scotch Master II</b>	+0.8	0.49	+6.5	-5.5	60.4
<b>Sony EHF</b>	+0.6	0.77	+5.2	-7.0	59.7
<b>Sony Dev. High-Bias**</b>	+1.3	0.40	+7.0	-4.8	61.1
<b>TDK SA-X</b>	+1.6	0.30	+5.2	-3.0	59.6
<b>Type IV</b>					
<b>TDK MA</b>	0.0	0.54	+6.5	-3.8	59.8
<b>Fuji Metal*</b>	-0.4	0.60	+6.1	-3.5	59.5
<b>JVC ME-P</b>	-1.0	0.54	+6.2	-6.2	60.0
<b>Maxell MX</b>	-0.4	0.58	+6.2	-5.0	60.2
<b>Memorex Metal IV</b>	+0.3	0.31	+7.9	-2.8	61.4
<b>PD1100 Metal*</b>	-0.8	0.94	+5.5	-2.8	59.5
<b>Scotch Metafine***</b>	+0.5	0.48	+6.8	-5.2	62.3
<b>Sony Metallic</b>	0.0	0.54	+6.5	-4.5	60.8

\*Single sample only supplied

\*\*New product, to be released soon

\*\*\*No samples supplied; tapes purchased 4/81 commercially

# AN EARLY-WARNING CAR BATTERY-DRAIN INDICATOR

*To avoid a dead battery, replace the standard "idiot" light with a visual-audible monitoring system*

**L**IKE THE common cold, the "dead battery" syndrome seems to be especially prevalent in the winter. This project, the Multipurpose Electrical System Monitor And Tester (MESMAT), may help your car avoid the dreaded ailment.

MESMAT replaces the ineffective "idiot" light, which will not light until the 12-V battery is discharging at a rate of 10 to 15 amperes. Consequently, a battery could be completely discharged while the car is running without the standard warning light being activated. The project uses five LEDs to continuously monitor the condition of the vehicle's electrical system, as well as a buzzer that sounds when voltage exceeds 16 to 17 V. (Excessive voltage might be caused by a defective voltage regulator, which could damage an alternator or other part of an electrical system.)

In addition to providing an early warning if a battery is being rapidly discharged, MESMAT audibly indicates when headlights are still on and the ignition is switched off. Furthermore, it can act as a continuity tester for checking fuses, bulbs, etc.

**How It Works.** MESMAT operates from a circuit board that mounts on

the underside of the car's instrument panel. Five LEDs light up in sequence as the voltage through the electrical system is increased. One or the other of three green LEDs will remain lit over the range of normal voltage. A yellow and a red LED on either end of the display light when the voltage is too low or high, respectively.

As shown in Fig. 1, MESMAT is built around two integrated circuits—the LM3914 dot/bar graph display driver and the LM324N low-power quad operational amplifier.

In addition to a large network of voltage comparators, the LM3914 contains its own adjustable reference and accurate 10-step voltage divider. In MESMAT, the LM3914 is used in its dot-display mode and some of its outputs are connected together so that it drives four different LEDs instead of the normal 10.

As shown in Fig. 1, *R1* is adjusted so that pin 6 of *IC1* is 1.1 volts more positive than pin 4. (These pins are basically reference voltages.) The voltage (with respect to ground) at pin 6 determines the point when *IC1*'s LED No. 10 output turns on (this output sinks current when it turns on). The voltage at pin 4 plus 0.11 volt determines the point when *IC1* turns

on its LED No. 1 output. The other *IC1* outputs turn on at voltages between that at pin 4 and pin 6. The input at pin 5 is equal to approximately one-third that of the positive supply. Note that the positive supply is connected to the *R13/R5* voltage divider. When the voltage at pin 5 exceeds that at pin 4 plus 0.11 volt, *IC1* turns on its LED No. 1 output. When the voltage at pin 5 exceeds that at pin 6, *IC1* turns on its LED No. 10 output. The outputs at LED No. 2 through LED No. 9, of *IC1*, turn on when the voltage at pin 5 is somewhere between that at pin 4 and pin 6.

In this particular device, 10 different LEDs indicating 10 different voltage levels are unnecessary (and may actually be more confusing than helpful), so several of the outputs are connected to the cathode of a single LED. This is possible since *IC1* uses open-collector outputs. Since outputs on LED Nos. 1, 2, and 3 are all connected to the cathode of *LED2*, this LED is lit whenever these outputs are turned on. This occurs only when the applied vehicle voltage is between about 12 and 13.1 volts. *IC1*'s LED No. 4 through LED No. 6 outputs are connected together so *LED3* stays lit in the 13.1-to-14.2-volt range. The outputs on LED No. 7 through LED No. 9 are tied together, which means *LED4* stays lit in the 14.2-to-15.3-volt range. The LED No. 10 output is connected to the cathode of *LED5*, which lights when the supply voltage exceeds about 15.3 volts. The LM3914 has a small amount of over-lap between segments to ensure that all LEDs are not off at once.

Op amps *IC2A*, *IC2B*, and *IC2D* are used as voltage comparators. The



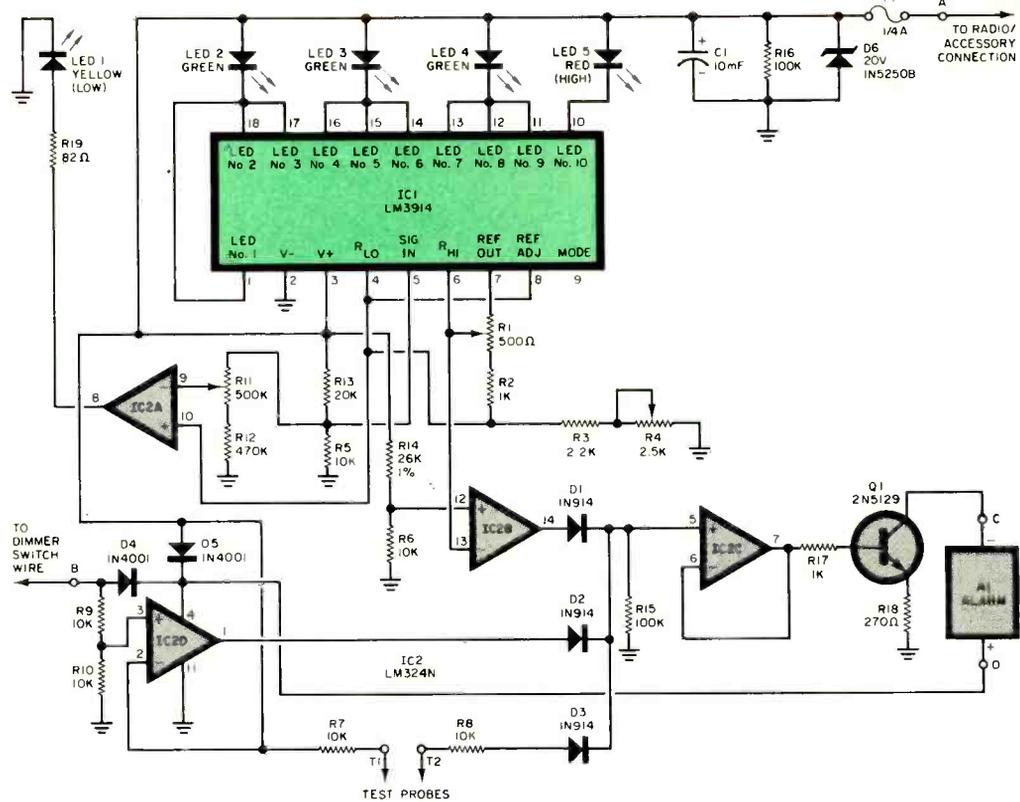


Fig. 1. There are two ICs in the circuit: a display driver and a quad op amp.

**PARTS LIST**

- A1—6-V minibuzzer
- C1—10- $\mu$ F, 25-V electrolytic
- D1,D2,D3—1N914 or equal silicon diode
- D4,D5—1N4001 or equal silicon rectifier
- D6—1N5250B or equal 20-V, 500-mW zener diode
- F1— $\frac{1}{4}$ -A fuse
- IC1—LM3914N dot/bar graph display driver
- IC2—LM324N quad op amp
- LED1—Yellow LED
- LED2, LED3, LED4—Green LED
- LED5—Red LED
- Q1—2N5129 or equal general-purpose npn transistor

- R1—500- $\Omega$  pc trimmer potentiometer
- R2,R17—1-k $\Omega$ ,  $\frac{1}{4}$ -W, 10% resistor
- R3—2.2-k $\Omega$ ,  $\frac{1}{4}$ -W, 10% resistor
- R4—2.5-k $\Omega$ , pc trimmer potentiometer
- R5,R6—10-k $\Omega$ ,  $\frac{1}{4}$ -W, 1% resistor
- R7,R8,R9,R10—10-k $\Omega$ ,  $\frac{1}{4}$ -W, 10% resistor
- R11—500-k $\Omega$ , pc trimmer potentiometer
- R12—470-k $\Omega$ ,  $\frac{1}{4}$ -W, 10% resistor
- R13—20-k $\Omega$ ,  $\frac{1}{4}$ -W, 1% resistor
- R14—26-k $\Omega$ ,  $\frac{1}{4}$ -W, 1% resistor
- R15,R16—100-k $\Omega$ ,  $\frac{1}{4}$ -W, 10% resistor
- R18—270- $\Omega$ ,  $\frac{1}{4}$ -W, 10% resistor
- R19—82- $\Omega$ ,  $\frac{1}{4}$ -W, 10% resistor

Misc.—Printed circuit board, fuse holder, alligator clips, wire, acrylic panel, small right-angle brackets, screws, solder, etc.

**Note:** A complete kit of parts, including the circuit board but not the front panel, is available from **MAGICLAND**, 4380 South Gordon Ave., Fremont, MI 49412 for \$27.50 postpaid in the United States. Also available separately from **Danocinths, Inc.**, PO Box 261, Westland, MI 48185; pc board for \$7.00 postpaid. Ask for part number **F402**. Michigan residents, add 4% sales tax.

noninverting input of IC2A is connected to pin 4 of IC1 and its inverting input to voltage divider R11/R12. The inverting input of IC2A is at a potential slightly less than one-third that of the supply. Trimmer R11 is adjusted so that LED1 (which alerts the driver to the presence of an abnormally low voltage in the electrical system), just lights when the supply voltage drops below the value that turns on LED2, which is about 12 volts. This is the same point at which the potential at the inverting input of IC2A drops below the potential at pin 4 of IC1. Notice that the R11/R12 voltage divider is required since, if the inverting

input of IC2A were connected directly to pin 5 of IC1 (which, at first glance, would seem the thing to do), IC2A wouldn't turn on until the supply voltage dropped below 11.67 volts. Also remember that IC1's LED No. 1 output doesn't turn on until the supply voltage exceeds approximately 12 volts. The output of IC2B is connected, through D1, to the noninverting input of IC2C, which functions as a buffer. The output of IC2C is connected to the base of Q1, which drives alarm A1. Transistor Q1 also reduces the loading on IC2C, which could cause instability.

Power for IC2, through D4 and D5.

is from two different sources—the radio accessory connection at the vehicle fuse block which leads to point A on the circuit board, and a headlight dimmer switch connection which leads to point B. The radio accessory connection is "hot" only when the ignition switch is on, and the dimmer switch connection is hot only when the headlights are on. Thus, IC2 is powered when either (or both) the ignition switch or the headlights are on. Due to R9 and R10, IC2D's noninverting input has a potential of one-half the battery voltage when the headlights are on, and 0 volt when they are off. The inverting input to IC2D is equal

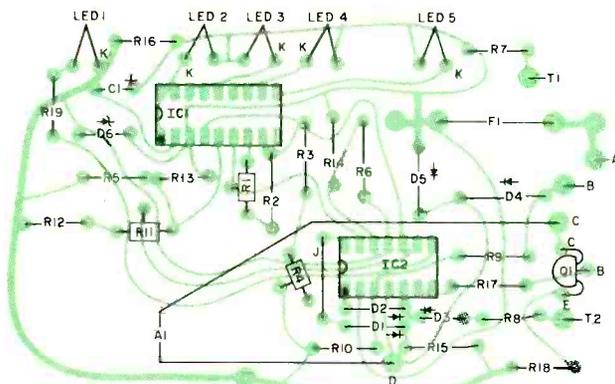
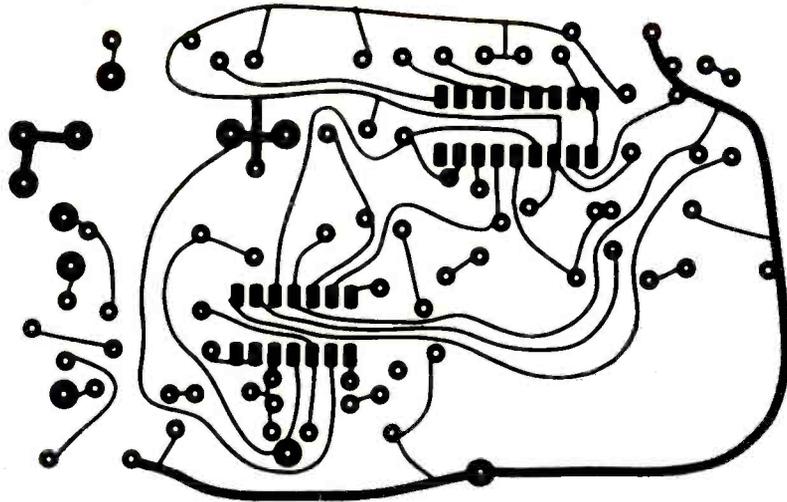


Fig. 2. Exact-size foil pattern (top) and component layout. Use 1/8" drill bit for holes marked with an "X."

to the battery voltage when the ignition switch is on and is at zero otherwise. Thus, when the headlights are on but the ignition is off, IC2D switches on, which, in turn (through D2, IC2C, and Q1), turns on the alarm.

The theory behind the operation of the audible continuity tester portion of MESMAT is simple. When a sufficiently low resistance exists between points T1 and T2, the positive voltage from T1 is applied to pin 5 of buffer IC2C through D3. The buffer, in turn, provides a current to the base of Q1, which then provides sufficient current to operate A1. Fuse F1 maintains the auto's electrical system reliability and zener diode D6 increases MESMAT's reliability.

**Construction.** The circuit can be assembled on a pc board such as that shown in Fig. 2. Connect A1's black (negative) lead to point C on the circuit board and its red (positive) lead to point D. For use as an audible continuity tester, connect short lengths of flexible wire, with an alligator clip at

one end, to points T1 and T2 on the circuit board.

While the circuit board can be mounted in any small enclosure, the prototype had only a colored acrylic front panel, with the bulk of the circuit board exposed. Holes in the front panel were drilled for the LEDs, and two small right-angle brackets were used to attach the front panel to the circuit board. The front panel is used only for cosmetic reasons, as the circuit board can be mounted to the auto's instrument panel.

Before applying power, check polarities of the LEDs, diodes, buzzer, and capacitor. Also, check to make sure you have installed the ICs and transistor properly.

**Testing and Adjustment.** A dc power supply, continuously adjustable from 0 to 18 volts or more, and a dc voltmeter are required. (The current requirements of the supply are less than 50 mA.) Set trimmer pots (R1, R4, and R11) to their approximate midpoint. Connect the power supply's ground to the circuit ground and the

supply's positive terminal to point A on the circuit board.

Set the voltmeter to measure 1.2 volts and connect its negative lead to pin 4 of IC1 and positive lead to pin 6. Adjust R1 for a 1.1-volt meter indication. Then set the voltmeter so it will be able to measure 20 volts and connect its negative lead to ground and its positive lead to point A. Adjust the power supply until the meter indicates 12.0 volts, and adjust R4 until LED2 just barely goes off. (Make sure that LED3 doesn't glow when LED2 is not illuminated.)

For the final adjustment, leave the power supply set at 12.0 volts and adjust R11 so that the yellow LED just barely glows.

To check the adjustments, set the power supply at 5 volts, and note that LED1 (yellow) starts to glow, and remains on to 11 volts. At 12.2 volts, LED2 (green) should be lit but LED1 should go off. (Note: As long as the voltage is over 5 volts, at least one of the LEDs should be lit at all times.) As the voltage is increased to 13.3 volts, LED2 should be off and LED3 (green) should glow. At 14.4 volts, only LED4 (green) should glow. At 15.5 volts, LED5 (red) should glow. As the voltage is increased over 17 volts, LED5 should still be lit but the alarm will sound. If necessary, readjust R1, R4 and/or R11. Note that, if you desire the alarm to sound off at a specific voltage of your choosing, replace R14 with a 50,000-ohm potentiometer and adjust it so the alarm sounds off at your specified voltage.

To check the headlight-on reminder circuit, set the supply to 12 volts and disconnect the power supply's positive supply lead from point A on the circuit board and connect it to point B. The alarm should sound off. Temporarily connect a jumper wire from point A to point B. The alarm should immediately be silenced.

To check out the optional continuity tester, short the two alligator clips (T1 and T2) together. The alarm should sound off immediately. Be sure you remove the temporary jumper between points A and B before installation in the vehicle.

**Installation.** The circuit board is designed to be mounted, foil side up, to the underside of the auto's instrument panel. Use two suitable screws (sheet metal screws will usually work) and two nonconducting thick washers or short spacers. Other types of mounting, including recess, can be used with slight modifications. Al-

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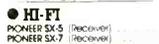
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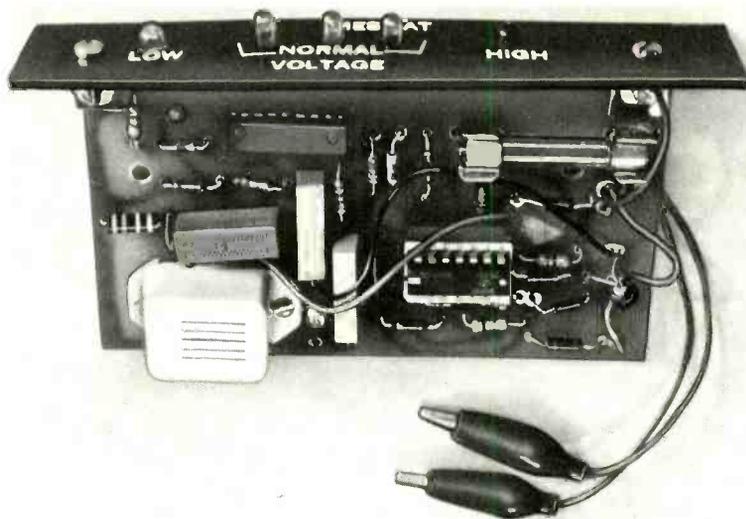
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Internal view of the authors prototype.

though the author's prototype doesn't have a cabinet all that is really visible to the driver is the front panel.

After you have attached the project to the instrument panel, connect a wire from its ground connection to the auto's ground. A nearby screw which threads into a metal chassis or body part is usually satisfactory. Next, connect a wire from point A to the auto's radio or accessory connection at the fuse block. (A connection can be made at the fuse block without soldering by bending a thin solder lug into a semicircle and squeezing it between the fuse and fuseholder.) Use your VOM to make sure this connection is only "hot" when the ignition switch is "on." Note: Whenever you make a connection to a fuse block, always check that the connection is made to the "protected" side—the side that is "dead" when the fuse is removed.

Connect point B on the circuit board to a connection at the dimmer switch that is "hot" whenever the headlight switch is "on." The position of the dimmer switch shouldn't matter. If you are on the ultra-cautious side, add a 1/4-amp in-line fuse in series with the wire to point B on the circuit board.

If you don't like to see the continuity tester's leads dangle when they aren't in use, simply clip the alligators to the bottom of the front panel.

**Use.** When the voltage of the electrical system is within normal limits (about 12 to 15 volts) one of the green LEDs will glow. Sometimes two LEDs glow at one time, but usually, the center or right green LED will be glowing. Under a heavy electrical load (as happens when the headlights,

wipers, fan and electrical defrost are on at the same time) the left green LED may be the only one on. If the yellow LED glows for any extended time, the battery is being discharged and you most likely have some trouble with the alternator or battery. Another possibility is that there is an excessive load due to some short or a malfunction in an electrical part.

When there is little electrical load, it is usually OK for the right green LED and red LED to glow at the same time. However, if the red LED alone stays on for an extended time, you may have voltage-regulator problems. (Immediately after some cold weather starts, the red LED may glow for a few minutes. This is OK.) If the red LED comes on and the alarm sounds off, the voltage in the electrical system of the auto exceeds approximately 16.5 volts, which can cause a multitude of problems with the electrical system—ruined battery, short headlight life, and blown fuses. The difficulty should be determined and repaired as soon as possible.

The headlight-on reminder causes the alarm to sound off if you leave the headlights on and turn the ignition switch off. If you wish to operate the headlights with the engine off and not listen to the alarm, place the ignition switch in the accessory position.

The built-in audible continuity tester can be used to check questionable fuses and bulbs. Simply touch the alligator clips to the appropriate contacts on the fuse or bulb. If the alarm sounds, the part is OK.

In addition to being a worthy accessory for an automobile, MESMAT can be useful in a boat or any other vehicle that uses a 12-V battery. ♦

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BY RANDY CARLSTROM

# DESIGNING WITH THE

# 8080 MICROPROCESSOR

## Part 4: A Typical Program



**P**REVIOUS parts of the article have described how a typical microprocessor works and gave design details for a practical module. Last month, we covered the details of designing software for the system. Here is an example.

**Morse Receiving Program.** What would Samuel F.B. Morse have thought if he had heard his code being sent at 100 words per minute at the same time it was being printed out on a printer or television screen? Actually, the process involved in such a scheme is fairly simple and straight forward (if you happen to have a microprocessor). The procedure involves converting Mr. Morse's combinations of dots and dashes (which may come from a shortwave receiver) into a more modern and usable code called *ASCII* (American Standard Code for Information Interchange). ASCII is a widely-used code in which 96 displayable characters (letters, number, etc.) and 32 non-displayable control characters are each assigned a unique 7-bit code. For instance, the character "A" is represented in the ASCII code as 41<sub>16</sub>; the character "3" is assigned the code 33<sub>16</sub> (see Fig. 22). Many of today's computers communicate with each other using the ASCII code, and ASCII has

even found its way into radio.

The 8080 readily handles the ASCII code since each register and memory location is capable of holding one ASCII character code. Occasionally the unused eighth bit (MSB) is utilized as a *parity bit*, which serves to minimize data errors when large amounts of information are being transferred. (This is the principal use of the Parity flag-related instruc-

tions in the 8080 instruction set.) The parity bit will not concern us in our discussions, however; we will assume it is always 0.

The Morse code is comprised of two types of marks—the dot and dash—and three types of spaces—the mark space, character space, and word space. Theoretically, the length of a dash is three times the length of a dot. Likewise, a

Character	ASCII Code (hex)	Morse Code	Character	ASCII Code (hex)	Morse Code
A	41	• —	0	30	— — — — —
B	42	— • • •	1	31	• — — — —
C	43	— • — •	2	32	• • — — —
D	44	— • •	3	33	• • • — —
E	45	•	4	34	• • • —
F	46	• • — •	5	35	• • • • •
G	47	— — •	6	36	— • • • •
H	48	• • • •	7	37	— — • • •
I	49	• •	8	38	— — — • •
J	4A	• — — —	9	39	— — — — •
K	4B	— • —		2E	• — • — • —
L	4C	• — • •	,	2C	— — • • — —
M	4D	— —	.	2D	— — — • •
N	4E	— •	?	3F	• • • • •
O	4F	— — —	:	3B	— • — — •
P	50	• — — •	;	3A	— — — • •
Q	51	— • • —	(	28	— — — — —
R	52	• — • •	/	2F	— • • — •
S	53	• • • •	-	2D	— • • • —
T	54	—	End of mes-	—	• — — — •
U	55	• • —	sage	—	—
V	56	• • • —	Wait	—	• — • • •
W	57	• — — —	End of work	—	• • • — •
X	58	— • • —	Invitation to	—	— • • — —
Y	59	— • — —	transmit	—	— • • — —
Z	5A	— — • •	Error	—	• • • • •

Fig. 22. The ASCII code for the Morse symbols.

mark space is equal in length to a dot, a character space equal to a dash, and a word space equal to seven dots.

Ideally, a Morse recognition algorithm would test mark and space lengths at the points half-way between mark and space types, e.g., 2/3 of a dash would be the "critical point" for deciding if a received mark was a dot or a dash, etc. However, after much experimentation with these critical points, a slightly modified algorithm was obtained. For instance, it was observed that many users of the code (hams in particular) tend to cut character spaces too short, which would confuse the ideal algorithm. For this reason, the character space critical point was changed from the ideal 2/3 dash to 1/2 dash (a 17% reduction), which enhanced character-recognition probability. A similar change was made for dot recognition for the same reason. Attempting to change or experiment with these critical points in a random-logic implementation of the code converter would be a large chore in itself.

The program has incorporated into it a subroutine for checking some types of noise often encountered while receiving Morse code on a radio receiver. This routine has proven to be quite effective in discriminating between some types of random noise and Morse code. In this respect, the routine can be thought of as a "software noise blanker." The "bandwidth" can be adjusted simply by changing the value of a parameter byte at memory location 010C of the program (the beauty of using a microprocessor). The bandwidth, and therefore the maximum code speed that may be accurately received, is inversely proportional to the numeric value stored in this memory location.

The Morse program assembly listing is shown here. The source program, which appears in the third column, was assembled beginning at memory location 0. The memory locations in the first column are followed by the object code contained, or assembled, into them. The second column lists the line numbers of the source program which are of no particular significance to the program itself.

Throughout the source program are "labels" to the immediate left of some of the instruction mnemonics. These labels represent relative addresses which are later assigned memory addresses during the assembly process. Labels allow the source program to reference other parts of the program (such as subroutines) without the need of knowing their memory addresses. For example, to Call the subroutine which checks for noise (labeled "VLDMK"), the corresponding source program instruction would be written as CALL VLDMK. VLDMK symbolizes the beginning address in pro-

gram memory of the noise-checking subroutine. A list of all labels used in the Morse program, followed by their computed values and source program line numbers that use them, appears at the end of the assembly listing.

When assembling a source program by hand, the numeric values of any labels used must be determined before the corresponding machine language instructions can be completely assembled (labels will usually complete an instruction's operand, such as an address). In the case of the VLDMK label, its address was determined to be 0104<sub>16</sub> after counting the number of bytes that preceded it in memory. So, in this case, "CALL VLDMK" and "CALL 0104H" are equivalent instructions and may be used interchangeably in the

source program. It is more convenient, however, to use the symbolic label representation since the memory address may not even be known until the source program is completed. For this reason, and the fact that program changes are almost inevitable in the course of writing new programs (which will most likely change all the memory addresses of the instructions following the program changes), it is recommended that calculating memory addresses be one of the last steps performed when writing new programs. Using labels freely throughout the source program will help avoid some of the tedious work when program changes have to be made.

The next part of this article will cover the hardware implementation of the CPU module. ◇

### MORSE-TO-ASCII CONVERSION PROGRAM

```

0000          0100  ORG 0000
0000          0105  *
0000          0110  LEN EQU 64          TERMINAL WIDTH
0000          0115  CW EQU 0FCH        MORSE INPUT PORT
0000          0120  MASK EQU 01        DATA INPUT BIT 0 HAS MORSE
0000          0125  PSTN EQU 0BFFH     LOCATE PSTN BYTE AT END OF RAM AREA
0000          0130  *
0000          0135  * * * * *
0000          0140  *
0000          0145  * * * MORSE-TO-ASCII CONVERSION PROGRAM * * *
0000          0150  *
0000          0155  * WRITTEN BY *
0000          0160  *
0000          0165  * * * RANDY CARLSTROM, 9/78 * * *
0000          0170  *
0000          0175  * * * * *
0000          0180  *
0000          0185  * * * MORSE RECOGNITION ALGORITHM * * *
0000          0190  *
0000          0195  * IF THE NEW MARK LENGTH IS GREATER THAN OR
0000          0200  * EQUAL TO TWICE THE LENGTH OF THE LAST MARK, THE
0000          0205  * NEW MARK IS STORED AS A DASH. IF THE NEW MARK
0000          0210  * LENGTH IS LESS THAN ONE-HALF THE LENGTH OF THE
0000          0215  * LAST MARK, THE NEW MARK IS STORED AS A DOT. IF
0000          0220  * THE NEW MARK DOES NOT FALL INTO EITHER ONE OF
0000          0225  * THESE CATEGORIES, THE NEW MARK IS STORED AS THE
0000          0230  * SAME TYPE AS THE LAST MARK.
0000          0235  * IF A SPACE LENGTH REACHES ONE-HALF THE
0000          0240  * LENGTH OF THE LAST DASH, A CHARACTER SPACE HAS
0000          0245  * BEEN RECEIVED AND THE ELEMENTS ARE ASSEMBLED AND
0000          0250  * CONVERTED TO THE CORRESPONDING ASCII CHARACTER.
0000          0255  * THE CHARACTER IS THEN PRINTED AT THE OUTPUT
0000          0260  * PERIPHERAL AND SPACE MEASUREMENT CONTINUES.
0000          0265  * IF THE SPACE LENGTH REACHES THREE-HALVES THE
0000          0270  * LENGTH OF THE LAST DASH, A WORD SPACE HAS BEEN
0000          0275  * RECEIVED AND THE SPACE IS PRINTED.
0000          0280  *
0000          0285  * * * * * REGISTER ALLOCATION * * * * *
0000          0290  *
0000          0295  * B = DASH REG. C = DOT REG. D = LAST MARK LENGTH
0000          0300  * E = LAST MARK TYPE: 00 = DOT, 01 = DASH
0000          0305  * H = MARK/SPACE COUNTER L = LAST DASH LENGTH
0000          0310  *
0000          0315  * * * * * * * * * * * * * * * * * * * * * *
0000          0320  *
0000          0325  DI          DISABLE INTERRUPTS
0000          0330          LXI SP,0BFDH  INITIALIZE STACK POINTER IN RAM
0000          0335          CALL CRLF    START A NEW OUTPUT LINE
0000          0340          LXI H,0040H  LAST DASH LENGTH = 64; COUNTER = 0
0000          0345          LXI D,2000H  LAST MARK LENGTH = 32; LAST MARK = DOT
0000          0350          LXI B,0000   CLEAR DOT AND DASH REGISTERS
0000          0355  *
0000          0360  * * * * *
0000          0365  *
0000          0370  * * * * * * * * * * * * * * * * * * * * * *
0000          0375  *
0000          0380  WAIT  CALL VLDMK   IS THERE A VALID MARK YET?
0000          0385          JC WAIT    NO: WAIT
0000          0390  NEWMK CALL MKTYP  YES: FIND MARK TYPE AND STORE IT
0000          0395          MVI H,00    CLEAR MARK/SPACE COUNTER
0000          0400  SPTYP CALL VLDMK   IS THERE A VALID MARK YET?
0000          0405          JNC NEWMK  YES: PROCESS NEW MARK
0000          0410          ORA A      NO: CLEAR CY FLAG AND
0000          0415          MOV A,H    CHECK SPACE LENGTH
0000          0420          RAL      REG. A = 2 * CURRENT SPACE LENGTH
0000          0425          CMP L     SPACE >= 1/2 LAST DASH? (CHAR. -SPACE)
0000          0430          JC SPTYP  NO: CONTINUE MEASURING SPACE LENGTH
0000          0435          CALL CONV  YES: CONVERT MORSE TO ASCII

```

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## 8080 microprocessor

```

002B CD BB 00      0440      CALL OUT      AND PRINT IT, THEN
002E CD DB 00      0445      CALL IPSTN    UPDATE PRINTER POSITION
0031 FE 3E          0450      CPI LEN-2     DO A CR/LF IF AT END OF LINE
0033 D4 AC 00      0455      CNC CRLF
0036 CD 04 01      0460 GENSP CALL VLDHK IS THERE A VALID MARK YET?
0039 D2 16 00      0465      JNC NEMMK     YES: PROCESS NEW MARK
003C B7            0470      ORA A         NO: CLEAR CY FLAG AND
003D 7D            0475      MOV A,L      CHECK SPACE LENGTH
003F 1F            0480      RAR
003F 85            0485      ADD L
0040 3D            0490      DCR A
0041 BC            0495      CMP H
0042 D2 36 00      0500      JNC GENSP
0045 CD E3 00      0505      CALL SPOUT
0048 C3 10 00      0510      JMP WAIT
004B              0515      *
004B              0520      *
004B              0525      *
004B              0530      *
004B              0535      *
004B              0540      *
004B              0545      *
004B              0550      *
004B              0555      *
004B              0560      *
004B              0565      *
004B              0570      *
004B 7A            0575 MKTYP MOV A,D
004C 17            0580      RAL          REG. A = 2 * LAST MARK LENGTH
004D 3D            0585      DCR A
004E BC            0590      CMP H
004F D2 5F 00      0595      JNC DOT     MARK >= 2 * LAST MARK LENGTH? (DASH)
0052 54            0600 DASH MOV D,H NO: GO SEE IF A DOT
0053 6C            0605      MOV L,H     YES: UPDATE LAST MARK LENGTH REGISTER
0054 1E 01         0610      MVI E,01   AND LAST DASH LENGTH REGISTER
0056 78            0615      MOV A,B     UPDATE LAST MARK TYPE REGISTER
0057 07            0620      RLC        SHIFT A "1" INTO DASH REGISTER
0058 F6 01         0625      ORI 01
005A 47            0630      MOV B,A
005B 79            0635      MOV A,C
005C 07            0640      RLC        SHIFT A "0" INTO DOT REGISTER
005D 4F            0645      MOV C,A
005E C9            0650      RET
005F 7C            0655 DOT MOV A,H A DASH WAS RECEIVED AND STORED; RETURN
0060 17            0660      RAL
0061 3D            0665      DCR A
0062 BA            0670      CMP D
0063 D2 72 00      0675      JNC SAME
0066 54            0680 DOTT MOV D,H IS MARK < 1/2 LAST MARK LENGTH? (DOT)
0067 1E 00         0685      MVI E,00   NO: WAS THE SAME AS LAST MARK
0069 79            0690      MOV A,C     YES: UPDATE LAST MARK LENGTH REG.
006A 07            0695      RLC        AND LAST MARK TYPE REGISTER
006B F6 01         0700      ORI 01     SHIFT A "0" INTO DASH REGISTER
006D 4F            0705      MOV C,A
006E 78            0710      MOV A,B
006F 07            0715      RLC
0070 47            0720      MOV B,A
0071 C9            0725      RET
0072 7B            0730 SAME MOV A,E A DOT WAS RECEIVED AND STORED; RETURN
0073 1F            0735      RAR        GET LAST MARK TYPE
0074 DA 52 00      0740      JC DASH    WAS IT A DASH?
0077 C3 66 00      0745      JMP DOTT   YES: GO TO DASH ROUTINE
007A              0750      *        NO: GO TO DOT ROUTINE
007A              0755      *
007A              0760      *
007A              0765      *
007A              0770      *
007A              0775      *
007A              0780      *
007A              0785      *
007A              0790      *
007A              0795      *
007B 07            0800 CONVT MOV A,C GET DOT REGISTER
007C 80            0805      RLC        MULTIPLY IT BY 2
007D 01 00 00      0810      LXI B,0000 AND ADD DASH REGISTER
0080 E5            0815      PUSH H     READY DOT AND DASH REG. FOR NEXT CHAR.
0081 21 19 01      0820      LXI H,TABLE REG. A NOW HAS UNIQUE CODE
0084 BE            0825 NEXT CMP H     POINT TO TABLE STARTING ADDRESS
0085 CA 94 00      0830      JZ ASCII  FOUND CHARACTER?
0088 F5            0835      PUSH PSW  YES: CONVERT TO ASCII
0089 7E            0840      MOV A,H   NO: SAVE UNIQUE CODE AND
008A B7            0845      ORA A     CHECK FOR END OF TABLE
008B CA 98 00      0850      JZ ERROR  END OF TABLE?
008E F1            0855      POP PSW   YES: NO MATCH WAS FOUND
008F 23            0860      INX H     NO: GET UNIQUE CODE BACK AND
0090 23            0865      INX H     BUMP REG. PAIR H TO NEXT
0091 C3 84 00      0870      JMP NEXT  CHARACTER ADDRESS
0094 23            0875 ASCII INX H AND TRY AGAIN
0095 7E            0880      MOV A,H  POINT TO ASCII CHAR. IN TABLE
0096 E1            0885      POP H    AND PUT IT IN REG. A
0097 C9            0890      RET
0098 F1            0895 ERROR POP PSW RETURN WITH IT
0099 E1            0900      POP H    ELSE RETURN WITH A "*"
009A 3F 2A         0905      MVI A,"*" IF NO MATCH WAS FOUND
009C C9            0910      RET
009D              0915      *
009D              0920      *
009D              0925      *
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# 8080 microprocessor

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009D          0965 * A MARK MEASUREMENT, OR THE MORSE ALGORITHM WILL FAIL.
009D          0970 * INCREASING THE VALUE OF REGISTER PAIR B INCREASES THE
009D          0975 * LENGTH OF THE TIME DELAY, THEREBY DECREASING THE FINAL
009D          0980 * COUNT OF REG. H FOR A GIVEN MARK OR SPACE LENGTH.
009D          0985 *
009D C5       0990 DLY1  PUSH B
009E 06 03    0995      MVI B,03      PRESET VALUE FOR REG. B
00A0 0E 40    1000      MVI C,40H     PRESET VALUE FOR REG. C
00A2 0D       1005 LOOP1 DCR C        KILL SOME TIME...
00A3 C2 A2 00 1010      JNZ LOOP1
00A6 05       1015      DCR B
00A7 C2 A2 00 1020      JNZ LOOP1
00AA C1       1025      POP B
00AB C9       1030      RET
00AC          1035 *
00AC          1040 *
00AC          1045 * CRLF OUTPUTS A CARRIAGE-RETURN LINE-FEED WHEN CALLED.
00AC          1050 *
00AC 3E 0D    1055 CRLF  MVI A,0DH     OUTPUT A CR
00AE CD BB 00 1060      CALL OUT
00B1 3E 0A    1065      MVI A,0AH     OUTPUT A LF
00B3 CD BB 00 1070      CALL OUT
00B6 AF       1075      XRA A        SET PRINT POSITION COUNTER TO 0
00B7 32 FF 0B 1080      STA PSTN
00BA C9       1085      RET
00BB          1090 *
00BB          1095 *
00BB          1100 * OUT SENDS THE CHAR. TO BE PRINTED TO THE OUTPUT
00BB          1105 * DEVICE (CRT, PRINTER, ETC.). THERE IS RESERVED
00BB          1110 * STORAGE SPACE HERE FOR THE USER'S OWN OUTPUT ROUTINE.
00BB          1115 *
00BB C5       1120 OUT  PUSH B        MY OUTPUT DRIVER NEEDS THE CHAR.
00BC 47       1125      MOV B,A      IN REG. B
00BD CD 19 C0 1130      CALL 0C019H CALL MY MONITOR'S OUTPUT DRIVER
00C0 C1       1135      POP B      RESTORE REG. B
00C1 C9       1140      RET      AND WE'RE DONE!
00C2          1145 DS 4      HERE'S MORE STORAGE AREA FOR THOSE
00C6          1150 DS 4      LONGER OUTPUT ROUTINES...
00CA          1155 DS 4      THE CHAR. TO BE PRINTED IS IN REG. A
00CE          1160 DS 4      AND THE ROUTINE MUST END WITH A "RET."
00D2          1165 DS 4      ALL REGISTERS MUST BE UNALTERED UPON
00D6          1170 DS 5      RETURN, WITH THE EXCEPTION OF REG. A.
00DB          1175 *
00DB          1180 *
00DB          1185 * IPSTN INCREMENTS THE PRINT POSITION COUNTER ONCE
00DB          1190 * EACH TIME IT IS CALLED.
00DB          1195 *
00DB 3A FF 0B 1200 IPSTN LDA PSTN   BUMP PRINT POSITION ONE COUNT
00DE 3C       1205      INR A
00DF 32 FF 0B 1210      STA PSTN
00E2 C9       1215      RET
00E3          1220 *
00E3          1225 *
00E3          1230 * SPOUT OUTPUTS EITHER A SPACE OR A CR/LF, DEPENDING
00E3          1235 * ON IF WE ARE GETTING CLOSE TO THE END OF A LINE.
00E3          1240 * IN THIS WAY WORDS ARE NOT BROKEN BETWEEN LINES
00E3          1245 * OF OUTPUT.
00E3          1250 *
00E3 3A FF 0B 1255 SPOUT LDA PSTN   GET PRINT POSITION
00E6 FE 36    1260      CPI LEN-10  ARE WE NEAR END OF LINE?
00E8 DA EF 00 1265      JC PRSP    NO: CONTINUE ON SAME LINE
00EB CD AC 00 1270      CALL CRLF  YES: START A NEW LINE
00EE C9       1275      RET
00EF 3E 20    1280 PRSP  MVI A,' '    PRINT A SPACE
00F1 CD BB 00 1285      CALL OUT
00F4 CD DB 00 1290      CALL IPSTN UPDATE PRINT POSITION
00F7 C9       1295      RET
00F8          1300 *
00F8          1305 *
00F8          1310 * MKLEN MEASURES THE LENGTH OF A MARK. THE LENGTH
00F8          1315 * IS RETURNED IN REG. H, AND SHOULD NEVER BE
00F8          1320 * ALLOWED TO BE GREATER THAN 7FH (SEE DLY1).
00F8          1325 * REG. H IS INCREMENTED BY ONE IF A MARK IS NOT
00F8          1330 * PRESENT (SPACE) WHEN THIS ROUTINE IS CALLED.
00F8          1335 *
00F8 24       1340 MKLEN INR H      BUMP MARK LENGTH COUNT
00F9 CD 9D 00 1345      CALL DLY1
00FC DB FC    1350      IN CW
00FE E6 01    1355      ANI MASK   END OF MARK?
0100 C2 FE 00 1360      JNZ MKLEN  NO: BUMP COUNT AGAIN
0103 C9       1365      RET      YES: RETURN WITH MARK LENGTH
0104          1370 *
0104          1375 *
0104          1380 * VLDHK CHECKS THE VALIDITY OF A MARK. ANY MARK LENGTH
0104          1385 * LESS THAN 6 IS CONSIDERED NOISE, THE NOISE LENGTH
0104          1390 * BEING ADDED TO THE PRESENT SPACE LENGTH BEFORE RETURN.
0104          1395 * OTHERWISE THE VALID MARK LENGTH IS RETURNED IN REG. H.
0104          1400 * THE CY FLAG IS CLEARED UPON RETURN IF A VALID MARK
0104          1405 * WAS RECEIVED; OTHERWISE IT IS SET.
0104          1410 *
0104 E5       1415 VLDHK PUSH H      SAVE SPACE LENGTH TEMPORARILY
0105 26 00    1420      MVI H,00   SET UP TEMPORARY MARK COUNTER
0107 CD F8 00 1425      CALL MKLEN MEASURE MARK LENGTH
010A 7C       1430      MOV A,H
010B FE 06    1435      CPI 06
010D DA 13 01 1440      JC SPACE  VALID MARK? (BANDWIDTH IS SET HERE)
0110 33       1445      INX SP    NO: ADD MARK LENGTH TO SPACE LENGTH
0111 33       1450      INX SP    YES: NEW MARK; DON'T WANT OLD
0112 C9       1455      RET      SPACE LENGTH ON STACK
0113 7C       1460 SPACE: MOV A,H    RETURN WITH NEW MARK LENGTH AND CY = 0
0114 E1       1465      POP H    ADD INVALID MARK LENGTH TO
0115 84       1470      ADD H     SPACE LENGTH
0116 67       1475      MOV H,A   REG. H HAS ADJUSTED SPACE LENGTH

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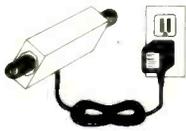
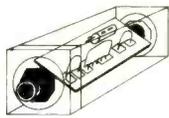
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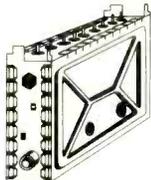
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0118	C9	1485	RET
0119		1490	*
0119		1495	*
0119	05	1500	TABLE DB 05H
011A	41	1505	DB 'A'
011B	16	1510	DB 16H
011C	42	1515	DB 'B'
011D	14	1520	DB 14H
011E	43	1525	DB 'C'
011F	0A	1530	DB 0AH
0120	44	1535	DB 'D'
0121	02	1540	DB 02H
0122	45	1545	DB 'E'
0123	1C	1550	DB 1CH
0124	46	1555	DB 'F'
0125	08	1560	DB 08H
0126	47	1565	DB 'G'
0127	1E	1570	DB 1EH
0128	48	1575	DB 'H'
0129	06	1580	DB 06H
012A	49	1585	DB 'I'
012B	17	1590	DB 17H
012C	4A	1595	DB 'J'
012D	09	1600	DB 09H
012E	4D	1605	DB 'K'
012F	1A	1610	DB 1AH
0130	4C	1615	DB 'L'
0131	03	1620	DB 03H
0132	4D	1625	DB 'M'
0133	04	1630	DB 04H
0134	4E	1635	DB 'N'
0135	07	1640	DB 07H
0136	4F	1645	DB 'O'
0137	18	1650	DB 18H
0138	50	1655	DB 'P'
0139	11	1660	DB 11H
013A	51	1665	DB 'Q'
013B	0C	1670	DB 0CH
013C	52	1675	DB 'R'
013D	0E	1680	DB 0EH
013E	53	1685	DB 'S'
013F	01	1690	DB 01H
0140	54	1695	DB 'T'
0141	0D	1700	DB 0DH
0142	55	1705	DB 'U'
0143	1D	1710	DB 1DH
0144	56	1715	DB 'V'
0145	0B	1720	DB 0BH
0146	57	1725	DB 'W'
0147	15	1730	DB 15H
0148	58	1735	DB 'X'
0149	13	1740	DB 13H
014A	59	1745	DB 'Y'
014B	12	1750	DB 12H
014C	5A	1755	DB 'Z'
014D	1F	1760	DB 1FH
014E	30	1765	DB '0'
014F	2F	1770	DB 2FH
0150	31	1775	DB '1'
0151	37	1780	DB 37H
0152	32	1785	DB '2'
0153	3B	1790	DB 3BH
0154	33	1795	DB '3'
0155	3D	1800	DB 3DH
0156	34	1805	DB '4'
0157	3E	1810	DB 3EH
0158	35	1815	DB '5'
0159	2E	1820	DB 2EH
015A	36	1825	DB '6'
015B	26	1830	DB 26H
015C	37	1835	DB '7'
015D	22	1840	DB 22H
015E	38	1845	DB '8'
015F	20	1850	DB 20H
0160	39	1855	DB '9'
0161	51	1860	DB 51H
0162	28	1865	DB '0'
0163	72	1870	DB 72H
0164	3F	1875	DB '2'
0165	2C	1880	DB 2CH
0166	2F	1885	DB '/'
0167	69	1890	DB 69H
0168	2E	1895	DB '2'
0169	4B	1900	DB 4BH
016A	2C	1905	DB '2'
016B	46	1910	DB 46H
016C	3A	1915	DB '3'
016D	54	1920	DB 54H
016E	3B	1925	DB '3'
016F	2D	1930	DB 2DH
0170	2D	1935	DB '2'
0171	FF	1940	DB 0FFH
0172	5C	1945	DB '5'
0173	34	1950	DB 34H
0174	3C	1955	DB '3'
0175	28	1960	DB 28H
0176	23	1965	DB '2'
0177	36	1970	DB 36H
0178	25	1975	DB '2'
0179	79	1980	DB 79H
017A	3E	1985	DB '3'
017B	00	1990	DB 00

SET INVALID MARK FLAG (CY = 1)  
AND RETURN WITH NEW SPACE LENGTH

ASCII	0094	0830
CONVT	007A	0435
CRLF	00AC	0335 0455 1270
CW	00FC	1350
DASH	0052	0740
DLY1	009D	1345
DOT	005F	0595
DOTT	0066	0745
ERROR	0098	0850
GENSP	0036	0500
IPSTN	00DB	0445 1290
LEN	0040	0450 1260
LOOP1	00A2	1010 1020
MASK	0001	1355
MKLEN	00F8	1360 1425
MKTPP	004B	0390
NEWMK	0016	0405 0465
NEXT	0084	0870
OUT	00BB	0440 1060 1070 1285
PRSP	00EF	1265
PSTN	00FF	1080 1200 1210 1255
SAME	0072	0675
SPACE	0113	1440
SPOUT	00E3	0505
SPTYP	001B	0430
TABLE	0119	0820
VLDMK	0104	0380 0400 0460
WAIT	0010	0385 0510

ERROR  
END OF MESSAGE  
EXCLUSIVE INVITATION TO TRANSMIT  
WAIT  
END OF WORK  
END-OF-TABLE FLAG

BY FRED BLECHMAN  
AND DAVID McDONALD

*An electronic replacement  
for the old mechanical  
music timer*

The mechanical metronome, reputedly invented by Maelzel in the 19th century, has been a familiar sight around musicians and music students for many years. It uses a wind-up clock mechanism to swing a weighted arm, generating a series of clicks as the escapement gears make contact. The clicking rate is conventionally adjustable from 40 to 210 beats per minute by positioning the weight on the calibrated oscillating arm to change the moment of inertia and the rate of the swing.

Redoubtable though it may be, Maelzel's brainchild suffers from defects common to all mechanical devices: wear, drift of calibration, and the need for fairly frequent maintenance. In addition, it must be wound often. A battery-operated, solid-state

electronic design, such as the LED Pendulum Metronome described here, circumvents or alleviates the problems of the mechanical metronome. It is stable in calibration and reliable.

Partly for nostalgic reasons, the pendulum movement of the mechanical metronome is simulated in the project as a flashing sequence of LEDs arranged in an arc. (A click from a loudspeaker occurs as the LED at either end of the string fires.) However, the LEDs offer the user the option of "reading" the metronome signal visually in circumstances where a click might be inaudible or objectionable to the user.

**Circuit Operation.** The "beats" are generated by *IC1*, which is used as an

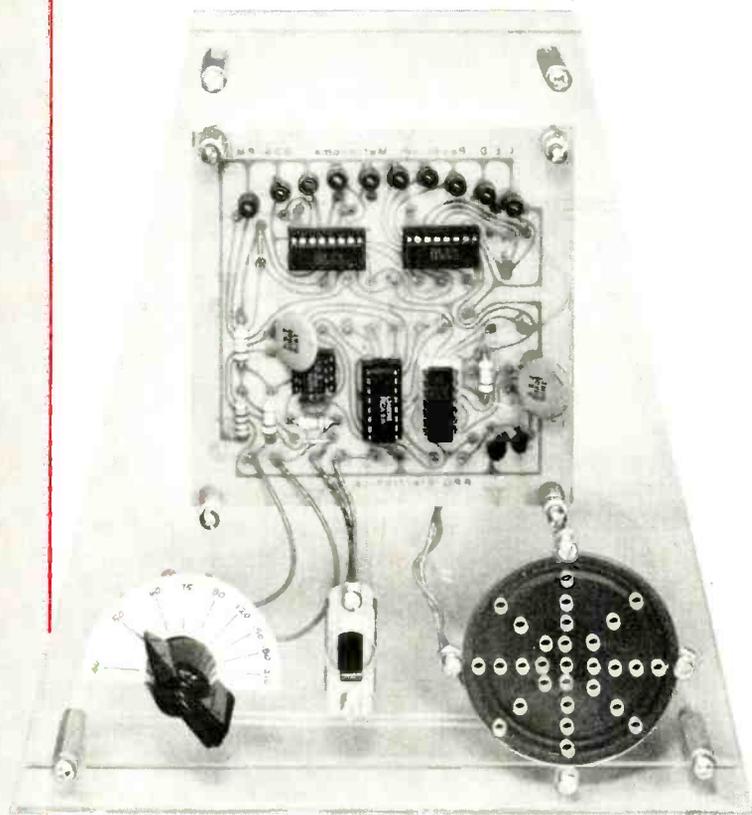
# A LED PENDULUM METRONOME

oscillator (Fig. 1). Resistors *R1*, *R3*, and capacitor *C1* limit the frequency of operation that can be set by means of potentiometer *R2*. Capacitor *C2* decouples the *IC1* modulation input. Each cycle of operation of *IC1* results in a positive-going pulse at pin 3, which is fed to the clock input of up-down counter *IC4*. This counter can be set to count from 0 to 9 (10 counts) or 0 to 15 (16 counts), depending on the status of pin 9. With pin 9 positive (as shown), *IC4* counts from 0 to 15. Counting up or down is controlled by pin 10; positive for up-counting, ground for down-counting. The A, B, C, and D outputs of *IC4* (pins 6, 11, 14, and 2) go positive in a 4-bit binary sequence with the D output (pin 2) low during counts 0 to 7 and high during counts 8 to 15.

Both *IC2* and *IC3* are identical 1-of-8 switches. Depending on the 3-bit binary input, one of eight outputs is connected to pin 3 through a low resistance (typically 120 ohms). This is called the "on" condition for this pin. The A, B, and C inputs to *IC2* and *IC3* (pins 11, 10, and 9) are addressed by the output pins (6, 11 and 14, respectively) of *IC4*. However, *IC2* or *IC3* must be enabled by a low on pin 6. For counts 0 to 7, pin 2 of *IC4* is low, enabling *IC2*. Notice, however, that pin 6 of *IC3* is high because of *IC5A*, one section of a quad 2-input NAND gate, wired as an inverter. This disables *IC3* while *IC2* is enabled.

As *IC4* counts from 0 to 7, the outputs of *IC2* are turned "on" in sequence. In this case, on is *not* ground, but is an internal low resistance to pin 3, which is grounded. This low resistance to ground allows the LED connected to an on pin to glow.

Only five LEDs are connected to the eight outputs, with *LED1* connected to three outputs, *LED2* to two outputs, and *LED3*, *LED4*, and *LED5* to one output each. This is done to simulate the swinging motion of a pendu-



lum, which is fastest near the center but slows down near each end of its swing as it finally stops and reverses. By using multiple counts for the LEDs farthest from the center, the apparent motion of the pendulum seems to slow down, stop, and reverse at each end of its swing. This same technique is used for the five LEDs connected to IC3.

As IC4 counts from 0 to 7, only IC2 is enabled, with IC3 cut off. When IC4 reaches the count of 8, the D output at pin 2 goes high. This turns off IC2, but, via the inversion by IC5A, IC3 is enabled, lighting LEDs 6 to 10 in sequence. Therefore, the first eight counts of IC4 are used to command the IC2 outputs, and the next eight counts command IC3 outputs.

Up to this point, IC4 was counting up since its pin 10 was high. This is

controlled by the output of a flip-flop formed by NAND gates IC5B and IC5C. When power is first turned on by closing switch S1, pin 9 of IC5B is pulled high through R6, and pin 5 of IC5C is pulled low by pin 13 of IC2. Pin 4 of IC5C is therefore high since it has a low on at least one input. Pin 4 is connected to IC4 pin 10, so IC4 counts up. The flip-flop holds this high on pin 4 of IC5C, even though the first count changes pin 5 of IC5C to a high. On count number 15 (actually the sixteenth count, if you start at 1 instead of zero), pin 4 of IC3 is switched on (low resistance to ground). This pin is directly connected to pin 9 of IC5B, pulling it low, so output pin 10 of IC5B goes high. This provides a high input to pin 6 of IC5C. Since pin 5 is already high, pin 4 of IC5C goes low to switch IC4 to the

down-counting mode. Even though IC5B pin 9 goes high on the next count, the flip-flop logic keeps pin 4 of IC5 low.

When IC4 gets down to the zero count, pin 13 is turned on, and pin 5 of IC5C is pulled low, making pin 4 of IC5C high, and thus putting IC4 in the up-counting mode. The top count of IC4 then causes the flip-flop to again change the output at pin 4 of IC5C. This flip-flop action keeps occurring at each end of the pendulum, causing it to "swing" back and forth. Capacitors C4 and C5 prevent noise from accidentally changing the up/down mode of IC4.

The clicking sound occurs at each end of the pendulum swing (as counts 0 and 15 are reached) by changing the state of IC5D. Pins 12 and 13 of IC5D are normally high, so pin 11 is in a low

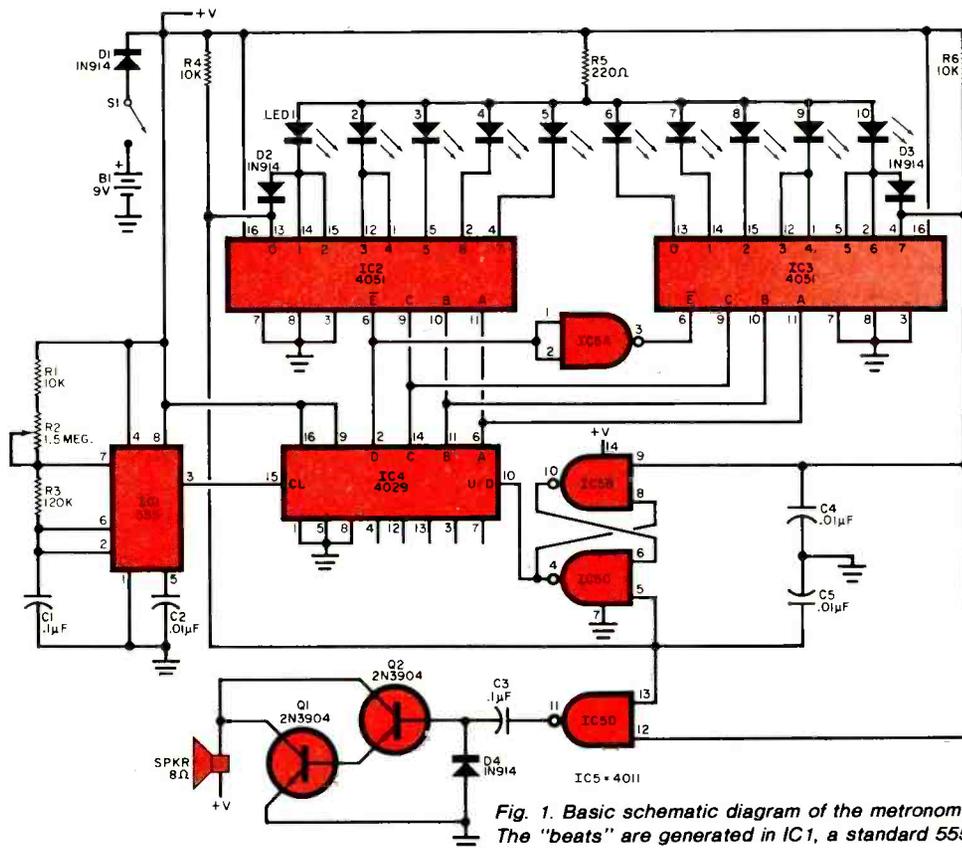


Fig. 1. Basic schematic diagram of the metronome circuit. The "beats" are generated in IC1, a standard 555 timer.

**PARTS LIST**

- B1—6-to-9-volt battery (see text)
- C1,C3—0.1- $\mu$ F disc capacitor
- C2,C4,C5—0.01- $\mu$ F disc capacitor
- D1 through D4—1N914 diode
- IC1—555 timer
- IC2,IC3—4051 1-of-8 switch
- IC4—4029 up down counter
- IC5—4011 quad 2-input NAND
- LED1 through LED10—Jumbo red LED

- Q1,Q2—2N3904 or similar transistor
- R1,R4,R6—10-k $\Omega$ , 1/4-W, 10% resistor
- R2—1.5-M $\Omega$ , liner-taper potentiometer
- R3—120-k $\Omega$ , 1/4-W, 10% resistor
- R5—220- $\Omega$ , 1/4-W, 10% resistor
- S1—Spst switch
- SPKR—Miniature 8- $\Omega$  speaker
- Misc.—IC sockets (optional), battery holder, knob, suitable enclosure, mounting hardware, etc.

**Note:** The following is available from PPG Electronics Co., Inc., Dept. B, 14663 Lanark St., Van Nuys, CA 91402 (213-988-3525): complete kit of parts including pc board (PM-K) at \$14.95. Also available separately: plastic "cabinet" (PM-C) at \$9.95; etched and drilled pc board (PM-B) at \$5.95. Add \$2 shipping and handling. California residents, add 6% sales tax. No foreign orders.

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state. When *IC2* pin 13 or *IC3* pin 4 is pulled low, on counts 0 or 15 respectively, pin 12 or 13 of *IC5D* is made low, and pin 11 of *IC5D*, following NAND logic, goes high. This positive voltage swing charges *C3*, causing a small positive pulse to forward-bias transistors *Q1* and *Q2*, which are arranged in a Darlington circuit. The small current pulse through the series-connected base-emitter circuits of *Q1* and *Q2* enables a larger current flow from the power source, through the speaker and the collector-emitter circuit of *Q1*. This is heard as a click. When pin 13 of *IC2* or pin 4 of *IC3* goes high on the next count, a high is put back on pin 12 or 13 of *IC5D*. Pin 11 goes low again, and capacitor *C3* is discharged through diode *D4*. Since transistor *Q2* is now reverse-biased, there is no sound from the speaker

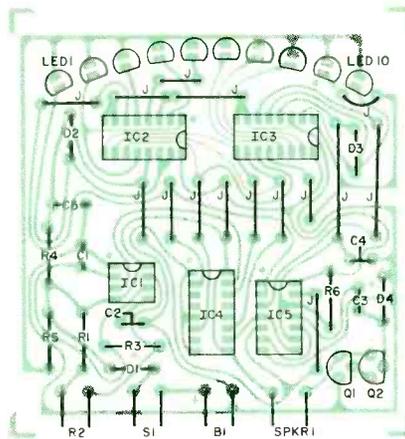
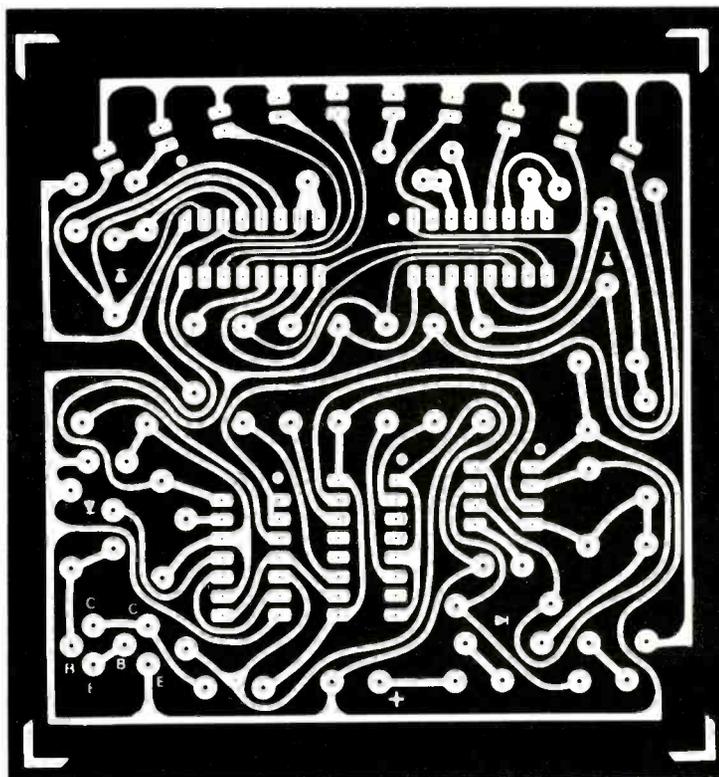


Fig. 2. An actual-size foil pattern for a printed-circuit board is shown below. Above is the component layout diagram. Install jumpers first.



until the pendulum "swings" to the other end.

Diodes *D2* and *D3* block the low voltage from pins 14 and 15 of *IC2* and pins 5 and 2 of *IC3* so the count is not prematurely reversed. Diode *D1* prevents damage to the circuit from reversed power leads, or from inserting the battery backwards.

**Construction.** The LED Pendulum Metronome circuit can be built on a perforated board with point-to-point

wiring. However, there are 120 terminations for the ICs, resistors, capacitors, diodes, and transistors—and this doesn't count the external speaker, potentiometer, switch, and battery wiring. Thus, it is more convenient to use the printed-circuit pattern and parts layout shown in Fig. 2.

If you use the pc board, assembly is straightforward. A small 25-watt soldering iron and resin-core solder should be used. First, install the 15 jumpers shown in Fig. 2, cutting wires

to the appropriate length and trimming about 1/4" of insulation from each end. Insert through the top of the board and solder on the foil side. Next, install and solder the resistors and diodes, making certain the bands (cathodes) on the diodes are properly oriented. Next, solder in the five sockets, but do not install the ICs yet. Now, solder in the three capacitors. The LEDs must be installed in the proper orientation. The cathode (bar of arrow symbol) is identified by a flat-spot or notch at the base of the LED, and the cathodes of all LEDs face the main portion of the circuit board. Next, solder in the transistors, with the flat side facing as shown, so the E, B, and C leads are properly placed. Solder two leads each for the speaker, switch, battery, and potentiometer, and your circuit board wiring is completed.

Your final packaging will dictate the placement of the external parts and the lengths of the leads. Double-sided tape can be used to mount the speaker to the foil side of the board. Potentiometer *R2* should be located below the circuit board to allow room for a calibrated scale and knob on whatever front panel you use. The switch and battery leads can be located wherever convenient.

A unique, modern package is provided by using two sheets of Plexiglas with spacers and screws used to mount the circuit board sandwich-fashion, as shown in the photo.

Since all the ICs have a broad voltage operating range (roughly 3 to 15 volts), you have a choice of what battery configuration to use. At 9 volts, the circuit draws an average of 30 mA, so it's practical to use a standard 9-volt transistor radio battery. Used 4 hours daily, a standard 9-volt zinc-carbon battery (such as Burgess 2V6) should last about 5 hours; an alkaline 9-volt cell should run at least 20 hours. For long, hard use, it would be less expensive to use four "AA" pen cells, "C" or "D" cells wired in series to provide 6 volts initially. Although the LEDs will not be quite as bright and the clicking not as loud, the average current drain is only about 20 mA. Four regular zinc-carbon pen cells should run the metronome for almost 50 hours, used 4 hours a day. Using zinc-carbon "C" cells under the same conditions you can expect 125 hours of use. Alkaline batteries will provide from 4 to 10 times as much useful life. Actually, the circuit will operate with as little as 3 volts, and only uses an average of 4 mA at that

voltage—but it's not very loud or bright, so not as effective!

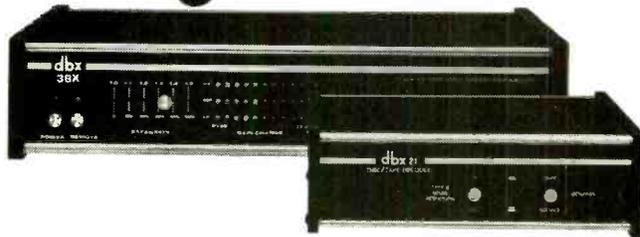
**Testing & Calibration.** It's a good idea to test and calibrate the unit before final installation in whatever cabinet you're using. Install the ICs carefully in their sockets, making sure they are oriented properly and that no pins are bent out or under. Since all the ICs except *IC1* are CMOS devices, take precautions to avoid static electricity when handling them. Solder the speaker, switch, potentiometer, and battery connector (if used) to the leads from the circuit board and don't forget the jumper across two of the potentiometer terminals.

Connect the battery power and turn on switch *S1*. If nothing happens, make sure that *D1* is not connected "backwards" and that battery polarity is correct. If any individual LED does not light, it may be soldered to the board backwards. If only one LED lights, the 555 (*IC1*) may not be operating. Check for the presence of positive voltage on pins 4 and 8, and see that pin 1 is grounded. Also make sure the values for resistors *R1* and *R3* and capacitor *C1* are correct. As always, solder connections should be checked. If the LEDs swing properly, but you hear no sound, check transistor installation and diode *D4* polarity. If you encounter problems beyond that, an ohmmeter, logic probe, and the circuit description should allow you to pinpoint the problem.

Calibration ideally requires a stopwatch, but a sweep-second watch or seconds-counting digital watch will do. You'll also need patience. The major calibration points are 60, 120, and 180 beats per minute (bpm), since these are 1, 2, and 3 beats per second. You can tell pretty closely in a 15-second timing period what the minute-rate will be for a particular pointer setting by just multiplying the number of beats by four. By trial and error, mark the pointer scale at these points. Next find the 90, 150, and 210 bpm points. Once you've found these points, space the other points equally between the calibrated points and you'll be close enough for all but precision use. If the clicking sound is too loud, add a resistor (up to 100 ohms) in series with either speaker lead. (A 100-ohm potentiometer or variable resistor can be used as a volume control if desired.)

The LED Pendulum Metronome is not intended to be used in precision-timing applications, but is a modern version of an established musical teaching aid. ♦

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  - ☆ Colour coded input terminals
  - ☆ and a usable response to beyond 5MHz

**Specifications:**

- Usable bandwidth DC to 5MHz plus
- Vertical deflection sensitivity: 10mV per division
- Horizontal deflection sensitivity: 500mV per division
- Time base sweep frequency: 10Hz to 100kHz in 4 ranges
- Synchronisation: internal and external
- Size: 202(W) x 160(H) x 306(D) mm
- Weight: approx. 3.8 kg

only \$ **229.00**

- Features:**
- ☆ Wide 1Hz to 200kHz frequency range
  - ☆ Sine, square, triangle and separate TTL square wave output
  - ☆ Continuously variable output to 10V P-P
  - ☆ Frequency sweepable over 100:1 range
  - ☆ Short-circuit proof outputs
  - ☆ Venier frequency dial with fine adjustment control

### Low cost Function Generator Model 5020A



**Specifications:**

- Frequency range: 1Hz to 200kHz in five overlapping decade ranges.
- Waveforms: Sine wave: Distortion <1% from 1Hz to 100 kHz; <3% from 100kHz to 200kHz
- Square wave: Rise/fall time <250nsec. Symmetry <98%
- Triangle wave: Non-linearity <1% to 100kHz
- Output: Impedance: 600Ω short-circuit proof. Amplitude (continuously variable): 10V P-P open circuit; 5V P-P into 600Ω max. Low Level: -40dB of high output. TTL square wave: >10 std. TTL loads
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### Interfaceable DMM Model 2020 MP

**Features:**

- ☆ 0.1% basic DCV accuracy
- ☆ 10 amps current measurement
- ☆ 31 ranges and 6 functions
- ☆ Hi power and Lo power Ohms
- ☆ Unique touch and hold capability\*\*
- ☆ Battery or AC operated\*\*\*
- ☆ Interface for most popular computers included

**Specifications:**

- 3 1/2 digit large 0.4" LED readouts
- Automatic decimal and minus (-) sign
- ACV frequency response: 40Hz to 40kHz on 200mV, 2V and 20V ranges
- Overload protection: 1200V (DC+AC peak) on all voltage ranges

\*\*CRT not included

\*\*\*Batteries or AC adaptor optional

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- Features:**
- ☆ 9-digit resolution for more precise readings
  - ☆ Excellent 30mV sensitivity up to 1GHz
  - ☆ 3 switch selectable gate times
  - ☆ 10MHz crystal controlled time base for greater accuracy
  - ☆ 2 separate inputs for added versatility
  - ☆ Front panel sensitivity control

### 1GHz 9-digit Frequency Counter Model 8000B



\$ **239.00**

**Specifications:**

- Frequency range: Model 8000B: 10Hz - 1GHz in 3 ranges. Model 8610B: 10Hz - 600MHz in 3 ranges
- Display: 9-digit 0.4" (10 mm) LED with automatic decimal point; separate LED gate activity indicator
- Resolution: 10MHz range: 0.1Hz with 10s gate time. 100MHz range: 1Hz with 10s gate time. 600MHz/1GHz range: 10Hz with 10s gate time
- Sensitivity: <20mV rms, 10Hz-100MHz; <30mV rms, 100-600MHz; <35mV rms, 600MHz-1GHz
- Input impedance: Input A-1MΩ/100pF. Input B-50Ω nominal
- Time base: Frequency: 10MHz. Setability ±2ppm. Temperature stability: ±1ppm from 0 to 40°C.
- Gate time: 0.1 second, 1 second, 10 seconds switch selectable.



### AUTORANGING DMM Model 2040 with 10 amps current measuring capability

This is a very sensitive, general purpose instrument which provides the facilities and quality required by today's electric/electronic technicians and engineers.

**Specifications:**

- Display: Numerical display: 3.5 digit LCD, maximum reading 1999. Unit and sign: mV, V, mA, A, Ω, KΩ, AUTO, BATT, ADJ, LO, -, AC
- Range selection: Autoranging on VOLT and OHM
- Polarity: Autopolarity, (-) sign when minus, (+) sign is implied and is not shown
- Battery warning: LO BATT sign
- Sampling rate: Two times per second
- Power consumption: 5mW typically
- Power supply: Two 1.5V batteries, type UM-3 or AA
- Battery life: 300 hour continuous operation
- Overload protection: One 3A 600V, BBS type fuse and one 0.3A 250V, 5x20 mm fuse for OHM and mA ranges
- Operating temperature and humidity: 0 to +40°C, less than 80%
- Zero adjustment: Zero adjustment by ZERO ADJ. Keyswitch
- Low power OHM ranges: For in-circuit resistance measurements at voltage levels below 0.33 volts

\$ **129.00**

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- ☆ Easiest operation: AUTORANGING SYSTEM requires no range selections
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- ☆ Low battery consumption of 5mW: 300 hour continuous use with two 1.5V batteries, type UM-3 or AA
- ☆ Difference Measurements: This instrument can be used like a galvanometer
- ☆ Ultimate Portability: Actualized light weight and compactness in excellently designed ABS cases



### Low Cost Handheld DMM Model 2038A

**Features:**

- ☆ 3 1/2-digit LCD display
- ☆ 0.6% basic DCV accuracy
- ☆ DC voltage: 1000V
- ☆ AC voltage: 750V
- ☆ Input impedance: 10MΩ
- ☆ Low battery indicator
- ☆ High impact ABS case
- ☆ AC/DC current: 2 amps
- ☆ Overload protection
- ☆ 2000 hours battery life
- ☆ Auto zero

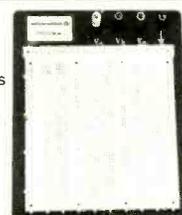
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### Solderless Breadboard Model 356S

**Features:**

- ☆ 3 terminal strips 5 distribution strips
- ☆ Aluminium plate
- ☆ Size: 200x175x8 mm actual area of breadboard
- ☆ Silver-plated contacts
- ☆ Accept all DIP size including RTL, DTL and CMOS devices
- ☆ Interconnect with any solid 20 to 29AWG (0.3-0.8 mm) wire
- ☆ Breadboard elements are mounted on ground plane, ideal for high frequency, high speed and low noise circuit

Other models also available.



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### Logic Probe Model LP-1

**Features:**

- ☆ Input impedance: 100KΩ
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- ☆ Input overload protection: ±50V Cont.
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# COMPUTER BITS

By Carl Warren

## Beef Up Your H-89

**I**F YOU'VE been thinking about additional capacity for your H-89 microcomputer, you might want to consider the double-density floppy disk controller from Magnolia Microsystems (2812 Thorndyke Ave. W., Seattle, WA 98199. Tel. 800-426-2841).

The \$595 unit comes with the PROMs, cables, CP/M, and documentation necessary to bring your system into the double-density world. The board supports up to 1210K bytes on a double-sided drive, and as much as 162K bytes on single-sided drives (such as those supplied in the Model H-77 disk system from Heath).

Other attributes of the controller include: the ability to work in concert with either the Heath single- or double-density controller, ease of configuration for a wide variety of systems, the ability to support as many as four 8" single- or double-sided drives, as well as four 5" single- or double-sided drives at the same time the Heath controller is supporting three 5" single-sided drives.

You should be aware, however, that the Magnolia board does not support either HDOS or Heath/Zenith CP/M. The CP/M Ver 2.2 that comes with the controller is optimized to take advantage of the various board characteristics and drives that can be attached to it.

**Installation.** Installing the controller takes about an hour. Most of the time is spent removing the CPU and terminal boards in order to make the necessary PROM and power-supply changes.

Because many of the H-89s in current use have the original power supply whose regulator is incapable of meeting the necessary current loads, your first step is to adapt the regulator by adding the part supplied for that purpose.

Your next task is to change the monitor PROMs designed to support the controller, and provide a well-thought-out command structure. Specifically, besides boot, you have P for program counter, S (substitute), G (go execute), M (dynamic memory test), and T (test drive rotational speed). Unlike the split-octal used by Heath, numbers are displayed on the monitor in hexadecimal.

Once you've made the necessary hardware additions and checked out the monitor commands, you're ready to configure the drives. On the 5" drives with your system, you only need to change the jumpers for head-load with motor. Incidentally, all the drives should be configured in this manner.

The 8" drive I chose was the single-sided FDD-100, 48 tpi (tracks-per-inch) from Siemens. Magnolia sells and recommends Qume's Data Trak 8, which currently is the only drive referenced in the set-up manual. (As of this writing, a new manual is being prepared to address a wider variety; getting the proper jumpers can be troublesome.)

As configured, my H-89 uses the Heath controller for the terminal-mounted drive, while the H-77 outboard drives are on the Magnolia controller, as is the 8" drive. The total system storage capacity is 1M byte: 80K bytes for the Heath drive, 160K bytes each for the 5" drives on the Magnolia controller, and 600K bytes for the 8" drive. All the drives are 48 tpi; but I could add 96-tpi drives in the daisy chain—either the 5" or 8" variety.

The Magnolia board is integrated into the system configuration via a specific program. This program is menu-driven and offers a number of selections, including density (single- or double-track), and one or two operational sides.

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TEXT EDITOR/ASSEMBLER — The editor/assembler is a software tool program designed to simplify the task of writing programs. As your programs become longer and more complex, the assembler can save you many hours of programming time. This software includes an editor program that enters the programs you write, makes changes, and saves the programs on cassettes. The assembler performs the clerical task of translating symbolic code into the computer-readable object code. The editor/assembler program is available either in cassette or a ROM version.

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Steel Cabinet for ASCII Keyboard/Terminal . . . \$19.95 plus \$2.50 P&I.\*  
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This way, the controller knows the bits per inch (bpi) are doubled; and for the 96-tpi drives, that you have more than 40 tracks to address. In addition, the configuration program permits logical and physical drive assignments in a lookup table. This technique maps the drives as they appear in your system. Moreover, you can set up a boot drive. I currently boot off a double-density 5" drive and use the 8" for maximum storage capacity.

During configuration, you can establish the density of a specific drive so that you can read a variety of disks. This is important since most software is delivered on single-density, IBM-compatible formats. Ideally, what you'd want is one 8" drive in double-density, the other in single. Should you have only a single 8" drive, prepare to make two systems disks, one configured for single-, the other for double-density.

**Capabilities.** The Magnolia controller achieves its double-density capability by the method it uses to record the data. With a 5" single-sided drive, such as that offered by Heath, you have tracks laid out 48 to an inch. Typically, the effective number of available tracks is 40, since the head travels just a little under an inch for a full stroke. The next parameter of concern is the linear densi-

ty, that is, number of bits recorded per inch. For the Siemens Model FDD 200-N, for example, the bit density is 2938 bpi for frequency-modulated (FM) recording. The total areal density (bpi  $\times$  effective tracks) is equal to 125K bytes.

To double the amount of data that can be put on the drive, only one parameter can be changed—the number of recorded bits per inch. The track density (tpi) is a physical limitation of the drive, although newer drives do offer 96 tpi, with an effective number of tracks ranging from 77 to 80.

Consequently, some method of increasing the bpi must be found, and the usual course is to employ modified frequency modulation (MFM) or M<sup>2</sup>FM. These two recording techniques reduce the size of the bit cell by applying a new definition of when a clock pulse starts or ends and data begins. The overall effect is to increase the number of bits per inch, thus increasing the overall real density of the drive. In the case of the Siemens drive, instead of 125K bytes you end up with 250K bytes. This is the technique employed by Magnolia.

You may have noted that I said the Magnolia controller gives you 160K bytes on the Heath drive, but that 250K bytes are specified with MFM recording techniques. The difference stems from

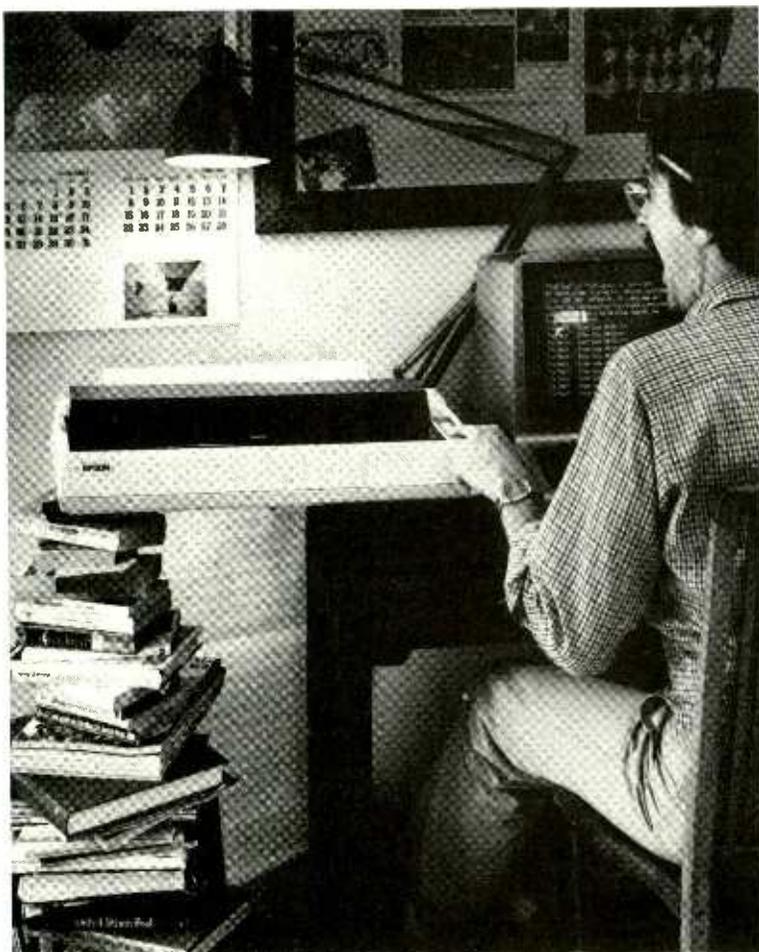
the fact that the 250K bytes is not formatted, while the 160K bytes is. Formatting implies overhead, in this case about 90K bytes.

The overhead is from the space required to define the sectors, provide sufficient spacing, and implant information on a track as to how the operating system should handle the disk. All of this must be accounted for by the controller so that errors aren't introduced. The Magnolia controller does this without any problems.

The Magnolia controller is well worth the money: it adds flexibility to your Heath system without sacrificing functionality.

I encountered only two problems as a result of the addition of the board. The first was system power difficulty due to low line voltage. This is easily cured by moving the line switch on the H-89 to low; but be aware that if the line comes up to nominal (typically 112 V), you will need to switch back to high or have a power supply running hot.

The second problem was making the Diablo Model 630 daisy-wheel printer work correctly. To do this, it is necessary to open the printer up, change a jumper between pins 5 and 6 on the HYPRO interface board, and then employ a pin-to-pin cable. The problem I had was in using a cable wired for an RS-232C that



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Needless to say, the specs on this machine — and especially at under \$1000 — are practically unbelievable. But there's something about the MX-100 that goes far beyond just the specs. Mere words fail us. But when you see an MX-100, you'll know what we mean.

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carried a less-than-full signal, and, as a consequence, "clear-to-send" was not being monitored by the H-89, so buffer overflow resulted. Changing the cable fixed the problem.

Speaking of cables, I've found that no matter where you go to buy either a round or flat one, the prices range from \$30 to \$40 for a six-foot section with connectors. Interestingly, the actual cost to the manufacturer is only about \$3.

**Okidata Microline 83A Printer.** With your storage needs solved, you might want to look at bettering your hardcopy output. One printer that deserves attention is the Microline 83A, made by Okidata Corp. (111 Gaither Dr., Mt. Laurel, NJ 08054).

The Microline 83A serial dot-matrix printer designed for applications requiring long-duty cycles, letter-quality printing, and full-size (15") paper.

Sporting both a 1200-bps RS232C serial interface and an 8-bit parallel interface, the Model 83A operates at 120 cps with a bidirectional print mechanism with shortline-seeking logic. In addition, standard characters are produced with a dense 9 x 9 dot pattern by a 9-wire, stored-energy print head. The Model 83A includes the full 96 ASCII character set (upper and lower case) plus 64 block graphics shapes. Furthermore, special character sets for British, German, French, Swedish, Danish, Norwegian, Dutch, Italian, and special-function TRS-80 codes are standard. A 136-character buffer is also standard.

The 64 graphics characters are similar to those of the TRS-80, and can be combined with the condensed and double-width functions to produce charts and graphs like those created on raster-type printers.

An optional 9600 bps is also available. The price for the high-speed interface Model HS-RS232, with a 256-byte buffer is \$180; with a 2K-byte buffer the price is \$270.

The printer with the optional \$50 tractor feed supports paper widths up to 15" or 14" with friction feed. Standard throughput ranges from 232 lpm for a 20-character line to 47 lpm for a full 136 characters per line.

In addition, line spacing is program-selectable (either 6 or 8 lpi), with a standard character size of 0.081" W x 0.105" H, with spacing of 10 cpi. In the condensed mode, spacing is 16.5 cpi, and double width is 5 cpi.

The flexible printer permits the use of single-sheet plain paper with friction feed, or continuous paper with the tractor feed. In addition, the adjustable snap-on tractor accommodates four-part stock up to 15" in width.

The Microline 83A power requirements range from 100V to 240V at 50 or 60 Hz, with a maximum power dissipation of 150 W. The 20.2" x 12.9" x 5.2" printer weighs 30.9 lb, and is priced at \$1195, with owners manual and interfacing guides. You can find it at most computer stores. ♦

Now with added words! \*  
**ELECTRIC MOUTH**



for \$100, ELF II, Apple TRS-80, Level II From **\$99.95** kit

Now — teach your computer to talk, increasing interaction between you and your machine.

That's right, the ELECTRIC MOUTH actually lets your computer talk! Installed on-line in just minutes, it's ready for spoken-language use in office, business, industrial and commercial applications, and in games, special projects, R & D, education, security devices — there's no end to the ELECTRIC MOUTH's usefulness. Look at these features:

- Supplied with 143 letters/words/phonemes/numbers, capable of producing hundreds of words and phrases.
- Expandable on-board up to thousands of words and phrases with additional speech ROMs (see new speech ROM described below).
- Four models, that plug directly into S100, Apple, ELF II and TRS-80 Level II computers.
- Get ELECTRIC MOUTH to talk with either basic or machine language (very easy to use, complete instructions with examples included).
- Uses National Semiconductor's "Digitaler".
- Includes on-board audio amplifier and speaker, with provisions for external speakers.
- Installs in just minutes.

**Principle of Operation:** The ELECTRIC MOUTH stores the digital equivalents of words in ROMs. When words, phrases and phonemes are desired, they simply are called for by your program and then synthesized into speech. The ELECTRIC MOUTH system requires none of your valuable memory space except for a few addresses if used in memory mapped mode. In most cases, output ports (user selectable) are used.

**SPOKEN MATERIAL INCLUDED (Vox II)**

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two	nineteen	cancel	drown	is	off	second	i	u		
three	twenty	case	equal	it	olt	set	e	v		
four	thirty	cent	error	kilo	on	space	f	w		
five	forty	centihertz	tone	feet	left	out	speed	g	x	
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seven	sixty	zms	science	fuel	lesser	narenthesis	start	i	z	
eight	seventy	40ms	silence	gallon	limit	percent	stop			
nine	eighty	60ms	silence	go	low	please	than	k		
ten	ninety	180ms	silence	gram	lower	plus	test			
eleven	hundred	320ms	silence	great	mark	point	time	m		
twelve	thousand	centi	greater	meter	round	try	n			
thirteen	million	check	have	mile	pulses	up	o			
fourteen	zero	comma	high	milli	rate	volt	p			
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button	emergency	hale	notice	safe	waiting
enter	call	hello	operator	second	warning
called	or	help	or	secure	was
caution	eth	hurts	pass	select	water
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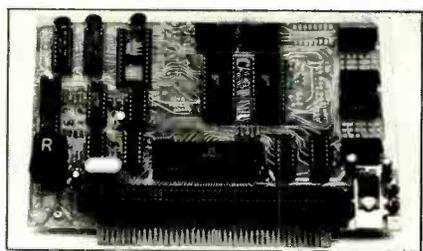
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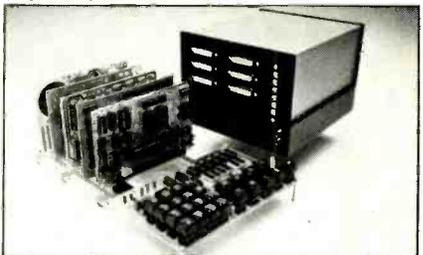
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# COMPUTER SOURCES

By Leslie Solomon  
Senior Technical Editor

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## Software

**IBM Personal Computer OS. SB-86** is a proposed standard operating system for 16-bit systems and is completely compatible with the new IBM Personal Computer. It is written in 8086 assembly language. It has no 64K program space limitation, although a single model can exceed 64K. The relocatable linking loader can provide for separate segments thus making it a truly relocatable operating system. It supports advanced error recovery procedures and has a device-independent I/O. Interfacing with hardware is simplified by its variable physical disk sector size. Disk programs can specify any logical record size making large files very efficient. SB-86 has no file or disk size limitation (unlike CP/M) and includes such features as text editing template, no need to log in disks, variable length logical records, remembered EOF markers, and the stamping of the creation and last-access dates and times on each file. IBM will offer software running under SB-86. Future extensions include disk buffering, graphics and cursor positioning, Kanji support, XENIX compatibility, multi-user, hard disk, and networking. **Address:** Lifeboat Associates, 1651 Third Avenue, New York, NY 10028 (Tel: 212-860-0300).

**ZX-80 Music.** The Player ZX80 is a music synthesis for the Sinclair ZX-80 with 4K BASIC and 1K RAM. It is single voiced with a 2-octave range and can

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perform tunes up to 127 notes in length. It can be run at any tempo and continuously replays until interrupted. Space Muse-ak, a random sound program is also included. \$6.95. Address: Wm. D. Maples, 688 Moore St., Lakewood, CO 80215.

**Activity Scheduler.** "Schedule" was developed for the North Star system, and is soon to be made available for other systems. In operation, it can record as many as 10 activities a day for a year, with exact times and reminders of activities with no set time. All activities are sorted with timed ones displayed in the proper order, and reminders displayed after timed activities. The output can be selected from a single date, or a span between dates. The data can appear on a CRT or printer. It is written in North Star BASIC, requires 8K of RAM, and a single-drive, single-density disk. \$19.95. Address: Azimuth Associates, Box, 1636, Arlington, VA 22210.

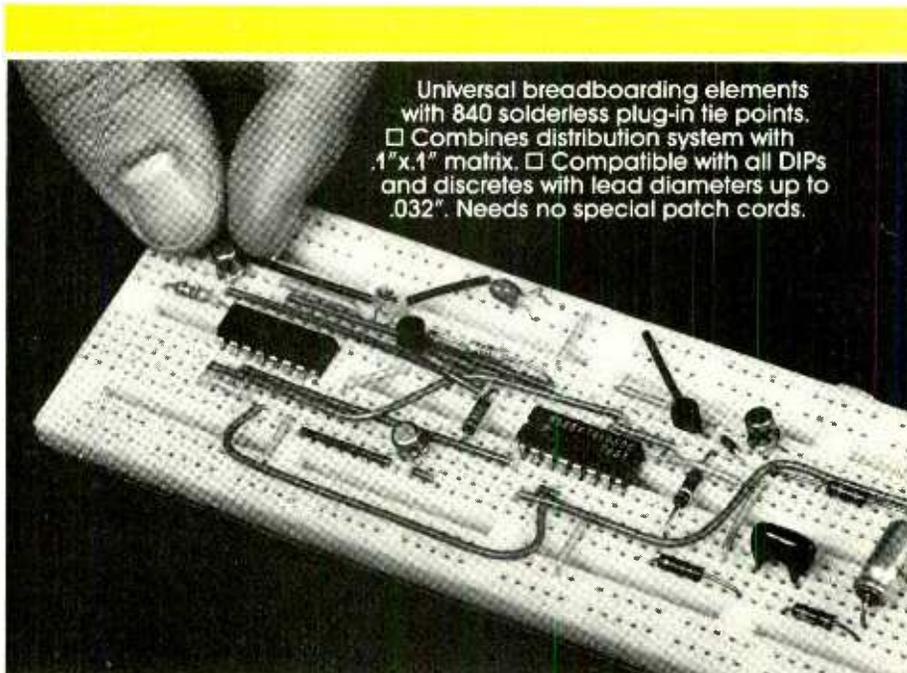
**Armored Attack.** TANKTICS is a war game in which a human plays tank warfare against the computer. Five scenarios are provided. It comes with a 22" by 16" mapboard and over 240 counters depicting 15 different armored vehicles. Cassette version includes Atari 400/800 with 24K, Apple II with Applesoft and 16K, PET with 16K, and TRS-80 Level II with 16K; \$24. Diskettes versions include TRS-80 with 32K, Apple II with 32K, and Atari 800 with 40K; \$29. Address: Avalon Hill Game Co., 4517 Harford Rd., Baltimore, MD 21214 (Tel: 301-254-5300).

**ZX-80 Games.** Written for the Sinclair ZX-80 and MicroAce computers, A Night in Las Vegas contains Slot Machine, Roulette, Blackjack, and Craps, all with graphics. Comes with cassette, manual, guide cards, miniature layouts, chips, and full color keyboard overlays. \$9.95. Address: Lamo-Lem, Box 2382, La Jolla, CA 92038 (Tel: 714-262-5681).

**TRS-80 Mailing List.** MAIL-M3 features multiple labels (up to 4) across a page, form input, report writer, multi-sort keys, duplicate label checking, random access, and search. Screen and edit functions allow cursor movement, and delete lines and characters. Fields include 9-digit zip codes, and remarks. The report writer allows specifying report formats without programming, multiple sort keys, filter criteria, field selection and record delete. It also allows unlimited report formats. The report generator allows for record range, selection code range, and sort field. You can also set the number of lines on a page. MAIL can store 500 records of 127 bytes each per diskette. It requires TRSDOS. 48K version is \$79, 32K version is \$59. Address: Micro Architect Inc., 96 Dothan St., Arlington, MA 02174 (Tel: 617-643-4713).

**Reference Charts.** Printed on durable credit-card plastic 8 1/2" X 11" sheets, the MICRO CHARTS contain the instruction set, disassembly tables, ASCII, hex-to-decimal, compare vs jump, effect on flags, interrupt structure, pinout, cycle times, memory map, diagrams, addressing, powers of two, and more. These charts are available for the Z80, 5502 (65XX), 8080A, 8085A, and relatives. \$5.95 plus \$1 postage/handling. Address: Micro Logic Corp., Dept. PE, Box 174, Hackensack, NJ 07602 (Tel: 201-342-6518).

**Applesoft Compiler.** This product translates standard Applesoft BASIC programs into true machine code that runs up to 12 times faster than the interpreter. Programs can be written in Applesoft BASIC, then compiled with the advantage of machine language. It requires 48K RAM, Applesoft, Autostart ROM, and at least one disk drive. It can also operate in a 64K environment with RAMcard. All Applesoft features, except dynamic array dimensioning and interpreter statements such as List and Trace, are supported. The 3.3 DOS ver-



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sion will soon be available. \$200. Address: Hayden Book Co., 50 Essex St., Rochelle Park, NJ 07662 (Tel: 201-843-0550).

**TRS-80 Bowling.** TENPINS is a machine-language bowling game for one to four players on a Model I/II system. The game features realistic action and sounds. Scoring, pinsetting, and ball return are all automatic, and the score-sheet is readily available. Cassette is \$14.95, disk for Model I is \$20.95. Disk version for Model III soon to be avail-

able. Address: Acorn Software Products Inc., 634 North Carolina Ave., SE, Washington, DC 20002 (Tel: 202-544-4259).

**TRS-80 Compiler.** The RSBASIC (26-2204) compiler is designed for the TRS-80 Model I and Model III machines, having 48K dual-disk drives. It will not convert programs written in the BASIC interpreter. The software features sequential, random and single-key ISAM file access; direct calls to machine language programs; full program

chaining capabilities with common variable storage; numerical accuracy to 14 significant digits for real numbers; step and trace debugging; printer and disk utilities; strings, arithmetic, trigonometric and bit operations; conversion between data types; and full editing. \$149. Model II Compiler BASIC (26-4705) requires a 64K single disk Model II and is priced at \$199. Address: Radio Shack stores and Computer Centers.

**FP FORTH.** Floating point FORTH is available for Z80/8080/8085 systems with CP/M or CDOS and must be ordered as an option with any version of Timin FORTH. It is written in 8080 code and requires a minimum of 24K. This product allows the user to work with decimal numbers similar to BASIC and FORTRAN. The 16-bit and 32-bit integer capability of FIG FORTH is retained, but a floating point mode is added. These are single precision floating-point numbers with approximately 7 significant decimal digits. Addition, subtraction, negation, multiplication, division, integer conversion, and comparison are supported. \$100 plus cost of Timin FORTH). Address: Timin Engineering Co., 9575 Genesee Ave., Suite E-2, San Diego, CA 92121 (Tel: 714-455-9008).

**Baseball Program.** Major League Baseball contains statistics on every major league player and the rules of baseball. The user is the team manager and you can play a game, recreate a season, series, or World Series. A box score is provided at the end of each game, and season's statistics are provided for each player used. TRS-80 Model I, Level II 16K cassette is \$25; Apple II 32K cassette is \$25; TRS-80 Model I, Level II 32K diskette is \$30; Apple II 48K diskette is \$30. Atari 800 version upcoming. Address: Avalon Hill Game Company, 4517 Harford Rd., Baltimore, MD 21214 (Tel: 301-254-5300).

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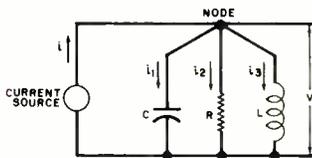
# FUNDAMENTAL FACTS

By Walter Buchsbaum

## The Basic Network Laws

**T**HE NAMES Kirchhoff, Thevenin, and Norton are known to anyone who has ever studied the principles of electricity. In the complexity of modern electronics, we tend to forget that any circuit can be understood in terms of the basic network laws laid down by these men. In analyzing a new circuit, we can apply them to gain an understanding of how the circuit is supposed to operate.

**Kirchhoff's Current Law.** This law states that the algebraic sum of all currents toward a node is zero. In other words, all current flowing into a node must also flow out of it. As illustrated in Fig. 1, the sum of the currents flowing from the node to the resistor, the capacitor and the inductor must be exactly equal to the current flowing into that node from the source. In the case of varying currents, we use the operators  $p$  and  $1/p$ . These are simply shorthand notations for the ac response of C and L. When considering sine waves, these operators become  $2\pi f$ , with the frequency given in hertz.



$$i = i_1 + i_2 + i_3$$

$$= C_p V + V/R + V/Lp$$

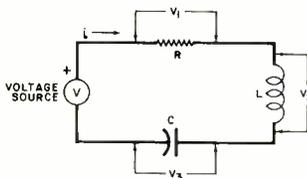
$$= (C_p + 1/R + 1/Lp)V$$

$$p = d/dt \text{ (derivative operator)}$$

$$1/p = \int dt \text{ (integral operator)}$$

Fig. 1. Kirchhoff's current law.

**Kirchhoff's Voltage Law.** This states that the algebraic sum of all the voltages around a closed loop of a network is zero. Thus, the applied voltage must equal the sum of all the voltage drops. This law, too, is based on the conservation of energy, one of the fundamental principles of physics. As illus-



$$V = V_1 + V_2 + V_3$$

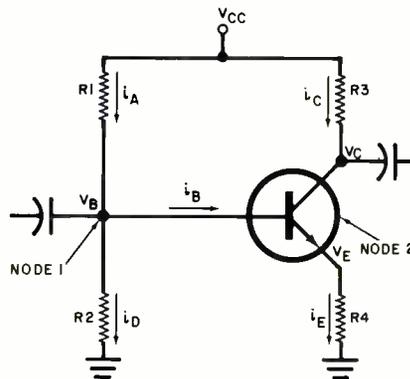
$$= Ri + Lp i + i/Cp$$

$$= (R + Lp + 1/Cp) i$$

Fig. 2. Kirchhoff's voltage law.

trated in Fig. 2, it means that we can determine an unknown voltage drop by knowing the applied voltage and the other drops in the circuit. As in the current law, the time-varying characteristics of the capacitor and the inductor are represented by the operators  $p$  and  $1/p$ .

**Examples.** In the small-signal, linear-transistor amplifier shown in Fig. 3 there are two nodes, and Kirchhoff's current law can be used to check that the base-bias current,  $i_B$ , will produce an



**Kirchhoff's Current Law:**

$$i_A = i_B + i_D$$

$$i_E = i_B + i_C$$

If,  $V_{CC} = 12 \text{ V}$ ;  $V_B = 7.5 \text{ V}$ ;  $V_C = 8 \text{ V}$ ;  $V_E = 0.45 \text{ V}$  and  $R_1 = 2.7 \text{ k}\Omega$ ;  $R_2 = 6.8 \text{ k}\Omega$ ;  $R_3 = 1 \text{ k}\Omega$ ;  $R_4 = 100\Omega$

Then:  $i_A = 4.5/2.7 = 1.65 \text{ mA}$   
 $i_D = 7.5/6.8 = 1.10 \text{ mA}$   
 $i_C = 4.0/1 = 4.0 \text{ mA}$   
 $i_E = 0.45/100 = 4.5 \text{ mA}$

And:  $i_B = i_A - i_D = 1.65 - 1.10 = 0.55 \text{ mA}$

Fig. 3. Example using current law.

emitter current,  $i_E$ , that adds up to the sum of the  $i_C$  and  $i_B$ .

In the high-voltage bleeder network of a CRT power supply (Fig. 4), to determine the anode voltage, it is necessary to measure only the focus voltage. Since the value of each bleeder resistor is known, the drops across them can be subtracted from the focus voltage.

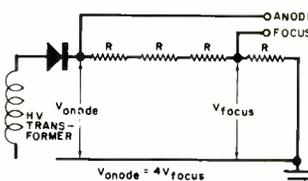
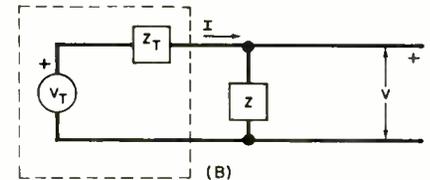
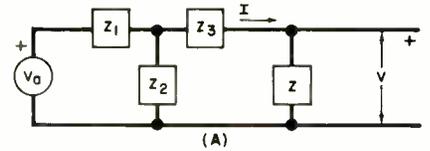


Fig. 4. Example using voltage law.

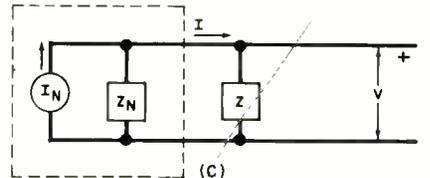
## Thevenin and Norton Equivalents.

These two theorems offer a means to simplify complex networks by consolidating them into an equivalent voltage source with a series impedance (Thevenin) or into an equivalent current source with a shunt impedance (Norton). The



$$V_T = V_A Z_2 / (Z_1 + Z_2)$$

$$Z_T = Z_3 + Z_2 \parallel Z_1$$



$$I_N = V_A Z_2 / [Z_3 (Z_1 + Z_2) + Z_1 Z_2]$$

$$= (Z_1 + Z_2) / [Z_3 (Z_1 + Z_2) + Z_1 Z_2]$$

For example:

If,  $V_A = 12 \text{ V}$   
 and  $Z_1 = Z_2 = Z_3 = 1 \text{ k}\Omega$   
 Thevenin equivalent:  
 $V_T = (12 \times 1) / 2 = 6 \text{ V}$   
 $Z_T = 1 + 0.5 = 1.5 \text{ k}\Omega$

Norton equivalent:

$$I_N = (12 \times 10^3) / (2 \times 10^6 + 10^6)$$

$$= 12 / (3 \times 10^3) = 4 \text{ mA}$$

$$Y_N = 2 / (3 \times 10^3)$$

$$= 0.667 \text{ millimhos}$$

Fig. 5. At (A) is a typical network. Solution at (B) uses Thevenin equivalent; at (C), Norton equivalent.

network in Fig. 5A consists of a voltage source, a Y-connected set of impedances, and a load or terminal impedance, Z. In actual applications, much more complicated networks, including those with multiple voltage sources, can be simplified by applying Norton's or Thevenin's theorems. The Thevenin equivalent is shown in Fig. 5B, together with the formulas for the Thevenin voltage source,  $V_T$ , and the Thevenin series impedance,  $Z_T$ . Another way to represent the network example would be through Norton's equivalent, illustrated in Fig. 5C. The current source,  $I_N$ , and the shunt admittance,  $Y_N$ , have somewhat more complex formulas than the Thevenin equivalents, but this is due to the particular example we have chosen.

Anyone doing network analysis soon learns which network features lend themselves to a voltage (Thevenin) or a current (Norton) transformation. Once either equivalent is obtained, it is relatively simple to get impedance matching solutions for any kind of Z.  $\diamond$

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## Global Model 4401 Frequency Standard



VA maximum. A 215-230-volt, 50-60-Hz version also is available.

**Comments.** The Model 4401 Frequency Standard was checked by the Lockheed Electronics Instrumentation Measurement Laboratory against a cesium-beam frequency standard (HP5062C) traceable to the National Bureau of Standards. The Model 4401 met or exceeded its claimed specifications in all respects.

The Model 4401 is an excellent source of discrete, selectable, square waves that can be used as an oscilloscope time-base calibrator, precision clock source for digital circuits including microprocessors, a frequency counter calibrator, or any other application where a precise frequency source is required.

Once power is turned on, the 4401 should be allowed to "sit" for 15 to 20 minutes with the OVEN READY indicator glowing. (This indicates that the internal crystal oven has come up to temperature.) During this period, the default frequency is 0.1 Hz as indicated by its glowing LED. The FREQUENCY SELECT pushbutton is then depressed to step the glowing LED indication to the desired frequency. The FREQUENCY MULTIPLIER rotary switch is used to select either X1 (the front-panel LED indicated frequency), X2, or X5. This arrangement allows for 24 frequencies between 0.1 Hz and 5 MHz.

While the 4401 was warming up, we checked the accompanying well-written and profusely illustrated manual. Besides giving full details of installation, operation, and testing of the 4401, the manual provides some examples of application for the instrument.

The first use we made of the 4401 was to check out our frequency counters and scopes. The two frequency counters were "on the head," but one of our expensive scopes was found to require some work in the horizontal section to improve the linearity. Any measurements (frequency or period) made with this scope would have been almost 10% off! With the information provided by the manual, we used the output square waves to check some audio equipment. Square waves can be used for audio testing since, in order to reproduce, say a 10-kHz square wave, the amplifier must resolve at least 10 harmonics, making it good to 100 kHz. This is an excellent and fast test for audio systems, since it also shows the effect (and confirms operation) of bass/treble controls, and any variable equalizers in the circuit. In an instant, you can see the reaction of

**T**HE Global Specialties Corporation's Model 4401 Frequency Standard is a low-cost, line-powered instrument capable of generating 24 frequencies of very stable, 50% duty cycle square waves between 0.1 Hz and 5 MHz. It measures 3"H x 10"W x 7"D and weighs 2 pounds. Suggested retail price is \$288.

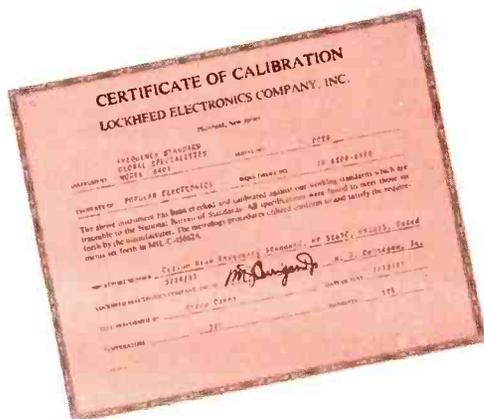
**General Description.** All controls, indicators, and output connections are made on the front panel. There is a row of eight LEDs indicating output at 0.1, 1, 10, and 100 Hz; 1, 10, and 100 kHz; and 1 MHz. These LEDs glow in accordance with the selection made via

the FREQUENCY SELECT pushbutton. Multiplication of the base frequency (X1, X2, and X5) is made via the FREQUENCY MULTIPLIER rotary switch. Another LED is used to indicate OVEN READY conditions. The 10-MHz crystal oscillator is maintained at a constant temperature within this oven to provide accuracy and long-term stability despite ambient changes.

Two BNC connectors provide outputs at 10 MHz and the selected frequency. The POWER toggle switch is on the left side. The case is supported on four skid-proof bumpers and ventilation slots are provided on both the upper and lower surfaces of the enclosure.

The 10-MHz output provides a 50-ohm, TTL-compatible square wave, buffered to drive up to 10 TTL loads. It is short-circuit protected and the rise and fall times of the output waveform are 20 nanoseconds into a 50-ohm load. The selected output frequency is also 50-ohm, TTL-compatible and can drive up to 10 TTL loads. It too is short-circuit protected, with 20-nanosecond rise and fall times into 50 ohms.

The basic frequency reference is a 10-MHz crystal mounted within an oven maintained (as long as power is turned on) at 55°C. Stability is  $\pm 0.5$  ppm (parts per million) and aging is less than 1 ppm/year. An accessible, internal control can be used to calibrate the instrument against station WWVB. Power requirement is 105-135 volts, 57-63 Hz, 5



the input square wave to the adjustable frequency response of the system. A small value capacitor can be connected across the audio input terminals to "slow down" the 20- $\mu$ s risetime.

The manual section on testing of transmission lines sparked quite a bit of interest since we often use long lengths of coaxial cable. The tests show whether a short, open, or "kink" in the line can affect performance. Various impedance discontinuities show up when using the 4401 with a scope, and the technique will even measure the length of cable when the cable is still on the spool!

We did not use the 4401 to clock a microprocessor, but we did use it to trigger some digital logic circuitry we were working on. We experimented with raising the clock rate, then hand-selecting the input TTL logic, until we could run the circuit at almost double the clock rate we had originally intended to use.

We are using the model 4401 as a frequency standard to check many of our bench instruments. We turn the standard on first thing in the morning and allow it to keep warm all day in the interest of stability. Frequency counters and scopes are checked each morning to make sure that all measurements made during the day will be "on the head." Using the 4401, a scope, and Lissajous patterns, we also check the dial calibration of our audio and r-f signal generators. About the only thing we cannot check with the 4401 is our DMMs.

We routed the SELECT output signal, via a switch, to our audio bench, where it was terminated in a 50-ohm resistor, and used for fast audio system checking. This same source is used to check digital circuits that require clocking, since a wide variety of clock rates is available.

Although an accurate frequency standard may appear as a luxury item at first glance, given the state-of-the-art of most modern electronic appliances, a service bench without some type of standard against which instruments can be checked, is an instrument bench that may be giving false data.

—Les Solomon

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CIRCLE NO. 29 ON FREE INFORMATION CARD

# SOLID-STATE DEVELOPMENTS

By Forrest M. Mims

## Liquid Crystals

**I**F LIQUIDS flow and crystals are solid, then how can a crystal be a liquid? The answer to this question provides the key to why liquid crystals can be used in such important applications as temperature measurement, pressure detection, electro-optical shutters and various kinds of displays.

A liquid crystal is a viscous fluid containing rodlike molecules aligned in a recognizable order. Many organic compounds fit this description.

Liquid crystals are categorized, according to the alignment of their molecules, into three major types: nematic, smectic and cholesteric. Figure 1 illustrates the molecular alignment of the three types.

The molecules in the nematic liquid crystal are parallel to one another. The word "smectic" comes from the Greek

word for soap, the most common representative of this kind of liquid crystal. Its molecules are arranged in layers. Adjacent layers can slide over one another since the molecules in one layer cannot move into an adjacent layer.

The molecules in cholesteric liquid crystals are also in layers. However, in each layer, they are parallel to one another and so the crystals resemble nematic material. The orientation of the molecules in a layer is displaced slightly from the layer above and the one below. Plotting the orientation of the molecules in each layer along a vertical axis shows how the molecules rotate to give a helical pattern.

**Applications for Liquid Crystals.** Cholesteric and nematic liquid crystals have several important applications in solid-state electronics. All liquid crystals exhibit temperature sensitivity, but cholesteric crystals indicate temperature differences by changing their color. Nematic crystals, which are normally transparent, can be made opaque by the application of a low voltage. Both these effects are caused by the movement of molecules in the liquid crystal.

There are two principal ways of using cholesteric liquid crystals to detect temperature. In one method the liquid crystal is mixed with a solvent called a carrier and sprayed or brushed over a black, water-soluble lacquer which has previously been applied to the object being tested. The solvent quickly evaporates, leaving behind a thin film of cholesteric fluid.

Depending upon the cholesteric material used, temperatures ranging from 0° C to 200° C can be detected. Within this broad range, the liquid crystal will indicate a temperature interval as narrow as 0.5° C by a change in color from red (coolest temperature) to blue (warmest temperature). The crystal will appear black, the color of the underlying paint, at temperatures above and below the sensitive region. The sensitive region of some materials may be several tens of degrees wide.

Often, color photographs are made of a cholesteric liquid-crystal temperature monitoring test. The resolution of the process is high enough to permit thermal mapping of some integrated circuit chips. This permits hot spots to be pin-

pointed. A cholesteric coating can also reveal the location of conductors in an opaque material, cracks in metal and flaws in honeycomb structures.

Even though a coating of cholesteric liquid crystals can be removed with water, the application and removal of the material is messy and takes time. I know from firsthand experience because I've used cholesteric liquid crystals on mylar films to visualize the cross-section of infrared laser beams.

A far better approach is to encapsulate the liquid-crystal compound in spherical capsules about 10 to 30 microns in diameter using a process perfected by the National Cash Register Company (Dayton, OH 45409). NCR sells encapsulated crystals in a water-based slurry suitable for direct application over a black background. This precludes the need for the solvent which is required in direct application of the material.

NCR also makes paper and plastic sheets coated with encapsulated, and therefore dry, crystals. These temperature-sensitive sheets can be used to make disposable thermometers, temperature indicators for power transistors and ICs and many kinds of novelty devices.

You can buy liquid-crystal thermometers at department and specialty stores. Edmund Scientific (101 E. Gloucester Pike, Barrington, NJ 08007) sells assorted sheets of encapsulated liquid crystals. They offer at least six different temperature ranges.

The major application for nematic liquid crystals is alphanumeric displays for

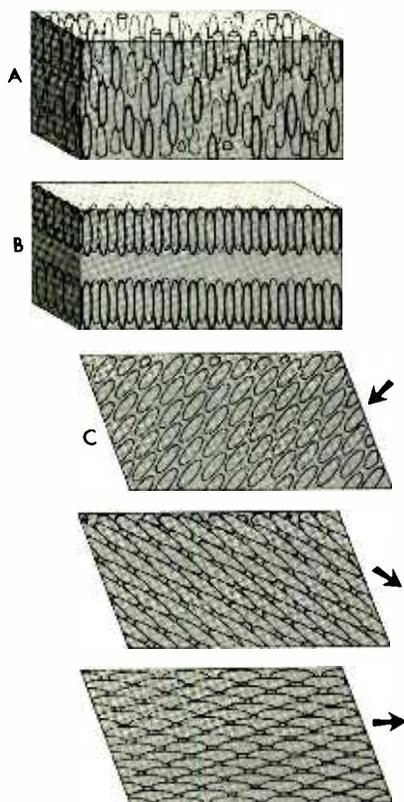


Fig. 1. How molecules are organized in liquid crystals: (A) nematic, (B) smectic, and (C) cholesteric.



Large-format LCD readouts from Beckman Instruments operate on 3 to 15 V rms and require as little as 50 microwatts.

## solid-state developments

clocks, watches, calculators and other devices with digital readouts. This technology has come a long way in the ten years since I first experimented with a homemade nematic crystal electro-optical shutter. In those days commercial nematic liquid-crystal displays were unavailable and it was necessary for experimenters to place drops of greasy nematic compound on small sheets of glass coated with a conductive layer of tin oxide. Air bubbles and leakage were the two biggest problems—assuming the experimenter was able to hold everything together with tape or glue.

Today a wide range of both digital and alphanumeric nematic liquid-crystal display (LCD) devices are available. Recently several companies have begun offering high-density dot-matrix LCDs which permit low-resolution graphic displays. The availability of these and other kinds of LCD readouts has made a great impact on the market for light-emitting diode (LED) displays. Since both LCD and LED displays are widely used in applications requiring miniature readouts, it's instructive to compare their advantages and disadvantages.

**LED Displays.** LEDs can be directly driven by most ICs, although CMOS chips may require buffering if high current levels are desired. They operate at less than 2 volts and possess the long life, reliability and robust nature of most other solid-state devices. And they are readily multiplexed.

On the other hand, the manufacture of LED readouts involves a complex series of fabrication steps. Light-emitting semiconductors are much more costly than liquid crystals. A single segment or element of a LED readout consumes from 0.5 to 5 milliamperes. Also, LED displays are difficult or even impossible to read in bright sunlight.

**Liquid Crystal Displays.** LCDs are easier to make and less costly than their LED counterparts. They are easily read in bright sunlight. They consume minuscule amounts of power (typically 1 to 5 microamperes per square centimeter of display area). And they can be made in much larger formats than LED versions of the same display.

Beckman Instruments, Inc. (2500 Harbor Blvd., Box 3100, Fullerton, CA 92634) makes large-format, 7-segment LCD readouts that can be driven directly by suitable CMOS circuitry and can operate at 3 to 20 volts rms. Normally all segments are transparent. Applying a voltage to a segment causes the nematic crystal to become opaque and the segment to appear dark.

Power consumption of the Model 740 6-digit readout is only 50 microwatts. A comparable LED readout would consume some 70 milliwatts assuming a forward voltage of 2 volts, a drive current of 5 milliamperes per segment and a multiplexing scheme wherein only one of the six digits is on at any time.

Operating advantages like these are

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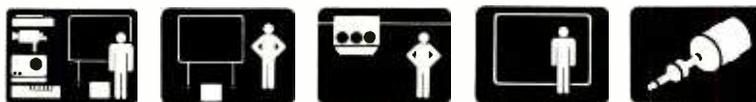
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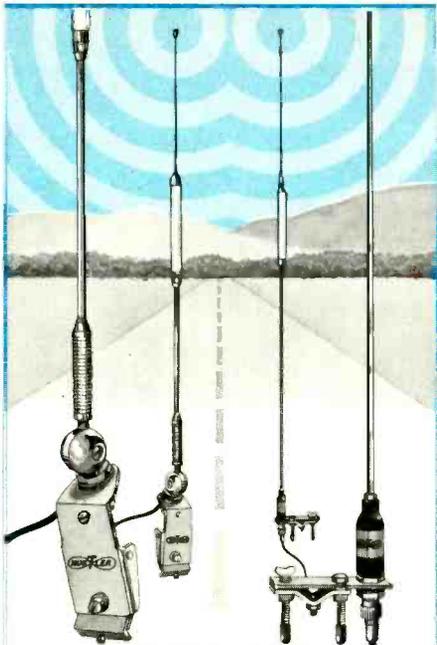
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## solid-state developments

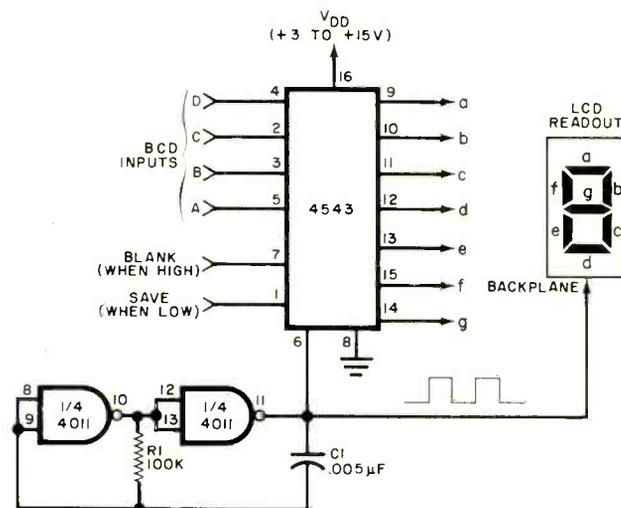


Fig. 2. How to use the 4543 CMOS LCD 7-segment latch/decoder/driver.

accompanied by several drawbacks. One is that LCD readouts cannot be read in the dark. For this reason some LCD readouts include a miniature incandescent lamp. Another disadvantage is that LCD readouts must be powered by an ac, not dc, voltage (usually 3 to 15 V). The driving requirements preclude direct drive of LCDs by standard ICs. Instead, special driving circuits or LCD compatible chips are required.

Operating temperature is another drawback since nematic liquid crystal will both freeze and boil. This gives a typical useful operating temperature range from about  $-10^{\circ}\text{C}$  to  $+70^{\circ}\text{C}$ . Finally, LCD readouts have much slower turn-on and turn-off times than LEDs. This and their driving requirements make multiplexing difficult.

Their disadvantages notwithstanding, LCD readouts have all but replaced LED displays in watches and calculators. One of the main reasons is their ultra-low power consumption. Couple this with CMOS circuitry and you have, for example, a watch that continuously displays the time for up to two years before battery replacement is required!

Of course LED readouts still have an important role to play, particularly when a self-illuminating display is required. In addition, LEDs are inherently sturdier than their glass-encased LCD counterparts.

For this reason, some IC makers cater to both kinds of displays. For example, Intersil makes two versions of a single-chip panel meter. The ICL 7106 is designed to drive LCDs while the ICL 7107 is designed to drive LEDs. They are available from Intersil (10710 N. Tantau Ave., Cupertino, CA 95014).

**An LCD Driver Chip.** Many different clock and watch chips designed specifically for driving LCDs are now available. But what if you want to interface a circuit of your own design with an LCD? One approach is shown in Fig. 2.

Here a 4543 CMOS BCD-to-7-segment latch/decoder/driver is used to drive a 7-segment LCD display. Though the 4543 can drive various kinds of displays, it is specifically designed for driving LCD readouts.

When used to drive an LCD display, a square wave must be applied to both the phase input (pin 6) of the 4543 and the backplane of the LCD readout. This provides the ac driving voltage required by the LCD. The decoded outputs from the 4543 are connected directly to the LCD segment pins.

Note how two NAND gates are used in Fig 2 to make a simple square-wave generator for the 4543. Be sure to tie unused inputs to  $V_{DD}$  or ground. For more information about the 4543, see the data sheets published by Motorola, National and other CMOS companies. The National data sheet for its version of the 4543 (CD4543BM/CD4543BC) includes the circuit for a  $3\frac{1}{2}$ -digit DVM with an LCD readout.

**Reader Mail.** I would like to thank Walter S. Swift of Boynton Beach, FL, for suggesting the topic of this month's column. Walter wrote to ask about the possibility of a liquid-crystal indicator much like the LED variety. He then suggested I cover the subject of liquid crystals in a forthcoming column.

As a result this month's column does not include news about new chips and components. And we've just barely begun to cover the subject of LCDs. For more information about liquid crystals, consult the hundreds of articles and technical papers on the subject that have been published since about 1965. You will find most of them at your local library. If you would like to suggest a topic for coverage in this column, jot down your idea on a postcard. Though the volume of mail makes it impossible to answer individual suggestions, I will try to schedule coverage of the suggestions which have potential. ◇

# EXPERIMENTER'S CORNER

By Forrest M. Mims

## Experimenting With a Joystick Part 2. Some Typical Applications

**L**AST MONTH we learned about and experimented with a few basic applications for single and two-axis joysticks. This month we'll experiment with several analog and digital applications for this very important type of control device.

The joystick I used with the experimental circuits we'll be discussing, each of which I assembled and tested, contains two 100-k $\Omega$  linear-taper potentiometers. It's available from Radio Shack for \$4.95 (catalog number 271-1705). Other joysticks may also be used. Those containing logarithmic-taper potentiometers, for example, are usually better suited for audio control applications. Other sources for joysticks include some of the mail order electronic parts supply companies that advertise in this magazine. You can also salvage used joysticks from discarded video games and remote-control transmitter systems.

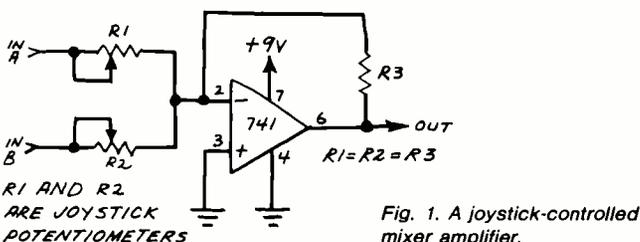
**A Joystick-Controlled Mixer.** A summing amplifier allows two or more signals to be simultaneously amplified. When used for audio applications, summing amplifiers are usually called *mixers*.

A typical mixer is a relatively simple circuit that allows two or more microphones to be connected to a single power amplifier. The mixer may or may not include a stage of preamplification. More elaborate mixers have half a dozen or more inputs, each having its own gain and, perhaps, other signal controls.

You can make a simple, but functional mixer by connecting several resistors, one for each channel, to one of the inputs of an operational amplifier. If potentiometers are used instead of fixed resistors, it is possible to control the amplitude of the signal from each channel.

This is where the joystick comes in. Normally, a separate potentiometer is required for each channel. Since the joystick incorporates two or four pots in a single structure, the signal amplitude of two to four channels can be controlled by moving a single control.

Figure 1 shows a very simple joystick-controlled mixer. The simplicity of this circuit will allow you to appreciate the ver-

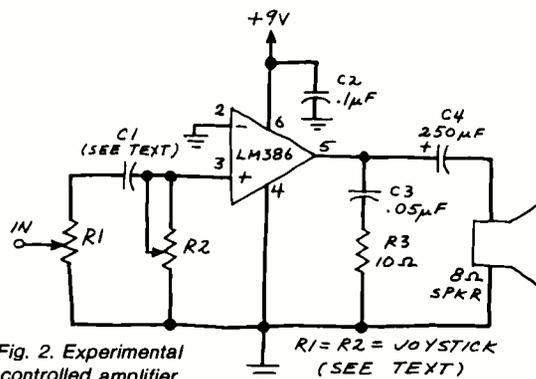


satility of a dual-channel mixer having a single mixing control. For example, I fed two audio channels (music and a tone) into the two inputs and was able to control the mixture with one hand while making other changes with the other hand.

I used a joystick containing 100-k $\Omega$  linear-taper potentiometers in this circuit. Logarithmic-taper pots would be better. You can try the basic resistive summer with many different op amps and audio amplifier chips. Joysticks containing four pots are hard to find, but they permit four channels to be simultaneously controlled.

**Joystick-Controlled Tone and Amplitude.** Figure 2 shows an experimental joystick tone- and amplitude-controlled amplifier. In operation,  $R1$  controls the amplitude of the signal while  $C1$  and  $R2$  serve as a simple, but adjustable, tone-control filter.

To experiment with this circuit, connect an audio-frequency tone source to the input. A sine wave works better than fast-rising square waves. Move the stick so that  $R1$  is rotated and the amplitude of the tone from the speaker will be altered.



Move the stick so that  $R2$  is rotated and the amplitude of the tone will also be altered; but in this case, the change in amplitude is dependent upon the frequency of the incoming signal.

The first-order, high-pass Butterworth filter formed by  $C1$  and  $R2$  has a half-power frequency cutoff that is the reciprocal of  $6.28R2C1$  or  $0.159/R2C1$ . The actual cutoff frequency you obtain may differ from that predicted by the formula. For example, when  $R2$  is 50,000 ohms and  $C1$  is 0.0001  $\mu$ F, the formula predicts a half-power frequency cutoff of 3,185 Hz. I measured a half-power frequency cutoff of about 1,000 Hz.

In any event, this simple circuit demonstrates how a single control can alter both the gain and frequency response of an amplifier. If you wish to design your own joystick-controlled amplifiers, there are several excellent books which contain helpful design guidelines. Among them are: *The Design of Active Filters, With Experiments* by Howard Berlin (E&L Instruments, 1977); *Handbook of Operational Amplifier Circuit Design* by David Stout and Milton Kaufman (McGraw-Hill, 1976); and Don Lancaster's *Active-Filter Cookbook* (Sams, 1975). You can find these and other books on the design of suitable filters in better libraries. You can also find some design information in application notes published by various op-amp manufacturers. See, for example, National's *Audio/Radio Handbook* and *Linear Applications Handbook*. (Due to the volume of mail I receive, I will be unable to design custom joystick-controlled circuits for individual readers.)

**Dual-Tone Mixer.** Figure 3 is an experimental joystick-controlled dual-tone mixer with which I've been experimenting. The circuit is designed around a 556 dual timer chip.

The 556 is functionally identical to two 555 timers on a single chip. Each half of the circuit in Fig. 3 is connected as an

astable multivibrator whose frequency of oscillation is controlled by one of the two 100-kΩ potentiometers in a joystick. The frequency is also controlled by C1 and C2.

Both halves of the circuit will oscillate without C3 and C4; but the capacitors are necessary to prevent uncontrolled interactions between the two oscillators.

The output from each tone generator is coupled into a simple audio mixer made from R5, C6 and a 741 op amp. Potentiometer R5 also serves as a balance control.

Capacitor C5 is not part of the mixer, but serves to stretch the fast rising and falling pulses from the two tone generators. This causes the sounds produced by this circuit to be more tolerable than they normally are.

For initial experiments, connect an external amplifier to pin 6 of the 741. The values of C1 and C2 should be similar or identical. Move the joystick off center, apply power and adjust R5 until the two simultaneous tones have approximately the same amplitude. You can then move the joystick in various directions to produce a wide range of unusual tone combinations.

The tones produced by this circuit are for the most part very unpleasant. Nevertheless, the circuit nicely demonstrates how a single control device can regulate two independent circuits. If you would like to follow up with circuit ideas of your own, consider substituting the 4046 CMOS phase-locked loop for the 556 (see the July and August 1980 installments of this column). The following circuit shows just one way the 4046 can be used in conjunction with a joystick.

**Percussion Synthesizer.** The sound produced by percussion instruments such as drums and wood blocks can be simulated by a damped op-amp oscillator. Changing one or two component values permits a variety of percussion sounds to be synthesized.

The circuit in Fig. 4 permits many different percussion

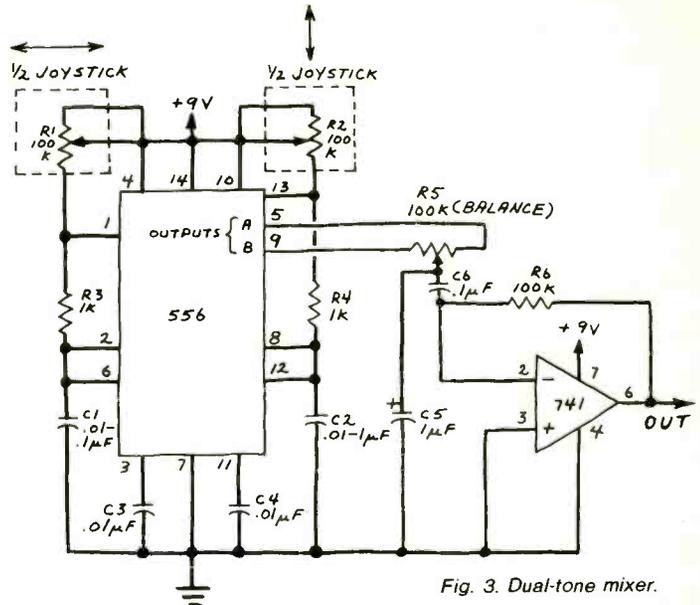


Fig. 3. Dual-tone mixer.

sounds to be generated under complete manual control. Percussion synthesizers described previously in this column operate when a switch is closed and released. The percussion effects produced by the circuit in Fig. 4 can be controlled in real time simply by moving a joystick back and forth. Wide, circular movements of the joystick produce sounds ranging from that emitted by a plucked violin string to a tapped glass. Various bell-like sounds can also be produced.

Referring to Fig. 4, note that R1 is the horizontal-axis pot in a two-pot joystick and is connected as a voltage divider

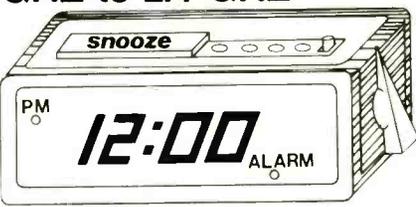


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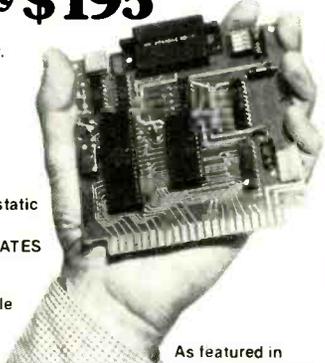
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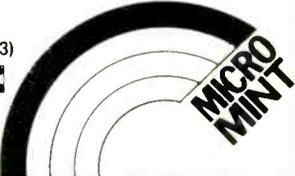


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which provides a variable voltage to the input of the voltage-controlled oscillator (vco) section of a 4046 phase-locked loop. The vco output is coupled into a 741 op amp through gain control *R2*. Capacitor *C3* enhances the percussion sounds by stretching the incoming signal from the 4046.

The vertical-axis pot in the joystick, *R3*, can be adjusted along with *R2* to alter the gain of the 741. Experimenting with the resistance of *R2* will help provide a full range of percussion sounds. The sounds themselves are produced when the joystick is moved in various patterns.

To create percussion sounds, for example, orient the joystick so that back and forth movements control the amplitude pot (*R3*). Push the stick away to its outermost limit, and the sound will cease as the noninverting input of the 741 is brought to ground through *R2*. Pull the stick straight back to achieve the percussion effect. You'll hear a sudden tone followed by rapid damping as the stick reduces the amplitude of the signal. For different tone frequencies, move the stick to the left or right before pulling it back. This will let you quickly change from plucking sounds to bells, etc.

If the joysticks are connected as shown in Fig. 4, the sound will cease when the stick is pulled back and rotated to the left. This allows you to fully recover and prepare for a new sound cycle without the presence of an intermediate (and highly distracting) tone.

For tinkling bell or plucked string effects, rapidly rotate the stick in a clockwise direction. You can experiment with the values of *C1* and *C3* and of *R2* for different effects.

**Digitizing the Output from a Joystick.** Joysticks provide a very important interface between computers and their operators. The practical implementation of this interface generally requires some form of analog-to-digital conversion.

Some joystick interfaces utilize software to detect the various tones produced by a joystick-controlled tone generator.

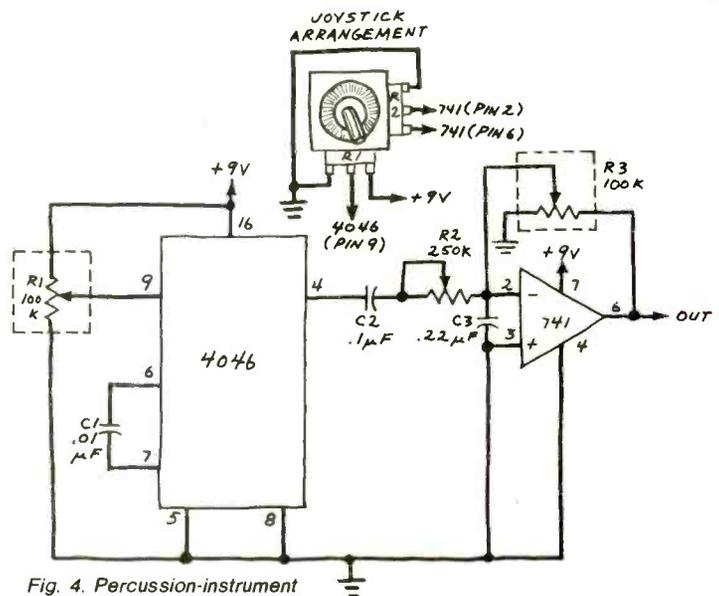


Fig. 4. Percussion-instrument synthesizer.

Figure 5 shows a straightforward hardware approach. This circuit digitizes the status of one of the pots in a joystick. The joystick pot is connected as a voltage divider which provides a variable voltage to the input of the analog-to-digital (A/D) converter chip. Many different A/D chips can be used in this application. The 8703, a product of Teledyne Semiconductor (1300 Terra Bella Ave., Mountain View, CA 94043), is a 24-pin DIP which provides 8 bits of conversion. Since the chip is a CMOS device, its power dissipation is a very low 20 mW.

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The 8703 has three-state outputs and is thus ideally suited for connection to microcomputer buses. Pin 24 controls the outputs; when it is low, the outputs are enabled. When pin 24 is high, the outputs assume the high impedance (off) state.

Eight bits per joystick pot is probably all or even more than you will need. If you need even higher resolution, you can select the 8704 (10-bit) or 8705 (12-bit) chips. These chips are essentially identical to the 8703 with the addition of more outputs.

When the circuit in Fig. 5 is connected as shown, the output will indicate an 8-bit binary count of 0000 0000 to 1111 1111 as the joystick pot is rotated from one extreme to another. Since joystick pots are usually limited by a plastic or metal template to only a portion of their maximum rotation, you may not obtain the full range of conversion. However, you can connect the joystick pot to a higher voltage to enhance the overall range.

Having spent some time experimenting with the circuit in Fig. 5, I would urge you to obtain the 8703 data sheet before using the chip in an actual circuit. ADC chips are sometimes a little tricky to use (certainly more so than op amps and unlocked logic), and the data sheet will help you understand their operation.

For example, you can vary the rate at which the 8703 performs conversions by connecting an external clock to pin 21. Each time the clock sends a positive pulse to pin 21, the 8703 will begin a new conversion cycle unless it is busy performing an existing cycle. When pin 21 is connected to  $V_{DD}$  (pin 19), the 8703 goes into a free-running mode in which conversions are automatically performed at a rate of approximately 800 per second.

**Decoding the Output from an ADC.** The binary output from a joystick with an ADC can be connected directly to a digital data bus. Or it can be decoded for a real-time indica-

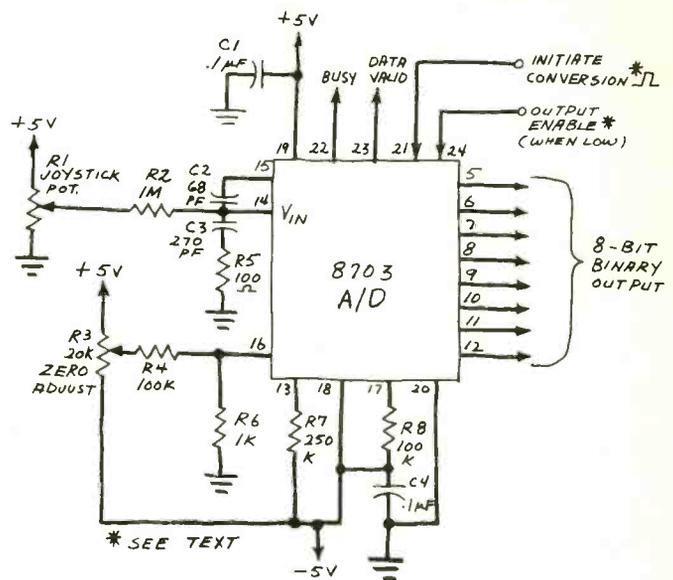


Fig. 5. Digitizing the output from a joystick with an A/D converter.

tion of the position of the joystick. Figure 6 shows one way to decode half the output from the 8703 A/D converter.

The 4049 buffers provide CMOS-to-TTL interfacing for the 8703. (The 8703 will interface directly with CMOS and LS chips.) The 74154 is a 4-to-16-line decoder which transforms a 4-bit input into its hexadecimal equivalent. When the joystick is swept from one extreme to another, the LEDs light

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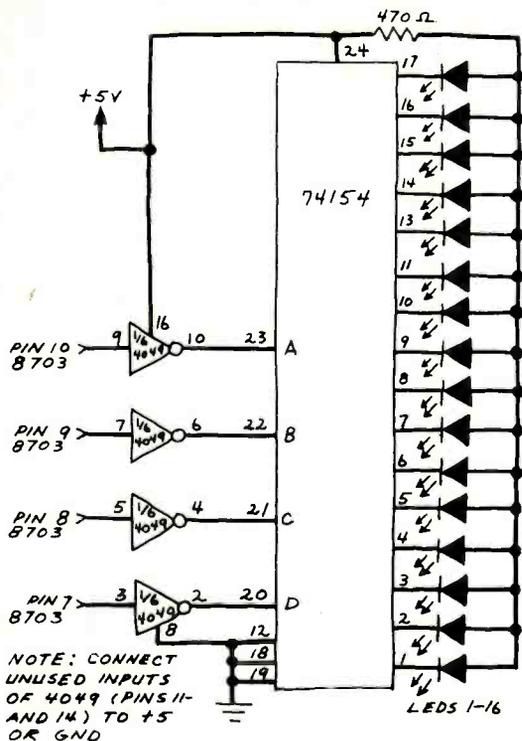


Fig. 6. How to decode the four lowest-order bits from the 8703 A/D.

up in succession. By placing the axis of the joystick pot perpendicular to the row of LEDs, the position of the glowing LED will track the position of the joystick.

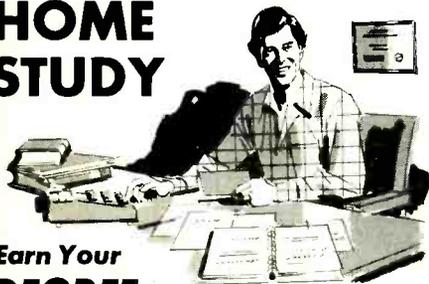
**Going Further.** This two-part series on joysticks has provided only a hint at what you can do with these important interface devices. Now that you know something about them, perhaps you can think of some circuits and applications of your own.

For example, try connecting a pair of circuits like the one in Fig. 5 to both pots in a two-axis joystick. This will provide you with a digitized indication of the real position of the joystick. You can place this data on a 16-bit microcomputer bus. Or you can place the same data on an 8-bit bus by using the four highest-order bits from each A/D.

You might want to experiment with applications which do not involve manual movement of the joystick. For example, if the joystick is inverted and connected to a weight, it will indicate movements of the potentiometer assembly by appropriate changes in resistance (or voltage). This application can be used in automatic leveling equipment, low-frequency vibration detection and anywhere you desire to measure the change in position or attitude of an object large enough to support the joystick assembly.

Finally, you might wish to design your own video games or radio-control devices using a joystick interface. One possibility for the latter application is a dual-tone two-channel control scheme. One pot varies the tone for one channel from, say, 50 to 500 Hz while the second pot varies the tone for a second channel from, say, 5,000 to 10,000 Hz. The receiver is designed with two output stages, one of which passes only the low-frequency tone range and the second only the high-frequency tone range. Separate frequency-to-voltage converters at each output provide the desired control signals. This, of course, is only one possibility. You can think of others. ♦

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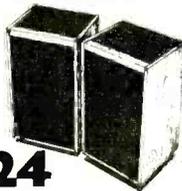
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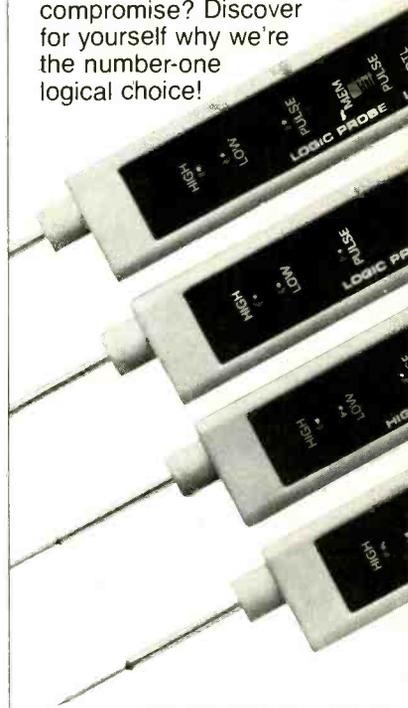
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# PROJECT OF THE MONTH

By Forrest M. Mims

## A Light-Sensitive Tone Generator

**I**F YOU like circuits which respond to light, you'll love the light-sensitive tone generator shown in Fig. 1. Most light-sensitive tone generators have a single light-sensitive component. The circuit in Fig. 1 has two. This provides an unusual up-down audible tone.

In operation, the 741 is connected as an oscillator which produces a clipped sine wave at its output. The output waveform therefore resembles a square wave with slow rise and fall times.

Photocells *PC1* and *PC2* control the frequency of oscillation of the 741. For best results, use cadmium sulfide photocells having a very low resistance when illuminated and a high dark resistance. Most of the photocells available from electronics part suppliers that cater to hobbyists and experimenters fall in this category.

When both *PC1* and *PC2* are dark, the frequency of the 741 oscillator will fall to about 700 Hz. If *PC1* is illuminated with a small flashlight while *PC2* remains dark, the frequency will suddenly fall to a few Hz and then rise quickly to 1 kHz or more.

If *PC2* is illuminated with the flashlight while *PC1* remains dark, the frequency will suddenly rise to 3 kHz or more. When both *PC1* and *PC2* are illuminated, the frequency will level off at about 1.2 kHz.

I made these measurements when the wiper of balance potentiometer *R3* was at its center position. The setting of *R3* can be altered to produce a wider range of tone frequencies.

The signal from the 741 is converted into an audible tone by an

LM386 audio amplifier. Potentiometer *R4* serves as a gain control. When you first apply power to the circuit, make sure *R4*'s wiper is rotated toward the ground connection. Otherwise the tone from the speaker may be uncomfortably loud. **Caution:** Don't substitute an earphone for the speaker! The sound level may be too high.

This circuit has more than its obvious novelty value. You can, for example, use it as a light-controlled sound effects generator. For this application both photocells should be placed at one end of individual plastic or cardboard tubes which have been coated on their inside with black paper or paint. The circuit can then be "played" by flashing light down the tubes or by blocking a continuous light source with your finger tips.

The circuit has educational value as well. It nicely demonstrates the feedback which makes possible the conversion of an amplifier into an oscillator. And it demonstrates the "memory effect" of cadmium sulfide photocells. You'll quickly notice this phenomenon while experimenting with the circuit. A cadmium sulfide photocell requires several seconds or even minutes to resume equilibrium following the removal of light, and this gives rise to a gradually changing tone even when the light source is removed.

Finally, you can replace the two photocells with two potentiometers in a joystick to obtain full manual control of the circuit. Pushing the stick back and forth will sweep the output frequency across its full spectrum. Moving the stick in a circular fashion will produce a sound like a siren. ♦

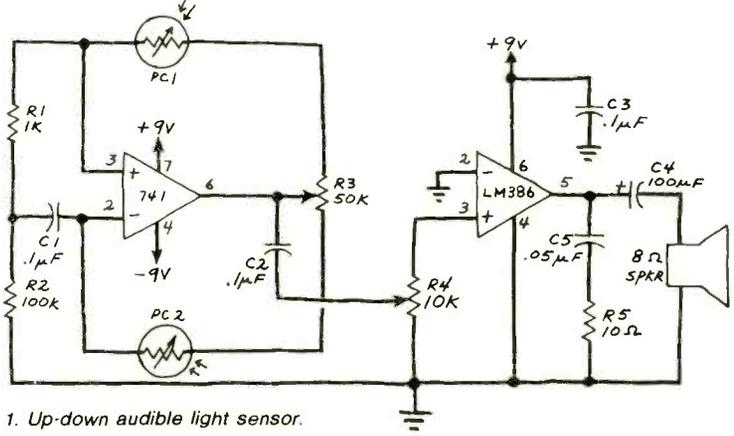


Fig. 1. Up-down audible light sensor.

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CD4002BE	18	CD4041BE	89	CD4510BE	58
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7402N	.24	7442N	.44	74109AN	.44	74161AN	.64	74199N	1.55	74S09N	.86	74S158N	1.16	74S299N	6.54	74LS30N	.18	74LS122N	.48	74LS174N	.44	74LS266N	.36	74LS378N	.99
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7408N	.26	7453N	.29	74122N	.54	74170N	1.96	74273N	2.39	74S32N	.89	74S174N	1.06	74S472N	8.45	74LS48N	.99	74LS136N	.42	74LS192N	.64	74LS290N	.72	74LS490N	1.89
7409N	.28	7454N	.29	74123AN	.48	74172N	4.95	74276N	2.19	74S37N	.99	74S175N	1.06	74S474N	9.65	74LS51N	.24	74LS138N	.48	74LS193N	.64	74LS293N	.44	74LS530N	85.00
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**#ALCP (10 per pack) . \$2.95/pkg.**

## JE215 Adjustable Dual Power Supply

General Description: The JE215 is a Dual Power Supply with independent adjustable positive and negative output voltages. A separate adjustment for each of the supplies provides the user unlimited applications for IC current voltage requirements. The supply can also be used as a general all-purpose variable power supply.

**FEATURES:**  
 • Adjustable regulated power supplies, pos. and neg. 1.2VDC to 15VDC.  
 • Power Output (each supply): 5VDC @ 500mA, 10VDC @ 750mA, 12VDC @ 500mA, and 15VDC @ 175mA.  
 • Two, 3-terminal adj. IC regulators with thermal overload protection.  
 • Heat sink regulator cooling.  
 • LED "on" indicator.  
 • Printed Board Construction.  
 • 120VAC input.  
 • Size: 3 1/2" x 5 1/16" L x 2 1/8" H

**JE215 Adj. Dual Power Supply Kit (as shown) . \$24.95**  
 (Picture not shown but similar in construction to above)  
 JE200 Reg. Power Supply Kit (5VDC, 1 amp) . \$14.95  
 JE205 Adapter Bd. (to JE200) +5, -9 & +12V. \$12.95  
 JE210 Var. Pwr. Sply. Kit. +5-15VDC, to 1.5amp. \$19.95

# MICROPROCESSOR COMPONENTS

### 8080A/8080A SUPPORT DEVICES

IN5800A	CPU	4.95
DP212	8-Bit Input/Output	3.25
DP212	Priority Interrupt Controller	5.25
DP216	BI-Directional Bus Driver	3.49
OP224	Clock Generator/Driver	3.95
DP226	Bus Driver	3.49
DP228	System Controller/Bus Driver	4.95
DP238	System Controller	5.95
IN5843	I/O Expander for 48 Series	9.95
IN5820	Asynchronous Counter Element	6.95
DP251	Prog. Comm. I/O (USART)	6.95
DP251	Prog. Interval Timer	8.95
DP251	Prog. Parity/Int. (IO PPI)	5.95
DP257	Prog. DMA Control	9.95
DP259	Prog. Interrupt Control	9.95
DP275	Prog. CRT Controller	9.95
DP275	Prog. Keyboard/MOUSE Interface	9.95
DP303	System Timing Element	6.95
DP304	8-Bit BI-Directional Receiver	3.95
DP307	8-Bit BI-Directional Receiver	3.95
DP308	8-Bit BI-Directional Receiver	3.95
DP310	Octal Latched Peripheral Driver	5.25
DP311	Octal Latched Peripheral Driver	5.25

### 6800/6800 SUPPORT DEVICES

MC6800	MPU	7.95
MC6802	MPU with Clock and RAM	14.95
MC6802A	128-B Static RAM	4.95
MC6821	Peripheral Inter. Adapt. (MC6800)	7.49
MC6828	Priority Interrupt Controller	12.95
MC6834	1024-Bit ROM (MC6800-8)	14.95
MC6850	Asynchronous Comm. Adapter	6.95
MC6852	Synchronous Serial Data Adapter	6.95
MC6859	240bps Modulator	12.95
MC6860A	Quad 3-State Bus Trans. (MC1726)	2.25

### MICROPROCESSOR CHIPS

Z80 (80C81)	CPU (1Mx8K/256K) (4MHz)	11.95
Z80A (780-1)	CPU (1Mx8K/256K) (4MHz)	13.95
CDP1802	CPU	19.95
3850	MPU	36.95
IQM2801ADG	CPU—4-Bit Slice (Com. Temp. Grade)	19.95
MC6502	MPU w/Clock (64K Bytes Memory)	11.95
IN5803N-4	MPU—8-Bit (6MHz)	7.95
IN5803N-4	CPU—581 Chip-8-Bit (128Bytes RAM)	14.95
IN5804N-4	CPU (256 Bytes RAM)	24.95
IN5807N	CPU—64 Bytes RAM	24.95
IN5807N	CPU w/Basic Micro Interpreter	29.95
IN5808	CPU	24.95
TM5990U-L	MPU—16-Bit	5.95

### SHIFT REGISTERS

MM502H	Dual 25-Bit Dynamic	5.00
MM503H	Dual 50-Bit Dynamic	5.00
MM504H	Dual 100-Bit Dynamic	5.00
MM510H	Dual 64-Bit Accumulator	4.95
MM442N	256-Bit Dynamic	2.95
MM502N	128-Bit Dynamic Accumulator	1.95
MM503H	500/512-Bit Dynamic	1.95
MM503N	Octal 80-Bit	9.95
MM503N	Octal 80-Bit	9.95
2604V (104A)	104-Bit Dynamic	1.95
2118N	Hex 32-Bit Static	3.95
262V	Dual 128-Bit Static	2.95
2524V	512-Bit Dynamic	2.95
2525V	1024-Bit Dynamic	2.95
2627V	Dual 256-Bit Static	2.95
2628V	Dual 256-Bit Static	2.95
2529V	Dual 2048-Bit Static	4.95
2629N	Quad 80-Bit Static	2.95
31AC1C	Fifo (Dual 8)	6.95

### DATA ACQUISITION

AF100-1CN	Universal Active Filter 23K	5.95
AF100-1CJ	Touch One Low Band Filter	19.95
AF102-1CJ	Touch Tone High Band Filter	19.95
LM393CH	Power Gain Op Amp	1.15
LM332Z	Constant Current Source	1.30
LM332Z	Temperature Transducer	1.40
LP358N	JFET Input Op Amp	1.10
OP226	Sample & Hold Amplifiers	3.95
LM399H	Temp. Comp. Prec. Ref. (5ppm/C)	5.00
AOC080ALCN	8-Bit A/D Converter (1 LSB)	4.95
DAC080ALCN	8-Bit D/A Converter (0.7% Lin.)	2.25

### TELEPHONE KEYPAD CHIPS

AV-5100	Push Button Telephone Dialer	14.95
AV-5200	Rotary Dialer	14.95
AV-5950	CMOS Clock Encoder	4.95
AV-5215	Microprocessor (48 keys)	7.95
HD00655	Keyboard Encoder (16 keys)	4.95
7K922	Keyboard Encoder (16 keys)	5.49
SR1500	Push Button Encoder (20 keys)	5.95
MM5330N	Push Button Pulse Dialer	7.95
MM5349N	16/14-Key Serial Keyboard Encoder	8.95

## JE608 PROGRAMMER

### 2704/2708 EPROM PROGRAMMER

**GENERAL APPLICATIONS:**  
 • EPROM program EPROM 2704 and 2708.  
 • Developmental system for microcomputer circuits.  
 • To read the contents of a pre-programmed EPROM.  
 • To compare EPROM differences.  
 • To emulate a programmed EPROM.  
 • To store program in RAMS for alterations.  
 • Three separate Display Registers: 8 LED's for Hex Key entries, 10 LED's (2x5) for Address Register and 8 LED's for Data Memory Register. The Data Memory Register displays the contents of the RAMs from the EPROM Chip. Development of microprocessor systems by means of a ribbon cable from the programmer panel test socket to the EPROM socket on the microprocessor board. Rapid checking verification of programmed data changes. User may move data from a master to RAM or write into RAM with keyboard entries. Allows manual (up and down) and auto address programming. Stand alone EPROM Programmer consisting of: A 16-Key Hexadecimal keyboard assembly, Programmer Board assembly with 4 power supplies and a LED's Assembly. The Test Socket is zero force insertion type. Power requirement: 115VAC, 60Hz, 6W. Compact desk-top enclosure. Color coordinated design in a case with light lens panels and molded end pieces in mocha brown. Size: 3 1/4" x 11" x 8 1/4" D. Weight: 5.5 lbs.

The JE608 EPROM Programmer is a completely self contained unit which is independent of computer control and requires no additional systems for its operations. The EPROM can be programmed from the Hexadecimal Keyboard or from a pre-programmed EPROM. The JE608 Programmer can emulate a programmed EPROM by the use of its internal RAM circuits. This will allow the user to test or create a program for a system, prior to programming a chip. Any changes in the program can be entered directly into the memory circuits with the Hexadecimal Keyboard so that rewriting the entire program will not be necessary. The JE608 Programmer contains a Programmable Data Register (PDR) which includes power supplies of -5V, +5V, +12V and +26V. The Hexadecimal Keyboard and LED's/Test Socket Panel boards are separate assemblies within the system.

**JE608K Kit . \$399.95**  
**JE608A Assembled and Tested . \$499.95**

## JE608-16K ADAPTER BOARD

### FOR 2716/2758 EPROMS

**GENERAL DESCRIPTION:**  
 The JE608-16K Adapter Board allows the JE608 Programmer to be modified for the additional programming of the 2716 and 2758 EPROMs. The adapter provides for adding an address switch for the 2<sup>nd</sup> bit and also for selecting the proper power and timing pulse to be applied to the EPROM. Programming and emulating the 2716 (16K) EPROM is done separately to each bit of the EPROM because of the 16K RAM capacity in the JE608 Programmer.

**JE608-16K Adapter Board Kit . \$29.95**  
**JE608-16K (Send assembled JE608 to factory for adapter installation of the JE608-16K Adapter Board Kit) . \$99.95**  
**JE608A-16K Mod. Assembled JE608 w/Adapter (JE608-16K) Installed . \$599.95**

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### BOOKS

National Semiconductor—Intell—Intel

30001	National CMOS Data Book (640 pages) 74C, CD4000, and A/D Converters	\$6.95
30002	National Interface Data Book (724 pages) DP, DS8000, DS80C, DS7500, etc.	\$6.95
30003	National Linear Data Book (1276 pages) LM, LF, ACC, DAC, LH Series	\$4.95
30004	National Series 80—Board Level Computer (224 pages)	\$4.95
30006	National TTL Logic Data Book (824 pages) 7400, LS, L, H, S, and DM8000 Series	\$6.95
30006	Buy above (3) 30001, 3, 5 as a set	\$19.95/lot
30009	Intel Interface Data Book (1074 pages)	\$6.95
010400	Intel Component Data Catalog (DS7500)	\$10.00

Full data sheets for Intel's products include: memory devices, microprocessors, peripherals & industrial micro products (1328 pages)  
 Intel Peripheral Design Handbook (128 pages) \$7.50  
 Full data sheets, appl. notes for Intel peripheral device components (644 pages)

## AC and DC Wall Transformers

Ideal for use with clocks, games, power supplies or any other type of AC or DC application.

Part No.	Input	Output	Price
AC 2-250	117V/60Hz	12 VAC 250mA	\$3.95
AC 500	117V/60Hz	12 VAC 500mA	\$4.95
AC1000	117V/60Hz	12 VAC 1 amp	\$5.95
AC1700	117V/60Hz	9 VAC 1.7 amp	\$3.95
DC 9000	117V/60Hz	9 VDC 200mA	\$3.25
DC 2500	120V/60Hz	9 VDC 500mA	\$3.95

## CONNECTORS

DB25P	D-Subminiature Plug	\$2.95
DB25S	D-Subminiature Socket	\$3.50
D20418-2	Screw Lock Hdw. (2) DB25S/P	2/\$9.99
DB51226	Cover for DB25P/S	\$1.75
D22/44SE	P.C. Edge (22/44 Pin)	\$2.95
UG88/U	BNC Plug	\$1.79
UG89/U	BNC Jack	\$3.79
UG175/U	UH Adapter	\$ 4.49
S0239	UH Panel Recp.	\$1.29
PL258	UH Adapter	\$1.60
PL759	UH Adapter	\$1.60
UG260/U	BNC Plug	\$1.79
UG1094/U	BNC Bulkhead Recp.	\$1.29

## TRS-80 16K Conversion Kit

Expand your 4K TRS-80 System to 16K.  
 Kit comes complete with:  
 \* 8 ea. MM5290 (UPD416/4116) 16K Dyn. Rams (\*NSI)  
 \* MM5290 for Conversion

TRS-16K2 *150NS	\$29.95
TRS-16K3 *200NS	\$24.95
TRS-16K4 *250NS	\$19.95

## JE610 ASCII

### Encoded Keyboard Kit

The JE610 ASCII Keyboard Kit can be interfaced into most any computer system. The kit comes complete with an industrial grade keyboard switch assembly (62-key), IC's, sockets, connector, electronic components and a double-sided printed wiring board. The keyboard assembly requires +5V @ 150mA and -12V @ 10 mA for operation. Features: 60 keys generate the 126 characters, upper and lower case ASCII set. Fully bi-directional. Two user-definable keys provided for custom applications. Caps lock for upper-case only alpha characters. Outputs directly compatible with TTL/DTL or MOS logic arrays. Easy interfacing with a 16-pin DIL or 18-pin edge connector. Size: 3 1/4" x 14 1/2" x 8 3/4" D.

**JE610/DTE-AK (After assembled as pictured above) . \$124.95**

**JE610 Kit 62-Key Keyboard, PC Board, & Components (no case) . \$ 79.95**  
**DTE-AK 62-Key Keyboard (Keyboard only) . \$ 34.95**  
**DTE-AK (case only—3 1/4"X14 1/2"X8 3/4" D) \$ 49.95**

## JE610 Negative 12VDC Adapter Board Kit

### FOR JE610 ASCII KEYBOARD KIT

Provides -12V DC from incoming 5VDC

**NEW! \$9.95**

## JE600 Hexadecimal Encoder Kit

Full 8-BIT LATCHED OUTPUT 19-KEY KEYBOARD

The JE600 Encoder Keyboard Kit provides two separate hexadecimal digits produced from sequential key entries to allow direct programming for 8-bit microprocessors or 8-bit memory circuits. Three additional keys are provided for user operations with one having a bistable output available. The outputs are latched and monitored with 9 LED readouts. Also included is a key entry strobe. Features: Full 8-bit latched output for microprocessor use. Three user-definable keys with one being bistable operation. Debounce circuit provided for all 19 keys. 9 LED readouts to verify entries. Easy interfacing with standard 16-pin IC connector. Only +5VDC required for operation. Size: 3 1/4" x 8 3/4" x 8 3/4" D.

**JE600/DTE-HK (After assembled as pictured above) . \$99.95**

**JE600 Kit 19-Key Hexadec. Keyboard, PC Board & Cmpnts. (no case) . \$59.95**  
**K19 19-Key Keyboard (Keyboard only) . \$14.95**  
**DTE-HK (case only—3 1/4"X8 3/4"X8 3/4" D) \$44.95**

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## INTEGRATED CIRCUITS

7400 TTL	74LS00 TTL	74S00 TTL	74ALS00 CMOS
7400	74LS00	74S00	74ALS00
7401	74LS01	74S01	74ALS01
7402	74LS02	74S02	74ALS02
7403	74LS03	74S03	74ALS03
7404	74LS04	74S04	74ALS04
7405	74LS05	74S05	74ALS05
7406	74LS06	74S06	74ALS06
7407	74LS07	74S07	74ALS07
7408	74LS08	74S08	74ALS08
7409	74LS09	74S09	74ALS09
7410	74LS10	74S10	74ALS10
7411	74LS11	74S11	74ALS11
7412	74LS12	74S12	74ALS12
7413	74LS13	74S13	74ALS13
7414	74LS14	74S14	74ALS14
7415	74LS15	74S15	74ALS15
7416	74LS16	74S16	74ALS16
7417	74LS17	74S17	74ALS17
7418	74LS18	74S18	74ALS18
7419	74LS19	74S19	74ALS19
7420	74LS20	74S20	74ALS20
7421	74LS21	74S21	74ALS21
7422	74LS22	74S22	74ALS22
7423	74LS23	74S23	74ALS23
7424	74LS24	74S24	74ALS24
7425	74LS25	74S25	74ALS25
7426	74LS26	74S26	74ALS26
7427	74LS27	74S27	74ALS27
7428	74LS28	74S28	74ALS28
7429	74LS29	74S29	74ALS29
7430	74LS30	74S30	74ALS30
7431	74LS31	74S31	74ALS31
7432	74LS32	74S32	74ALS32
7433	74LS33	74S33	74ALS33
7434	74LS34	74S34	74ALS34
7435	74LS35	74S35	74ALS35
7436	74LS36	74S36	74ALS36
7437	74LS37	74S37	74ALS37
7438	74LS38	74S38	74ALS38
7439	74LS39	74S39	74ALS39
7440	74LS40	74S40	74ALS40
7441	74LS41	74S41	74ALS41
7442	74LS42	74S42	74ALS42
7443	74LS43	74S43	74ALS43
7444	74LS44	74S44	74ALS44
7445	74LS45	74S45	74ALS45
7446	74LS46	74S46	74ALS46
7447	74LS47	74S47	74ALS47
7448	74LS48	74S48	74ALS48
7449	74LS49	74S49	74ALS49
7450	74LS50	74S50	74ALS50
7451	74LS51	74S51	74ALS51
7452	74LS52	74S52	74ALS52
7453	74LS53	74S53	74ALS53
7454	74LS54	74S54	74ALS54
7455	74LS55	74S55	74ALS55
7456	74LS56	74S56	74ALS56
7457	74LS57	74S57	74ALS57
7458	74LS58	74S58	74ALS58
7459	74LS59	74S59	74ALS59
7460	74LS60	74S60	74ALS60
7461	74LS61	74S61	74ALS61
7462	74LS62	74S62	74ALS62
7463	74LS63	74S63	74ALS63
7464	74LS64	74S64	74ALS64
7465	74LS65	74S65	74ALS65
7466	74LS66	74S66	74ALS66
7467	74LS67	74S67	74ALS67
7468	74LS68	74S68	74ALS68
7469	74LS69	74S69	74ALS69
7470	74LS70	74S70	74ALS70
7471	74LS71	74S71	74ALS71
7472	74LS72	74S72	74ALS72
7473	74LS73	74S73	74ALS73
7474	74LS74	74S74	74ALS74
7475	74LS75	74S75	74ALS75
7476	74LS76	74S76	74ALS76
7477	74LS77	74S77	74ALS77
7478	74LS78	74S78	74ALS78
7479	74LS79	74S79	74ALS79
7480	74LS80	74S80	74ALS80
7481	74LS81	74S81	74ALS81
7482	74LS82	74S82	74ALS82
7483	74LS83	74S83	74ALS83
7484	74LS84	74S84	74ALS84
7485	74LS85	74S85	74ALS85
7486	74LS86	74S86	74ALS86
7487	74LS87	74S87	74ALS87
7488	74LS88	74S88	74ALS88
7489	74LS89	74S89	74ALS89
7490	74LS90	74S90	74ALS90
7491	74LS91	74S91	74ALS91
7492	74LS92	74S92	74ALS92
7493	74LS93	74S93	74ALS93
7494	74LS94	74S94	74ALS94
7495	74LS95	74S95	74ALS95
7496	74LS96	74S96	74ALS96
7497	74LS97	74S97	74ALS97
7498	74LS98	74S98	74ALS98
7499	74LS99	74S99	74ALS99
7500	74LS100	74S100	74ALS100

## TEXAS INSTRUMENTS I.C. SOCKETS

SOCKET	IC	SOCKET	IC
7400	74LS00	7400	74LS00
7401	74LS01	7401	74LS01
7402	74LS02	7402	74LS02
7403	74LS03	7403	74LS03
7404	74LS04	7404	74LS04
7405	74LS05	7405	74LS05
7406	74LS06	7406	74LS06
7407	74LS07	7407	74LS07
7408	74LS08	7408	74LS08
7409	74LS09	7409	74LS09
7410	74LS10	7410	74LS10
7411	74LS11	7411	74LS11
7412	74LS12	7412	74LS12
7413	74LS13	7413	74LS13
7414	74LS14	7414	74LS14
7415	74LS15	7415	74LS15
7416	74LS16	7416	74LS16
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7470	74LS70	7470	74LS70
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7487	74LS87	7487	74LS87
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7491	74LS91	7491	74LS91
7492	74LS92	7492	74LS92
7493	74LS93	7493	74LS93
7494	74LS94	7494	74LS94
7495	74LS95	7495	74LS95
7496	74LS96	7496	74LS96
7497	74LS97	7497	74LS97
7498	74LS98	7498	74LS98
7499	74LS99	7499	74LS99
7500	74LS100	7500	74LS100

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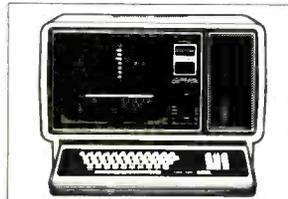
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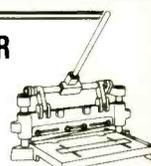
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**Modem Kit \$60.00**

State of the art, orig., answer. No tuning necessary, 103 compatible 300 baud. Inexpensive acoustic coupler plans included. Bd. Only \$17.00. Article in June Radio Electronics.

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16 bit I/O. 2 MHz clock. 2K RAM. ROM Breadboard space. Excellent for control. Bare Board \$28.50. Full Kit \$99.00. Monitor \$30.00. Power Supply Kit \$35.00. Tiny Basic \$30.00

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# ELECTRONICS WORLD®

## Personal Electronics News

**ACCESS TO COMPUSERVE DATA NETWORK** will be available to owners of Sorcerer computers when they buy an Exidy Systems Terminal Pac™ cartridge. Through a marketing agreement between Exidy and Compuserve, the purchaser will receive one free hour of connect time between 6:00 p.m. and 5:00 a.m. weekdays and 24 hours on weekends. The Terminal Pac contains software to configure the Sorcerer as a standard ASCII terminal so that it can receive data from the network's offerings of news, home economics, entertainment, and personal computing services. The Exidy (Sunnyvale, CA 94086) terminal sells for \$99.

**THE FCC** has been asked by the National Association of Broadcasters to clarify and modify two provisions of its new radio operator licensing requirements. NAB favors delegating some of the duties of a chief operator to subordinates acting under the chief operator's supervision. It also asked the FCC to clarify whether AM directional stations must continue to have First Class duty operators. For example, if an operator's license is renewed as a General Radiotelephone License, will this be adequate for the holder to be considered the operator on duty? If so, would the station continue to be exempt from the proof of performance requirements?



**WEATHER PICTURES FROM SATELLITES** can be received on a display system developed by University of Minnesota scientists and marketed by SynSat Communications Inc. (Minneapolis, MN 55436). Selling for under \$7000 and including a two-meter dish antenna, the radio receiving and video display system will store satellite pictures on audio cassettes and has a 2X zoom feature to allow the user to examine satellite images closely. It can also display standard weather facsimile information from land-line sources.

**LOCAL NETWORK WITHOUT MODEMS** has been announced by ElectroSound Systems, Inc., Phoenix, AZ. (914-347-3707). Called the Data Loop Exchange, it brings data terminals, word-processing work stations or personal computers, or a combination of these devices together into a local area network. Range is a half-mile radius, which can be increased with loop extenders. The network interconnects or communicates with a common computer without requiring more software. Installation is simple, using twisted-pair loop wiring, thus eliminating the cost of modems and telephone lines. No protocol, line discipline, or mixing of data rates is necessary with the network. Designed for small systems, with an RS232C interface, the company's DLX10 System uses time-division multiplexing.

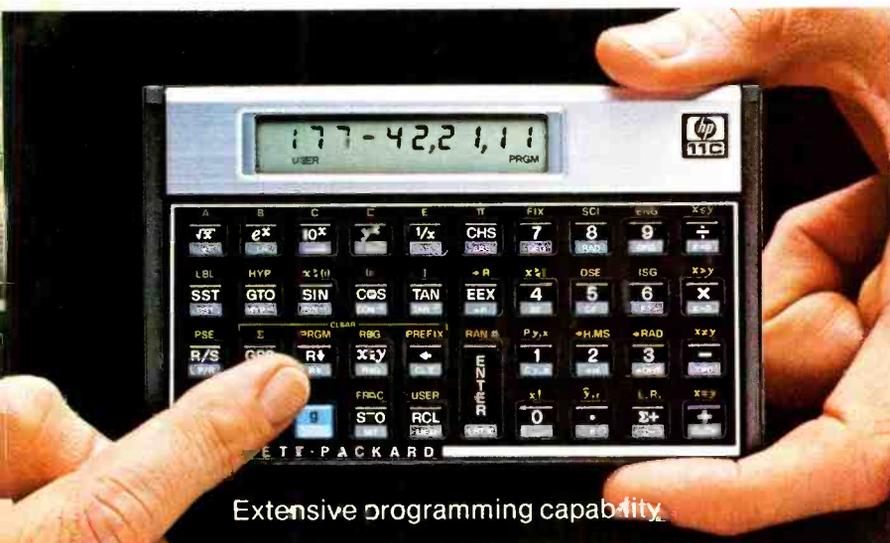
**VCR MOVIE RENTAL** from a local store will start in the spring of '82 when Warner Home Video launches new outlets to market its cassettes. Surveys made by WHV have shown that, for every video cassette sold in recent months, there were at least 12 rentals. So it plans to offer over 150 films, including Superman II, The Shining, and Excalibur, for rental. WHV will license the cassettes to the rental outlets (including supermarkets, drug chains, and other mass merchandisers).

**AN EXPERIMENTAL RADIO NET** whose mission will be to disseminate information on "Trends in the Communications and Electronics Industry" will be sponsored by the Long Island, NY, chapter of IEEE and the Long Island Mobile Amateur Radio Club (LIMARC). Topics will include direct broadcast satellites, satellite earth stations, cable TV, electronic warfare and counter-measures, spread spectrum communication, and more. The net will operate at a frequency of 147.375 MHz, and reception should be possible throughout most of Long Island and southern Connecticut. Target date for start-up is Wednesday, Nov. 11, at 8:30 p.m. Thereafter, the net will operate on the second Wednesday of each month. For more information contact Ed Piller at 516-349-2484.

**PE IS A CONVERSATION STARTER**, according to a story in the New York Times. One young lady carried a copy of the magazine into a singles bar with her and soon became the center of male attention, said the story. It did not give any details on what happened thereafter nor did it attempt to draw a moral therefrom.

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